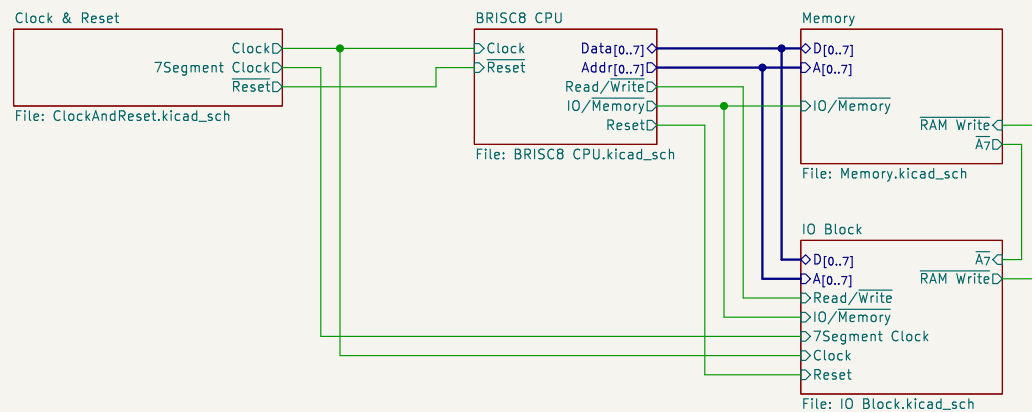


Simple 8-bit RISC CPU designed by Brian Duffy to explore the lowest level of computing. Compatible with the BRISC8 assembler and compiler suite.

Brian Duffy

Sheet: /

File: BRISC8.kicad_sch

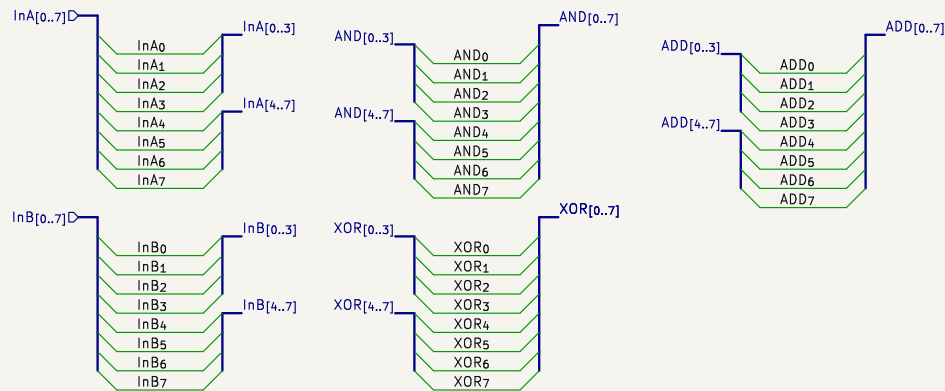
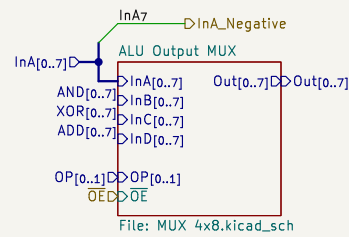
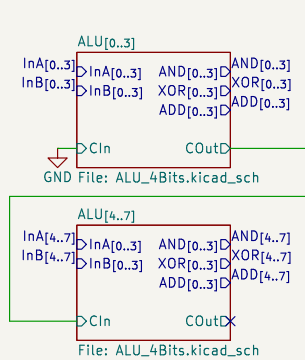
Title: BRISC8 CPU

Size: USLetter Date: 2025-06-26

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Id: 1/20



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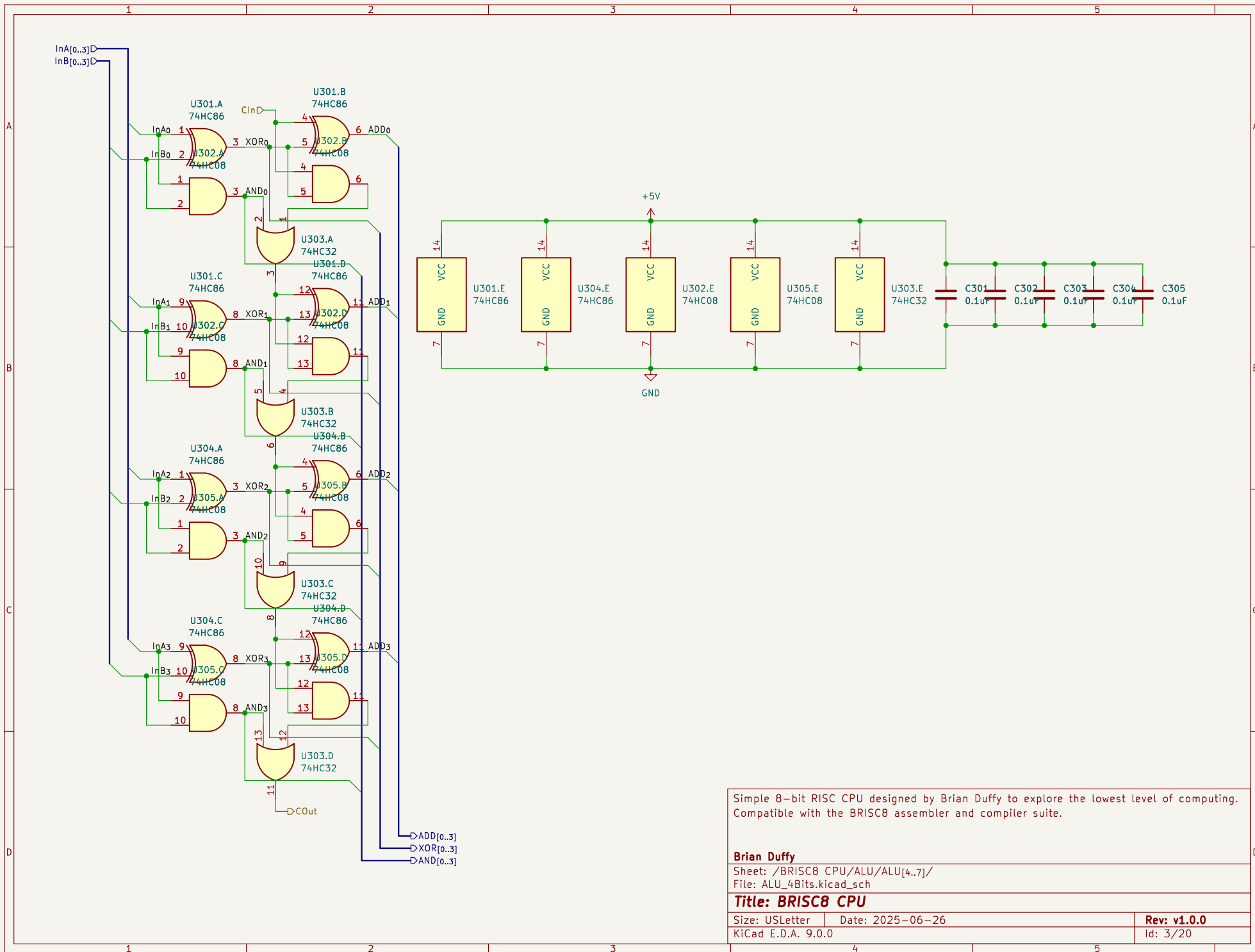
Brian Duffy

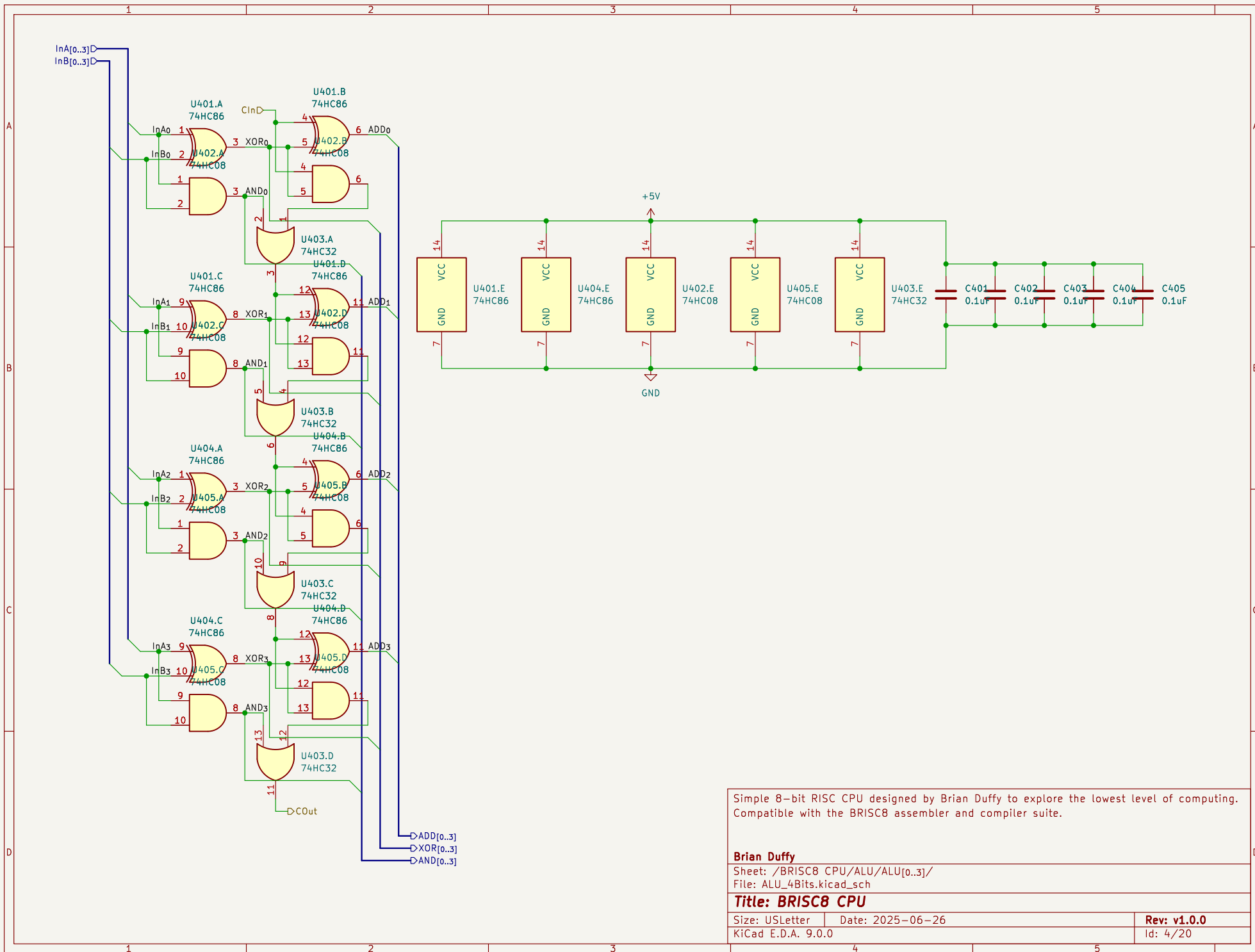
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File: ALU.kicad_sch

Title: BRISC8 CPU

Size: USLetter Date: 2025-06-26
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Id: 2/20





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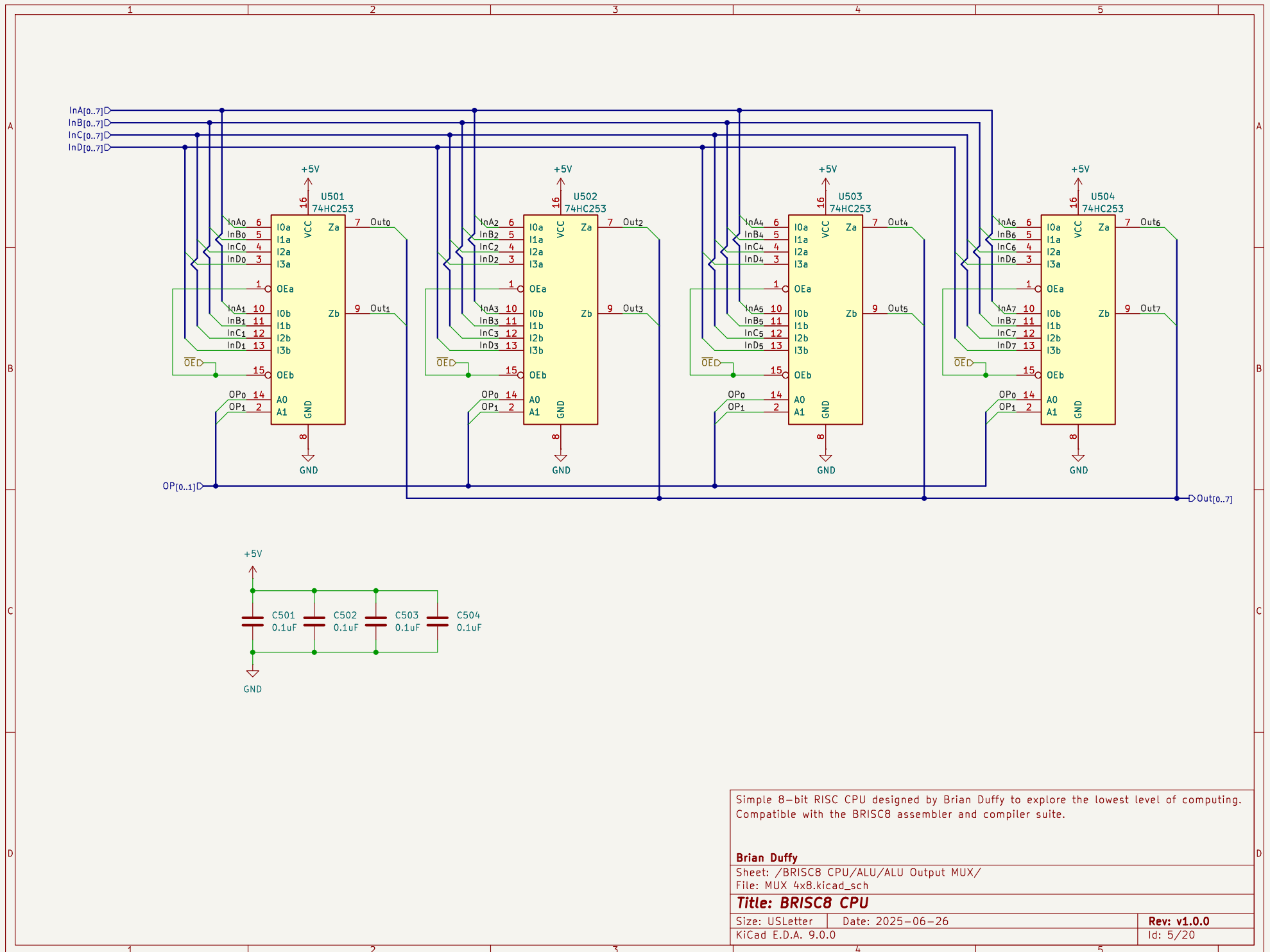
Brian Duffy

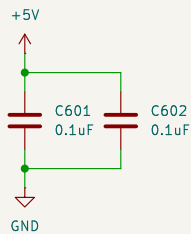
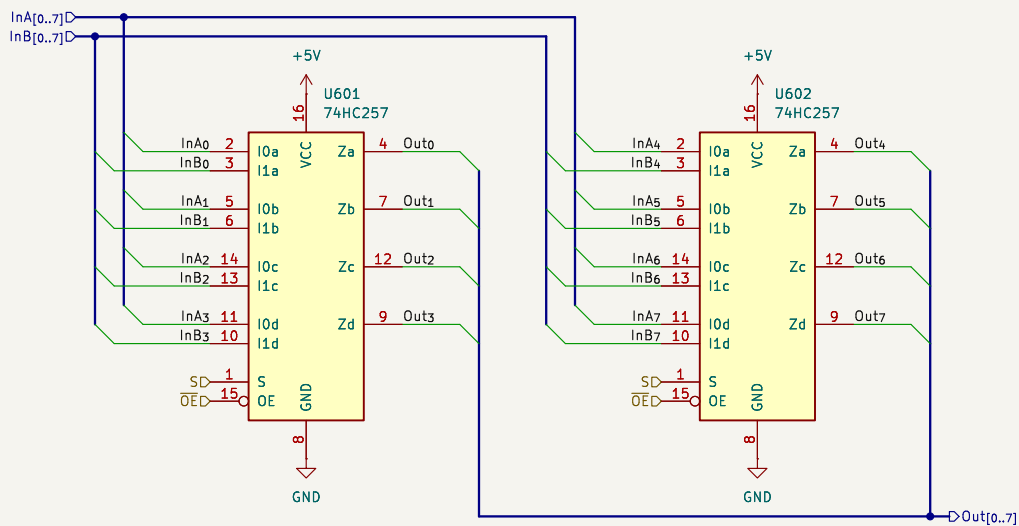
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Title: BRISC8 CPU

Size: USLetter Date: 2025-06-26
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Id: 4/20





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Compatible with the BRISC8 assembler and compiler suite.

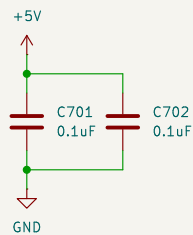
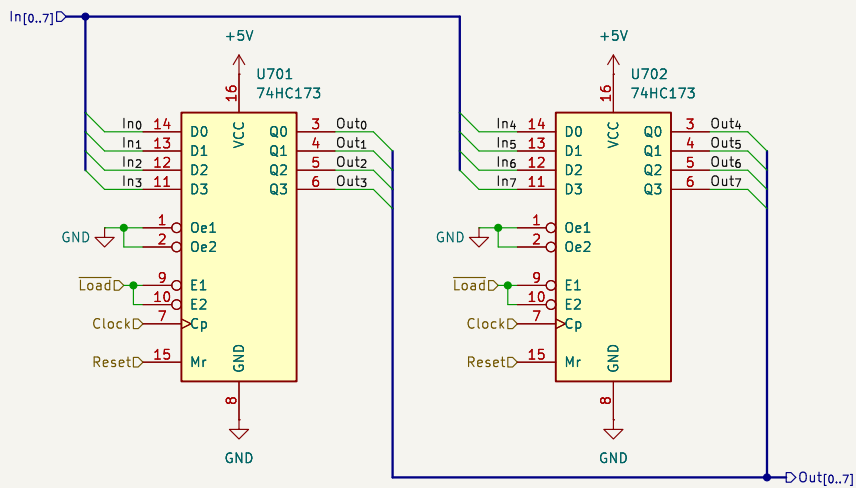
Brian Duffy

Sheet: /BRISC8 CPU/Address MUX/
File: MUX 2x8.kicad_sch

Title: BRISC8 CPU

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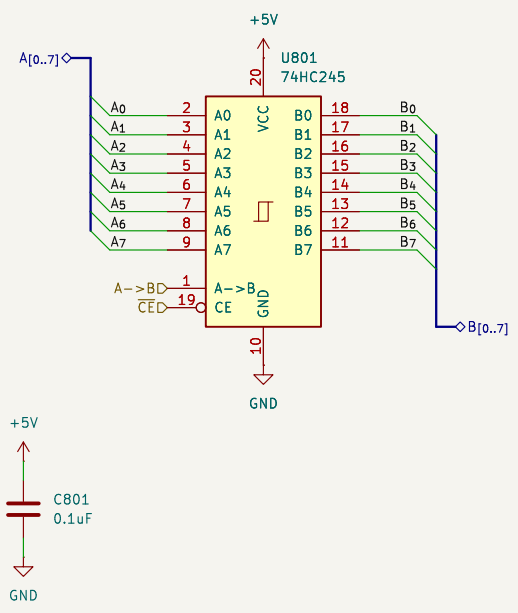
Brian Duffy

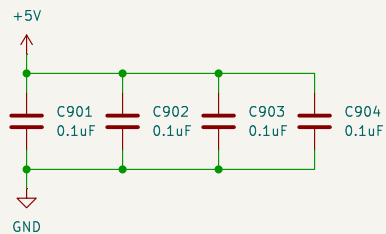
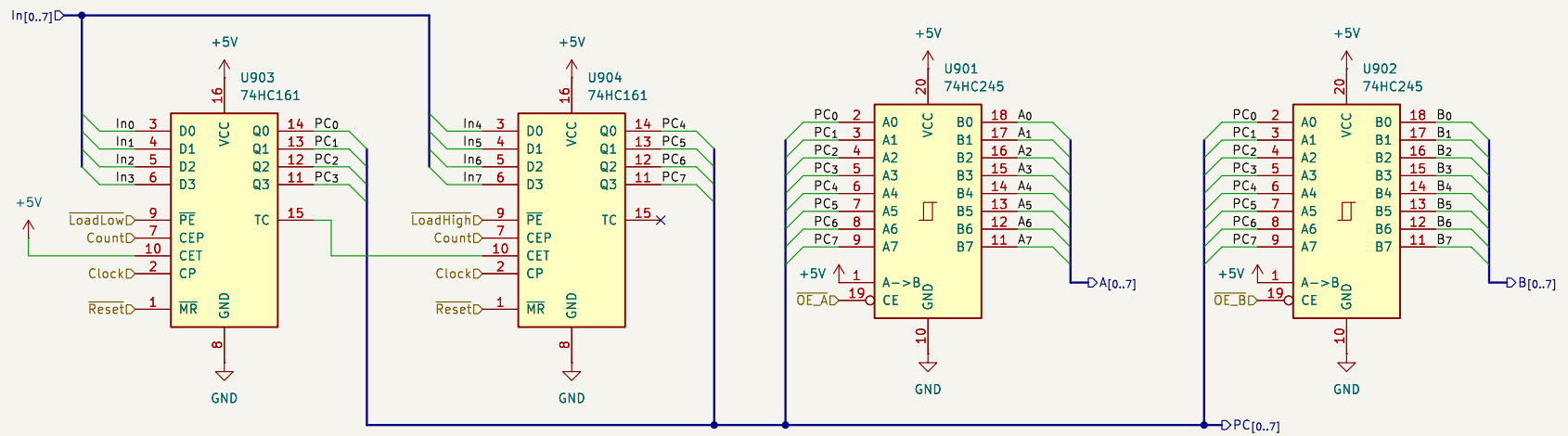
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Title: BRISC8 CPU

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Id: 7/20





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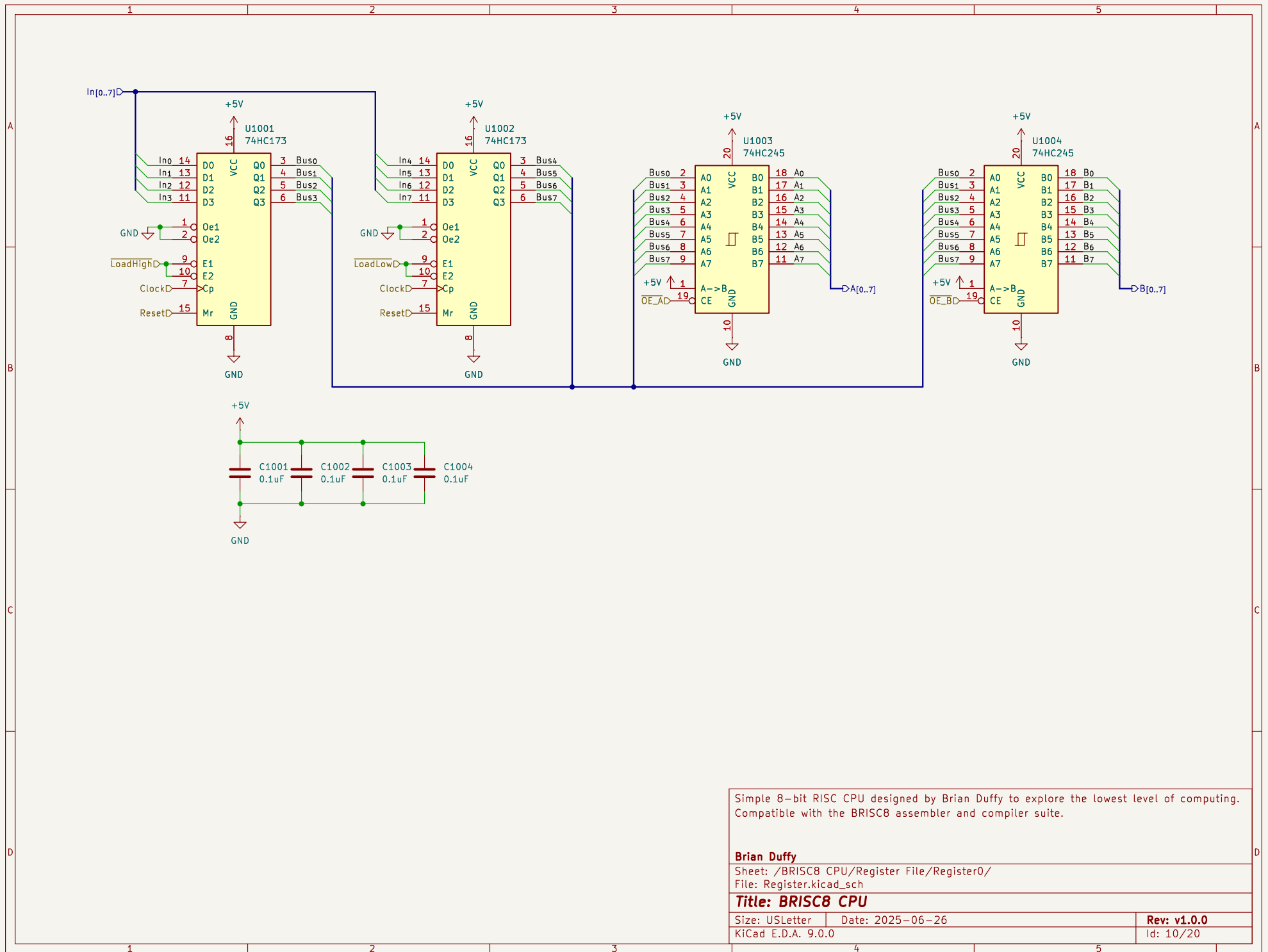
Brian Duffy

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File: ProgramCounter.kicad_sch

Title: BRISC8 CPU

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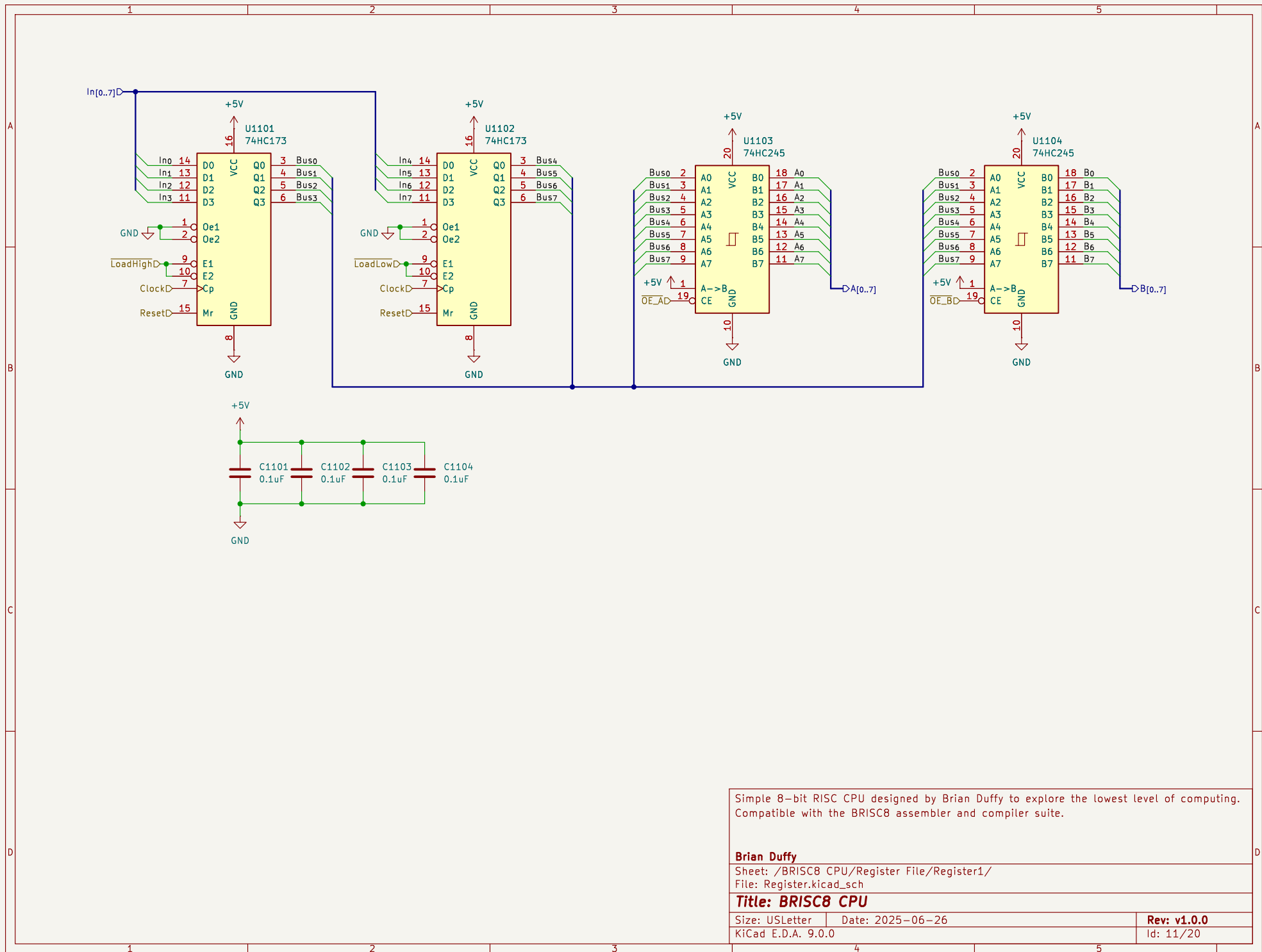
Brian Duffy

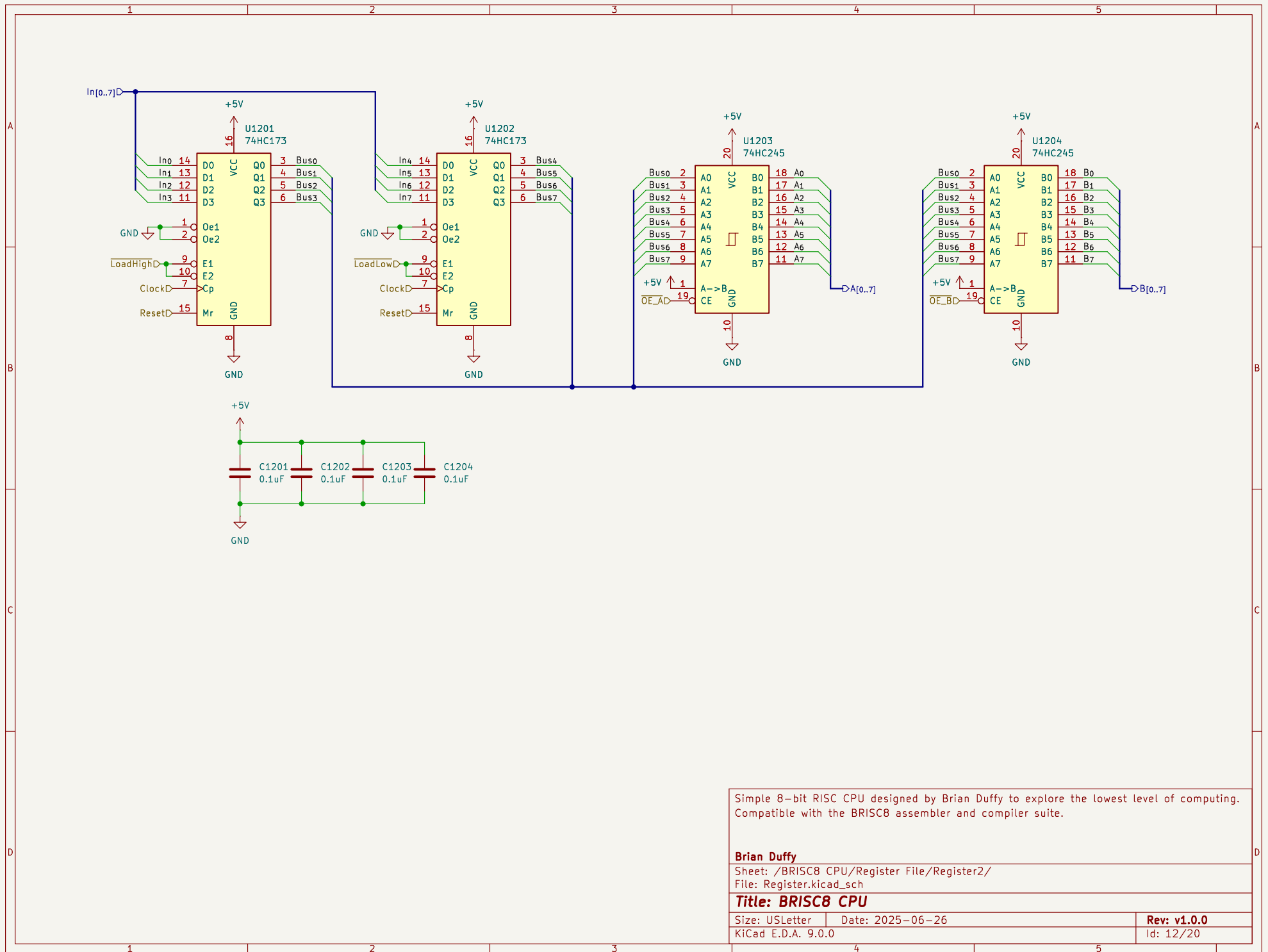
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File: Register.kicad_sch

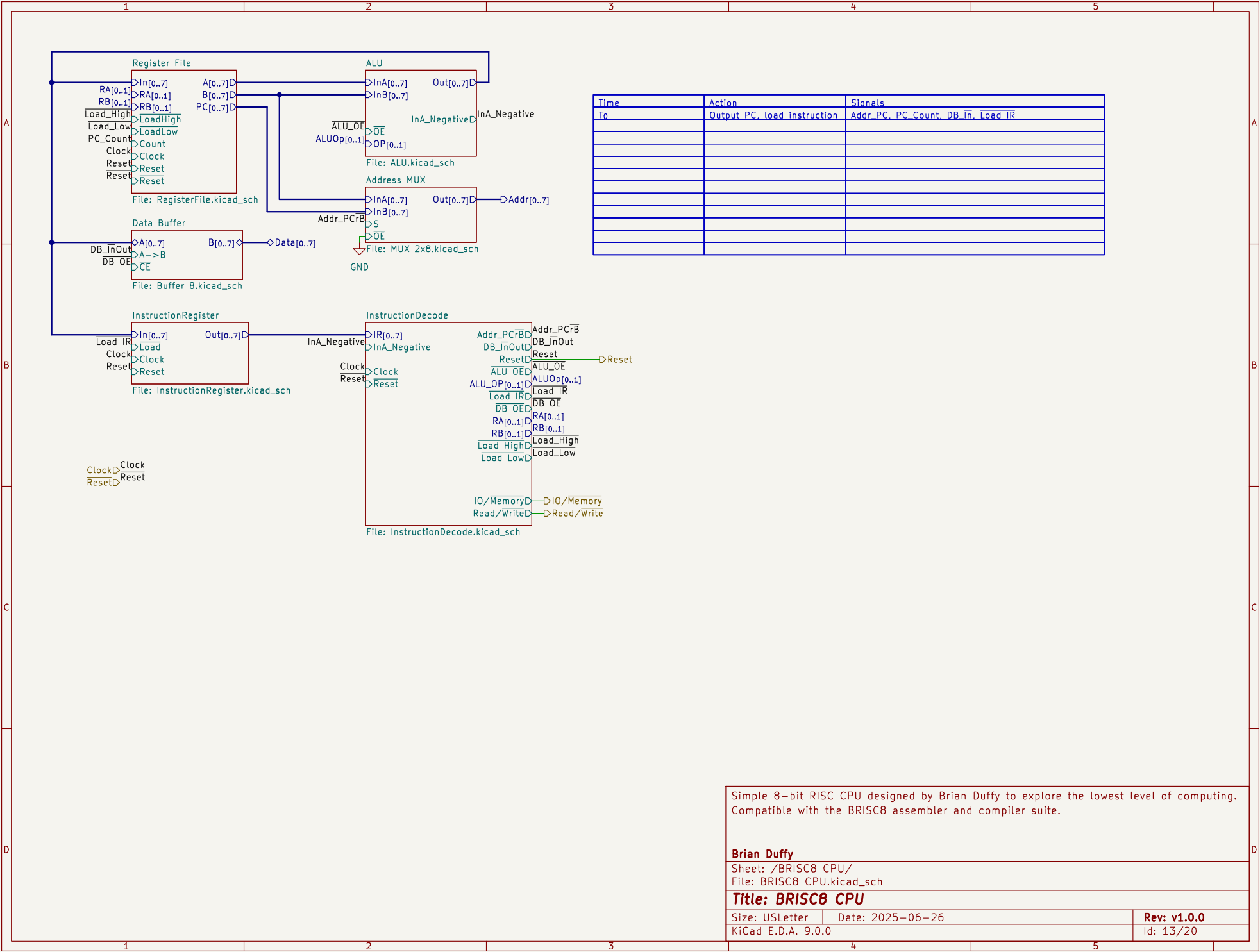
Title: BRISC8 CPU

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Rev: v1.0.0
Id: 10/20







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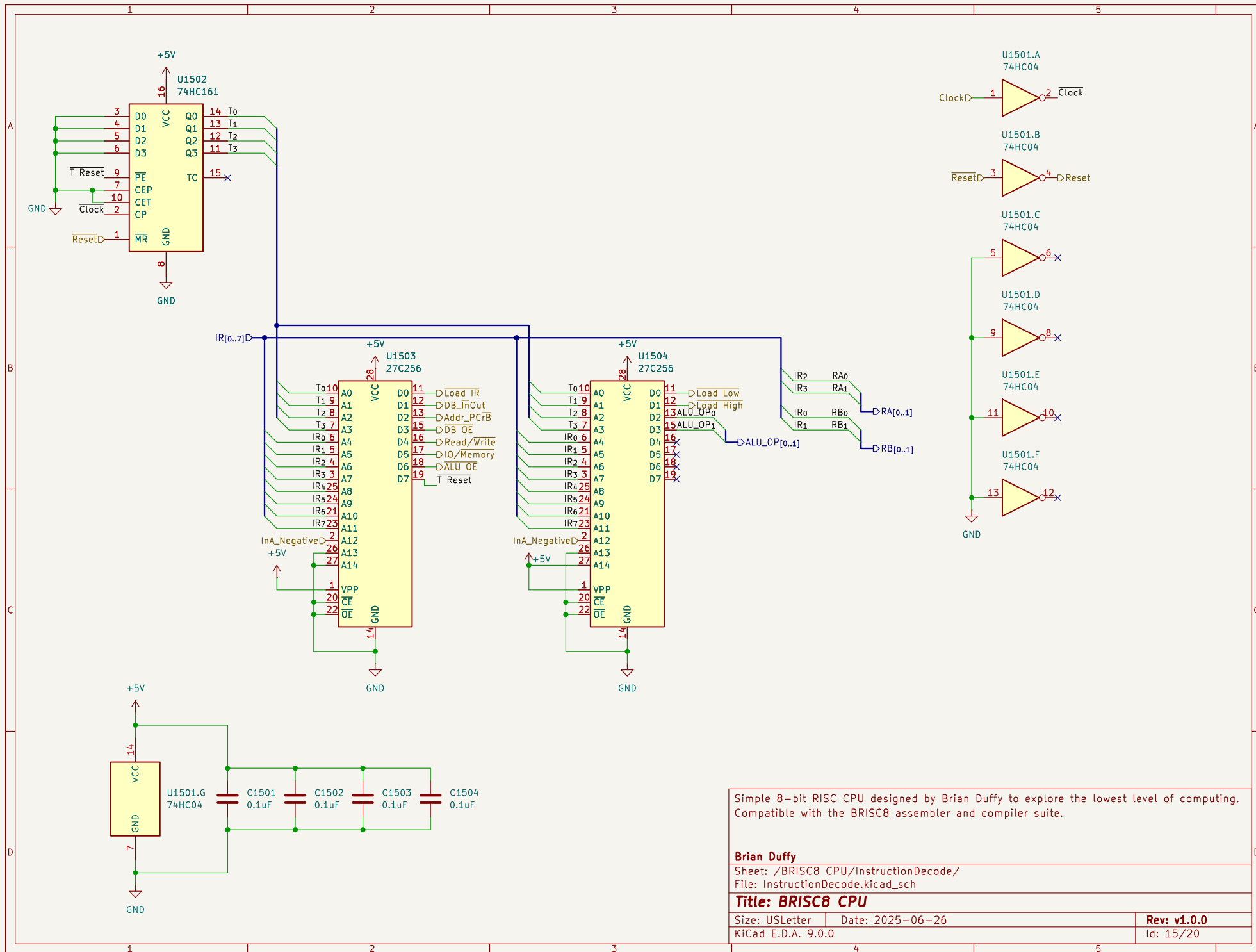
Brian Duffy

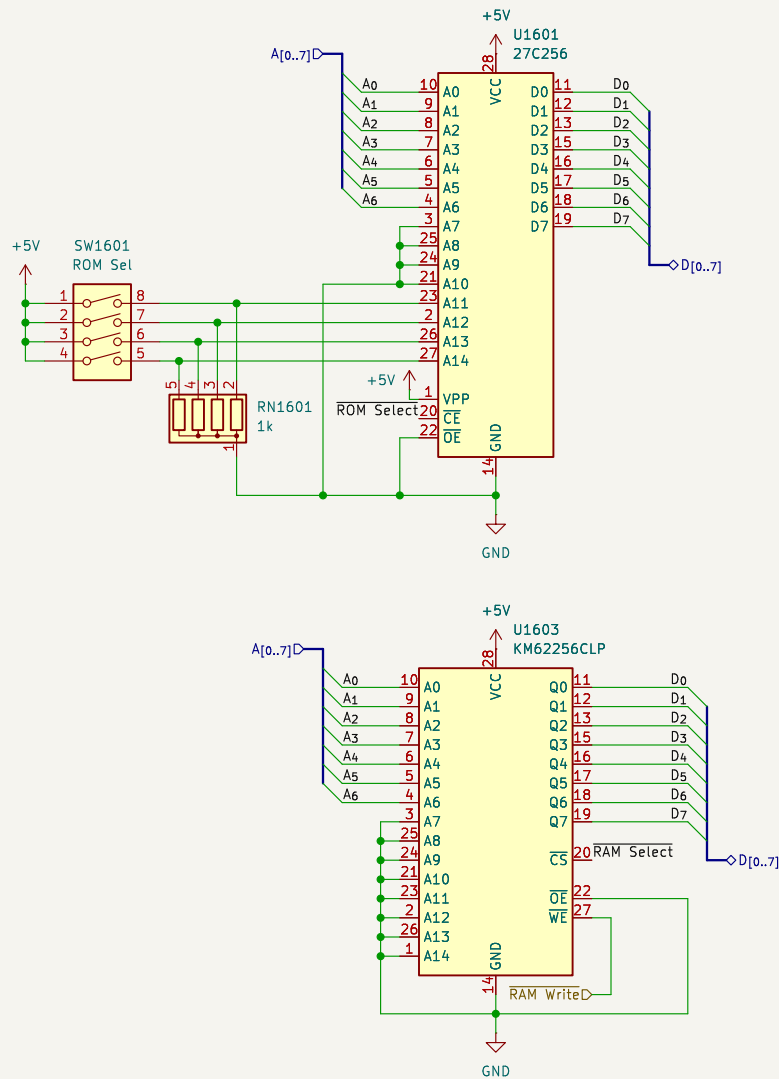
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File: BRISC8 CPU.kicad_sch

Title: BRISC8 CPU

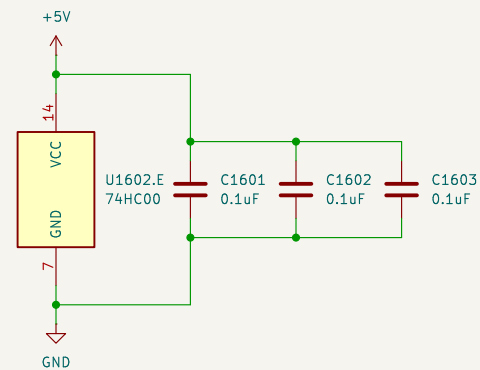
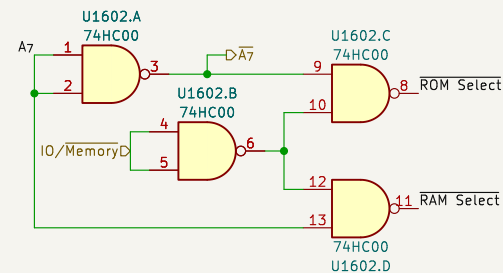
Size: USLetter Date: 2025-06-26
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InA	InB	NAND	NOR
0	0	1	1
0	1	1	0
1	0	1	0
1	1	0	0



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Brian Duffy

Sheet: /Memory/

File: Memory.kicad_sch

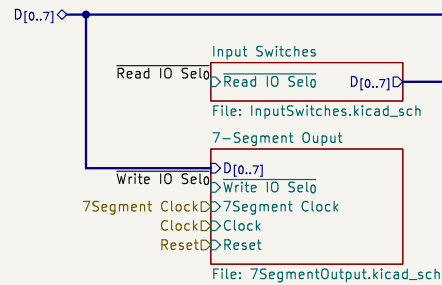
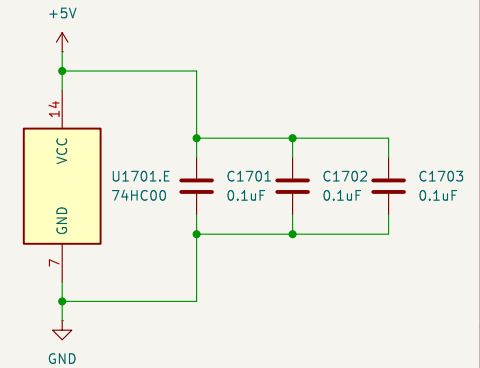
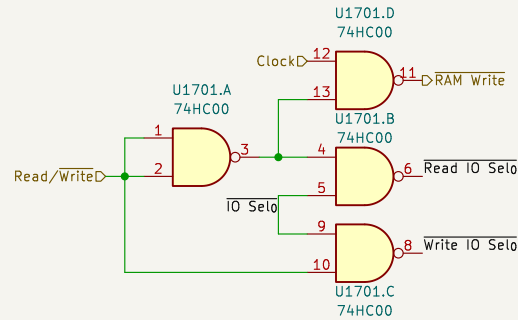
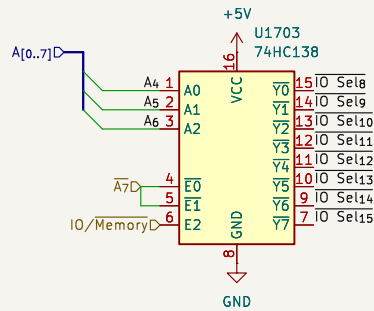
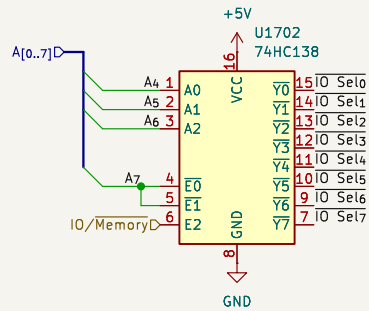
Title: BRISC8 CPU

Size: USLetter Date: 2025-06-26

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Brian Duffy

Sheet: /IO Block/

File: IO Block.kicad_sch

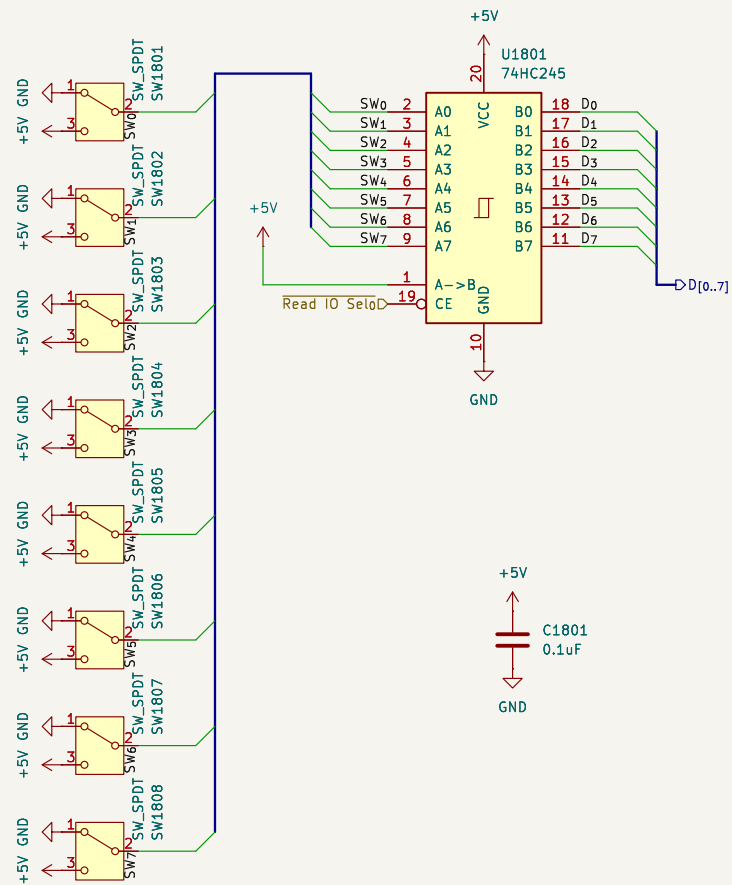
Title: BRISC8 CPU

Size: USLetter Date: 2025-06-26

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Rev: v1.0.0

Id: 17/20



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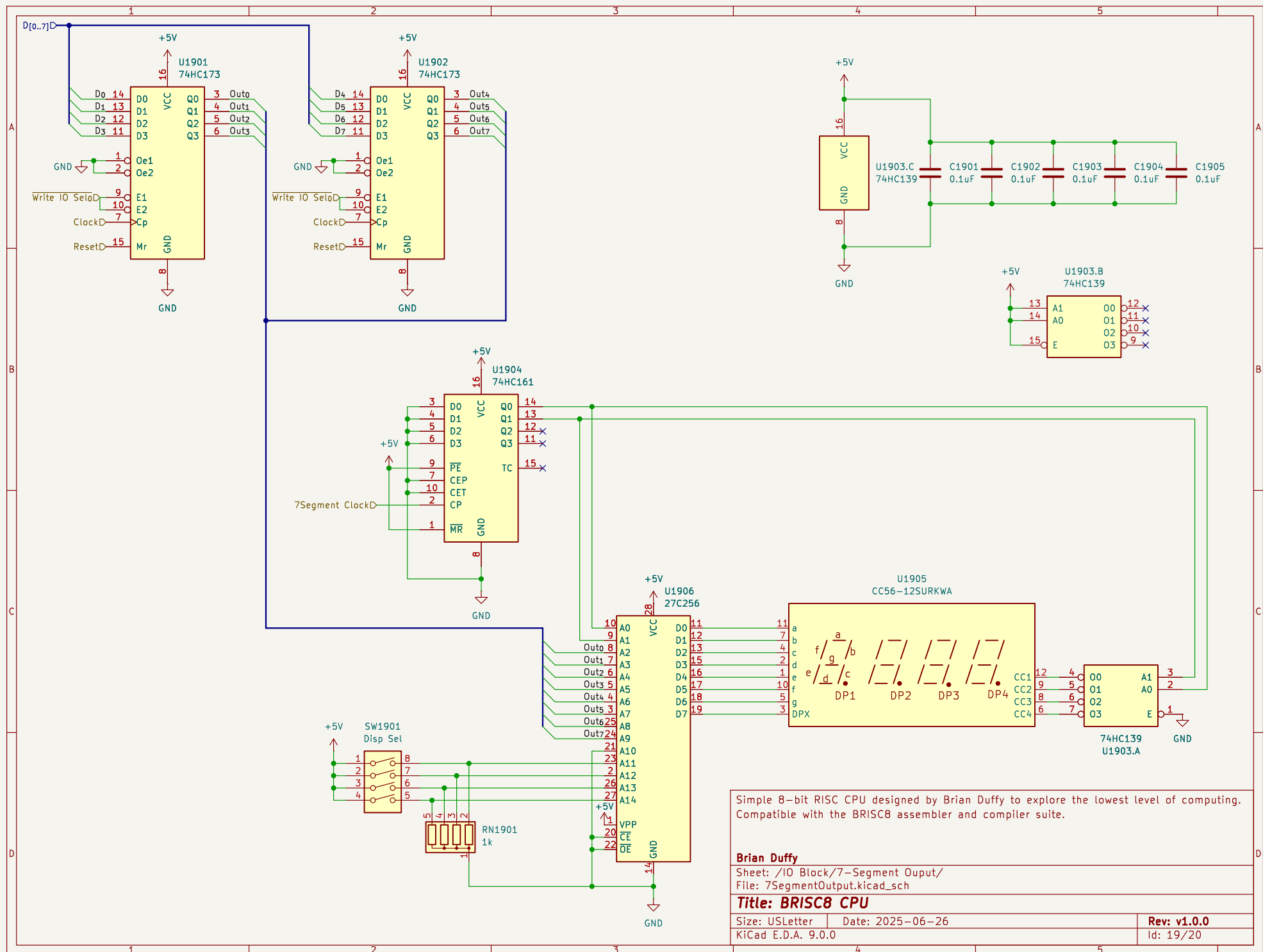
Brian Duffy

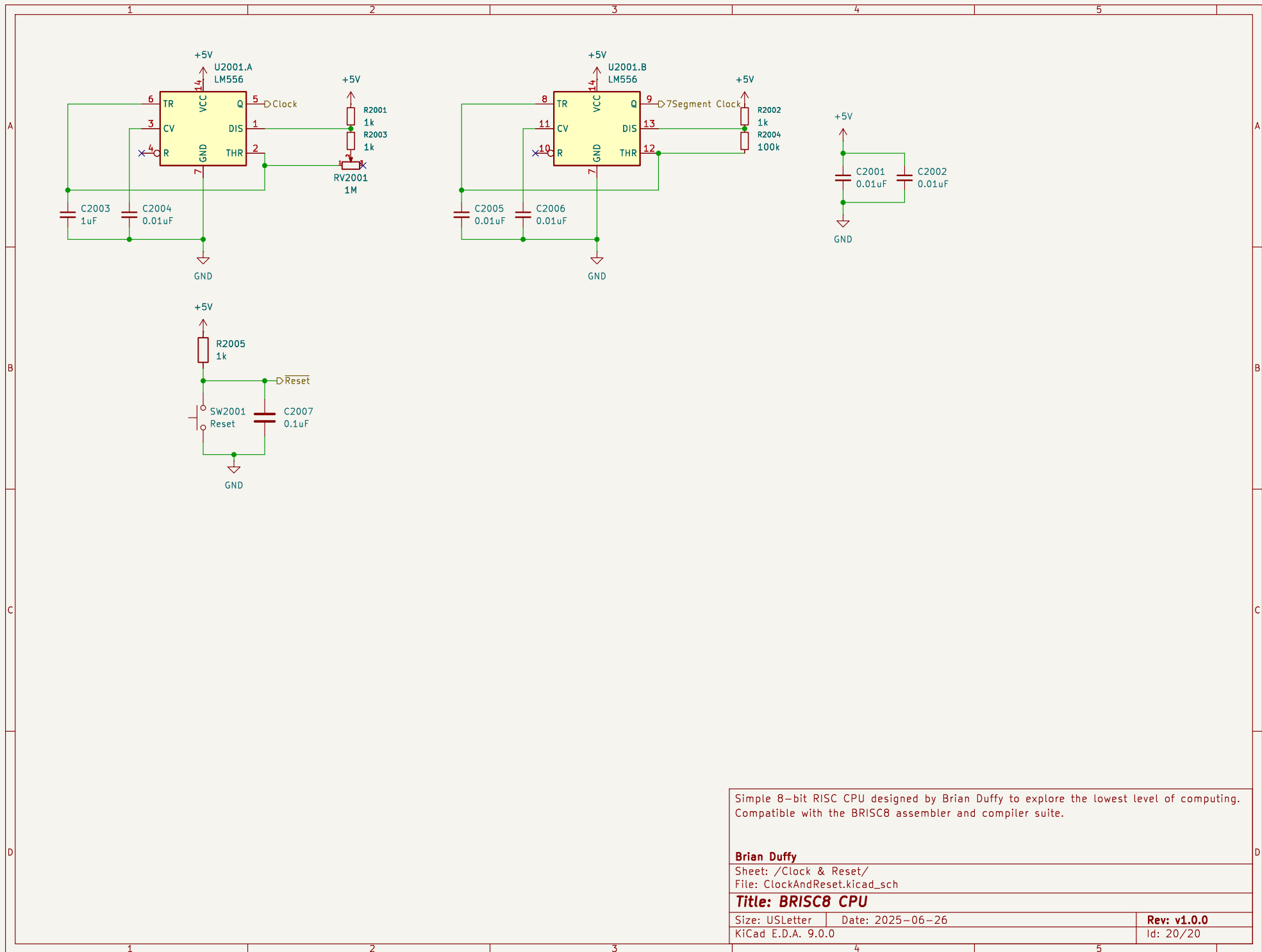
Sheet: /IO Block/Input Switches/
File: InputSwitches.kicad_sch

Title: BRISC8 CPU

Size: USLetter Date: 2025-06-26
KiCad E.D.A. 9.0.0

Rev: v1.0.0
Id: 18/20





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Brian Duffy

Sheet: /Clock & Reset/

File: ClockAndReset.kicad_sch

Title: BRISC8 CPU

Size: USLetter Date: 2025-06-26

KiCad E.D.A. 9.0.0

Rev: v1.0.0

Id: 20/20