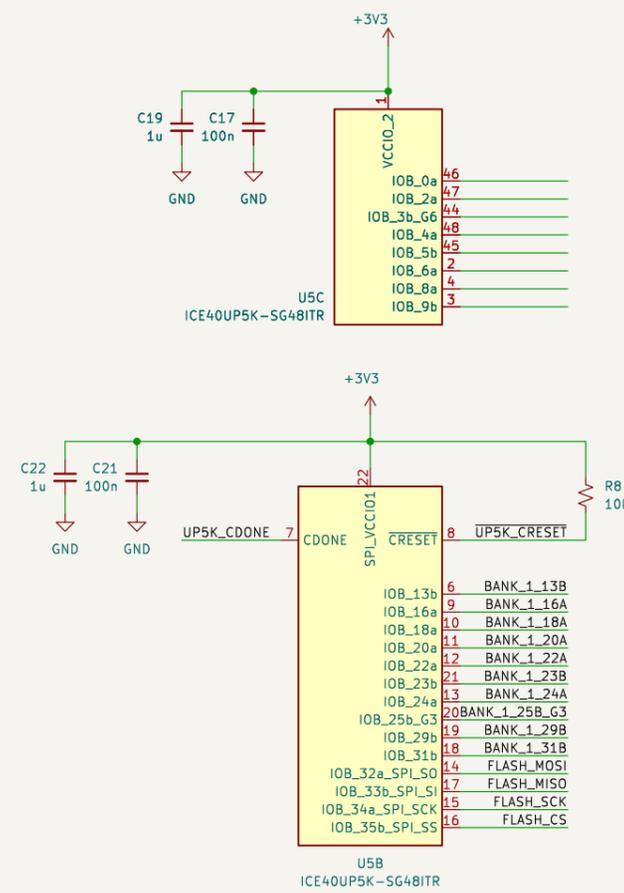
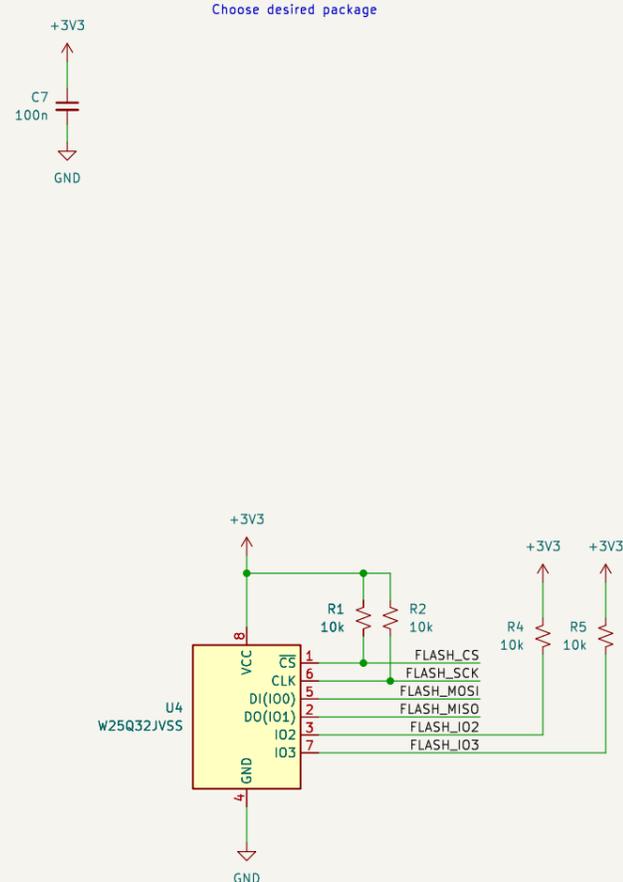
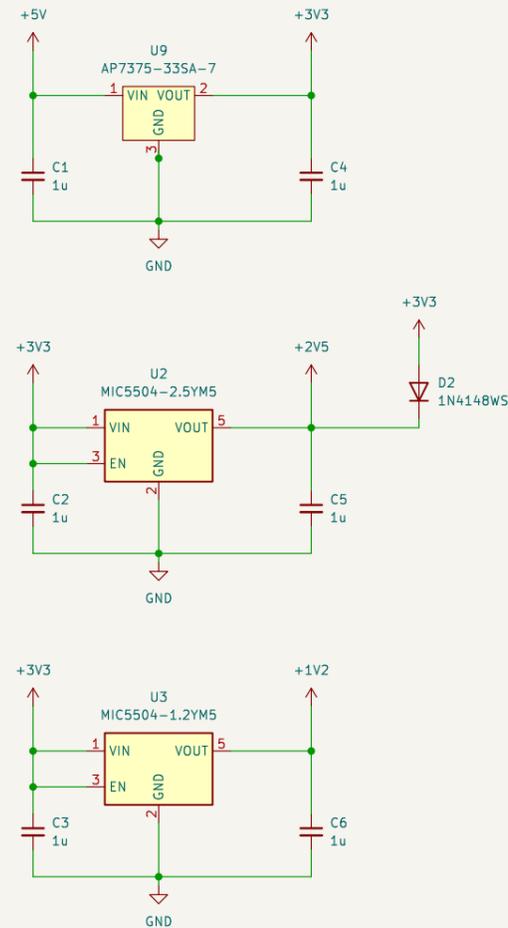


PGLogic Programmer Interface

SPI Flash

FPGA Pinout

LDOs



FPGA Core Power

