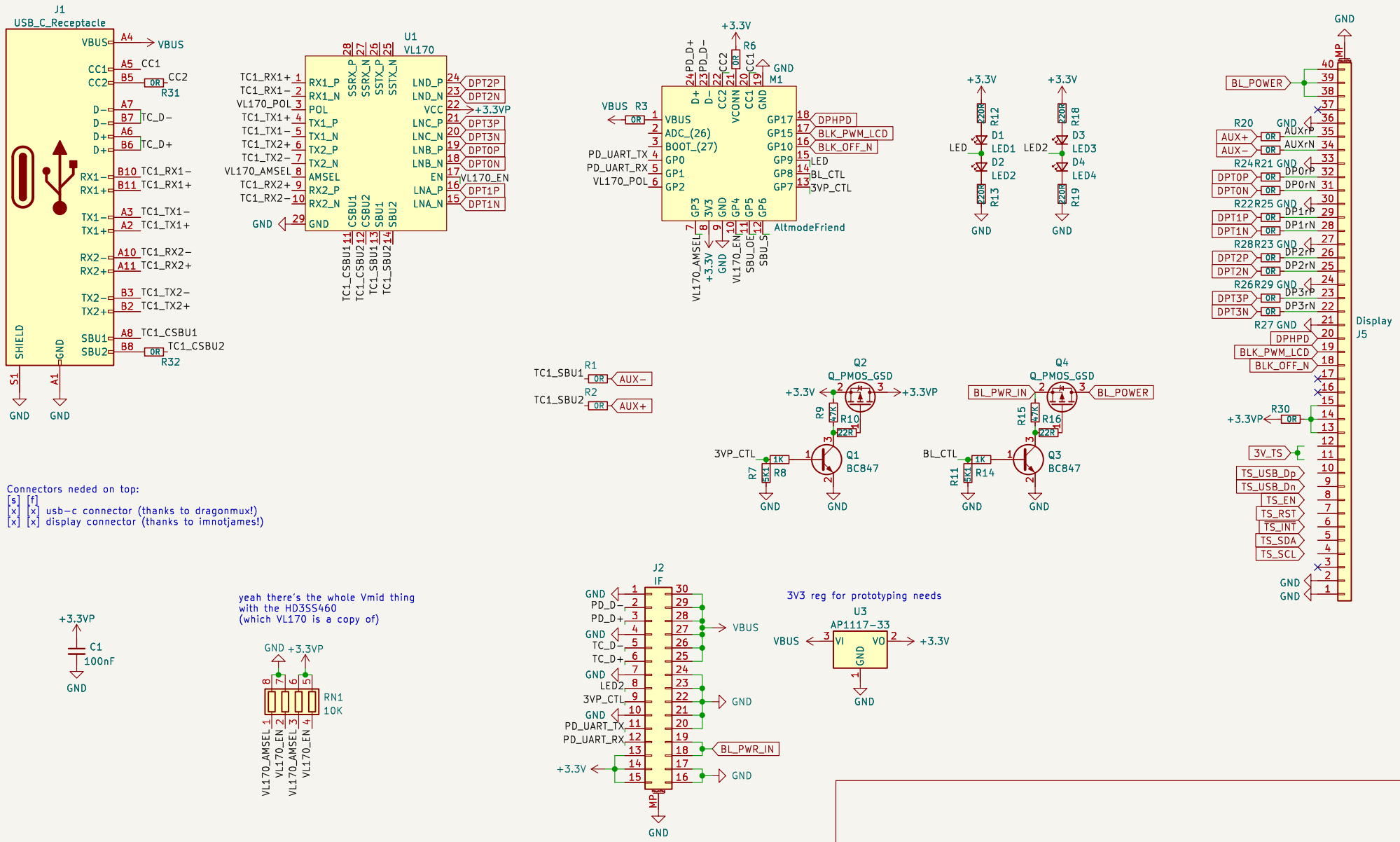
J1
USB_C_Receptacle
VBUS
CC1
CC2
D−
D+
RX1−
RX1+
TX1−
TX1+
RX2−
RX2+
TX2−
TX2+
SBU1
SBU2
SHIELD
GND
TC_D−
TC_D+
TC1_RX1−
TC1_RX1+
TC1_TX1−
TC1_TX1+
TC1_RX2−
TC1_RX2+
TC1_TX2−
TC1_TX2+
TC1_CSBU1
TC1_CSBU2
R31
R32
S1
A1
U1
VL170
RX1_P
RX1_N
POL
TX1_P
TX1_N
TX2_P
TX2_N
AMSEL
RX2_P
RX2_N
SSRX_P
SSRX_N
SSTX_P
SSTX_N
CSBU1
CSBU2
SBU1
SBU2
LND_P
LND_N
VCC
LNC_P
LNC_N
LNB_P
LNB_N
EN
LNA_P
LNA_N
VL170_POL
VL170_AMSEL
VL170_EN
DPT2P
DPT2N
+3.3VP
DPT3P
DPT3N
DPT0P
DPT0N
DPT1P
DPT1N
+3.3V
R6
M1
VBUS R3
ADC_(26)
BOOT_(27)
GP0
GP1
GP2
GP3
3V3
GP4
GP5
GP6
GP7
GP8
GP9
GP10
GP15
GP17
VCONN
PD_UART_TX
PD_UART_RX
PD_D+
PD_D−
DPHPD
BLK_PWM_LCD
BLK_OFF_N
LED
BL_CTL
3VP_CTL
VL170_AMSEL
VL170_EN
SBU_OE
SBU_S
AltmodeFriend
D1
LED1
D2
LED2
D3
LED3
D4
LED4
R12
R13
R18
R19
220R
BL_POWER
AUX+
AUX−
AUXrP
AUXrN
DP0rP
DP0rN
DP1rP
DP1rN
DP2rP
DP2rN
DP3rP
DP3rN
R20
R21
R22
R23
R24
R25
R26
R27
R28
R29
R30
3V_TS
TS_USB_Dp
TS_USB_Dn
TS_EN
TS_RST
TS_INT
TS_SDA
TS_SCL
Display
J5
R1
R2
TC1_SBU1
TC1_SBU2
Q2
Q_PMOS_GSD
Q4
BL_PWR_IN
R9
R10
R15
R16
47K
22R
Q1
Q3
BC847
R7
R8
R11
R14
1K
5K1
Connectors neded on top:
[s] [f]
[x] [x] usb−c connector (thanks to dragonmux!)
[x] [x] display connector (thanks to imnotjames!)
C1
100nF
yeah there's the whole Vmid thing
with the HD3SS460
(which VL170 is a copy of)
RN1
10K
J2
IF
PD_D−
PD_D+
TC_D−
TC_D+
LED2
3VP_CTL
PD_UART_TX
PD_UART_RX
3V3 reg for prototyping needs
U3
AP1117−33
VI
VO
Sheet: /
File: framework_mobo_lefthalf.kicad_sch
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. 9.0.0
Id: 1/1