

Files at github.com/Gregification/BeeMS

UTA Racing

Sheet: /
 File: MasterBoard_A.kicad_sch

Title: BeeMS Master Board (A)

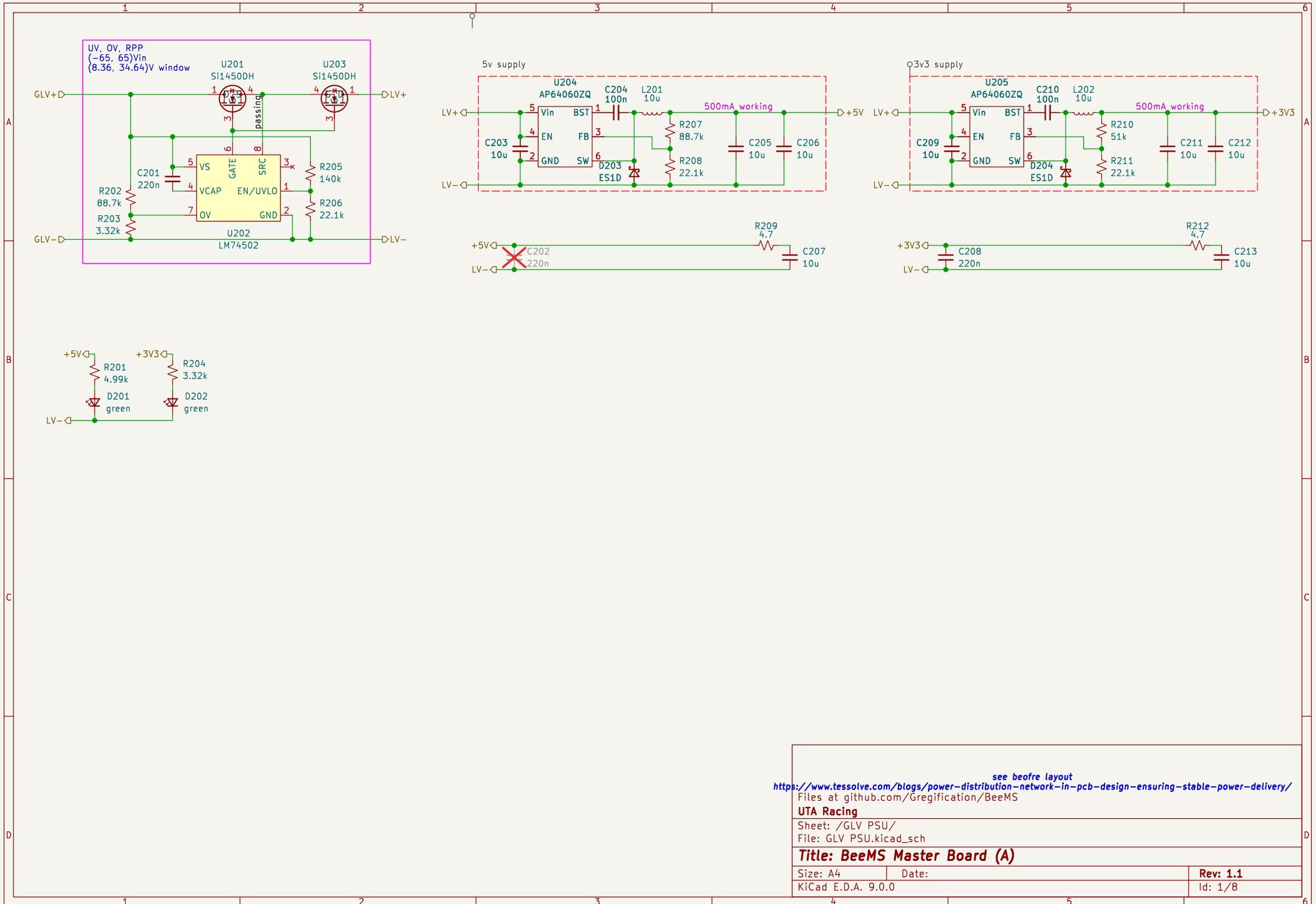
Size: A3 Date: 2026-01-08 Rev: 1.1
 KiCad E.D.A. 9.0.0 Id: 0/8

- LV only, no HV present
- (10.35)VDC GLV working
- swap relays for >12VDC operation
- IL assumed to share GLV GND
- IL sense diode set for -12VDC

TODO: add hardware watchdog

TODO: add random buzzword sensors.

- humidity
- Pressure
- hydrogen



see beofre layout
<https://www.tessolve.com/blogs/power-distribution-network-in-pcb-design-ensuring-stable-power-delivery/>
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UTA Racing

Sheet: /GLV PSU/
 File: GLV PSU.kicad_sch

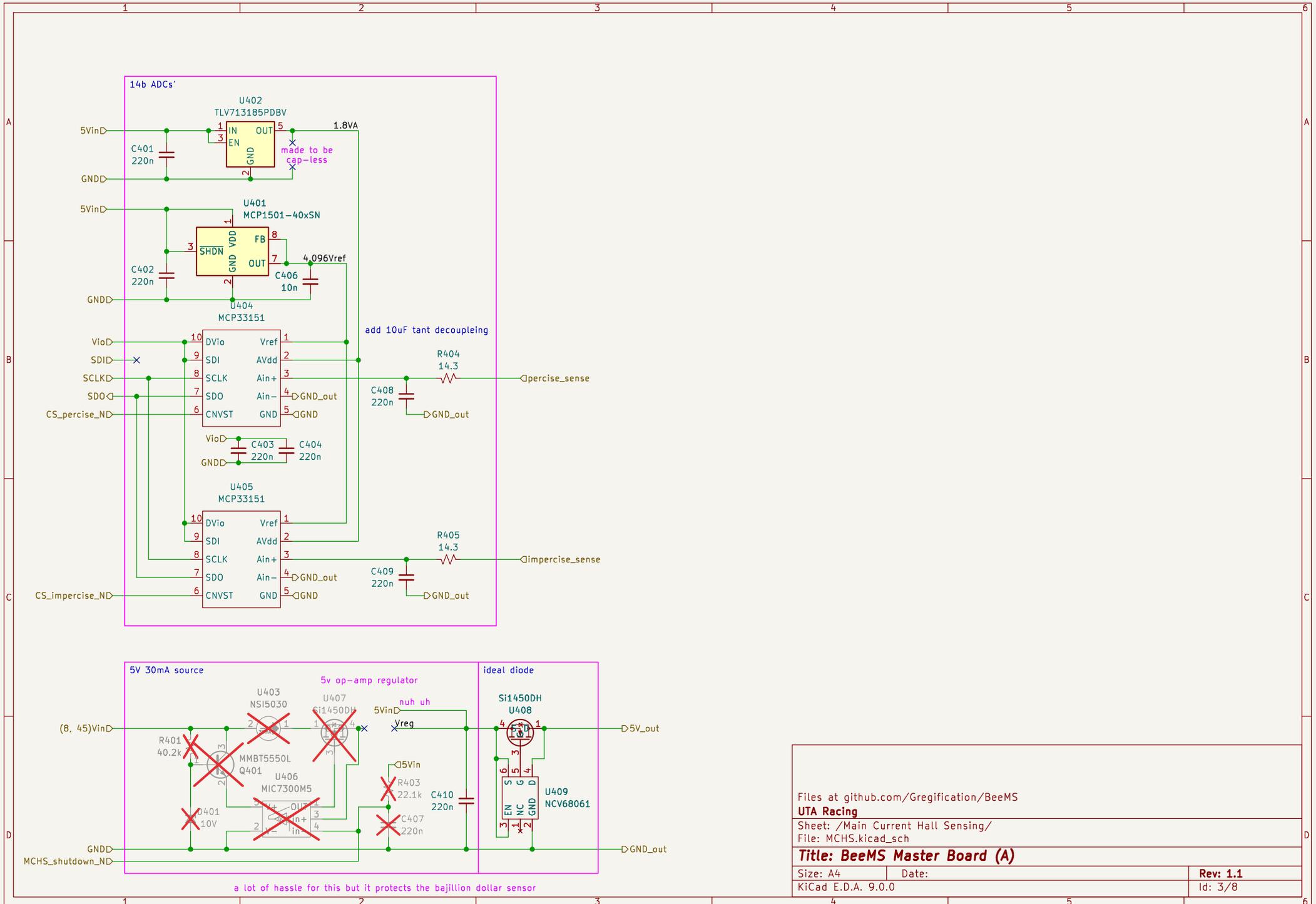
Title: BeeMS Master Board (A)

Size: A4
 KiCad E.D.A. 9.0.0

Date:

Rev: 1.1

Id: 1/8



Files at github.com/Gregification/BeeMS

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Sheet: /Main Current Hall Sensing/
File: MCHS.kicad_sch

Title: BeeMS Master Board (A)

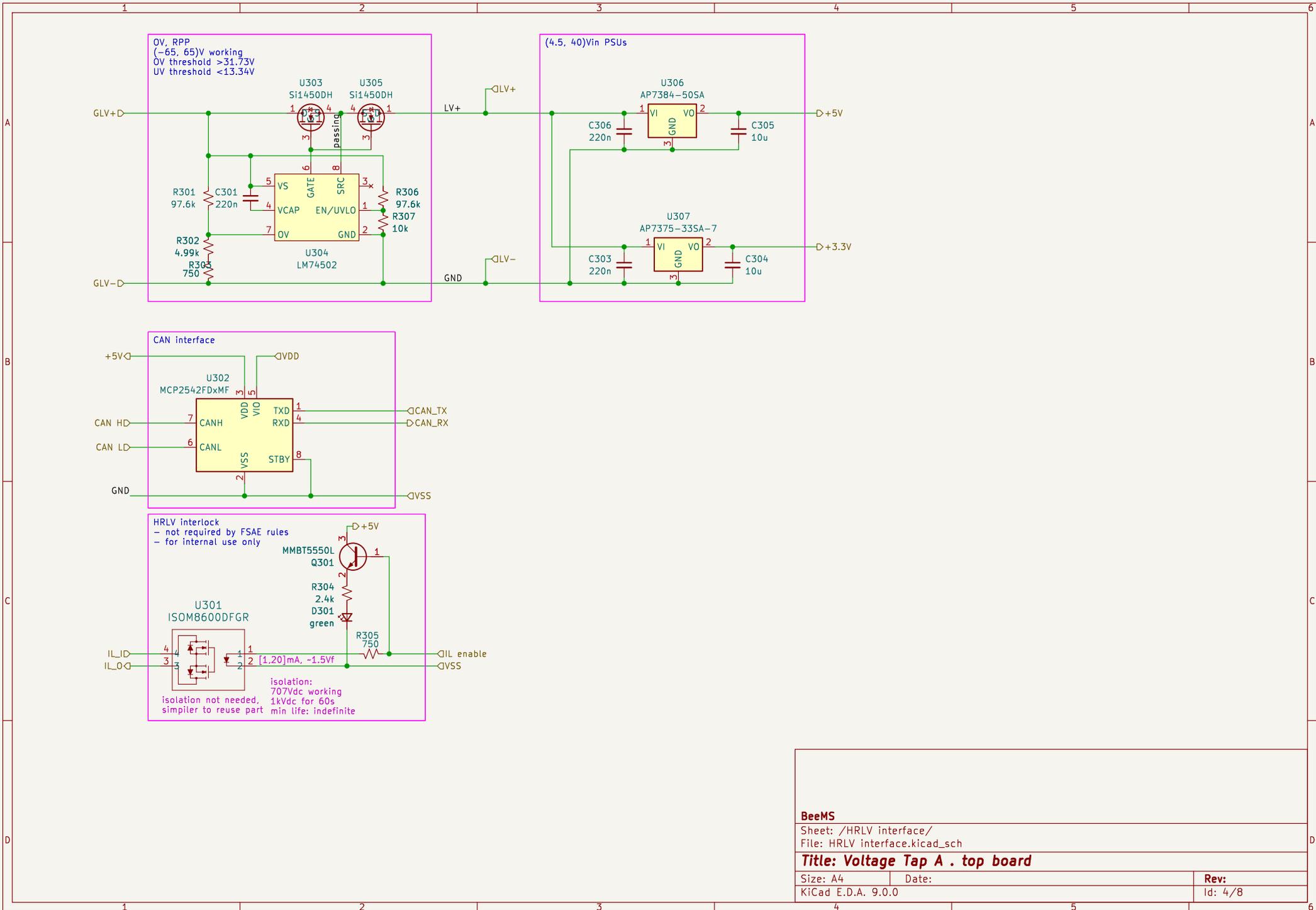
Size: A4

Date:

Rev: 1.1

KiCad E.D.A. 9.0.0

Id: 3/8



BeeMS

Sheet: /HRLV interface/
 File: HRLV interface.kicad_sch

Title: Voltage Tap A . top board

Size: A4

Date:

Rev:

KiCad E.D.A. 9.0.0

Id: 4/8

(0.37)V across LV+- , working

LV+□

LV-□

MCU_FAULT□

MCU_IL_enable□

Interlock_ctrl□

TODO
(nuh uh)

PSU_FAULT□

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UTA Racing

Sheet: /Analog Fault Failsafe/

File: Analog Fault Failsafe.kicad_sch

Title: BeeMS Master Board (A)

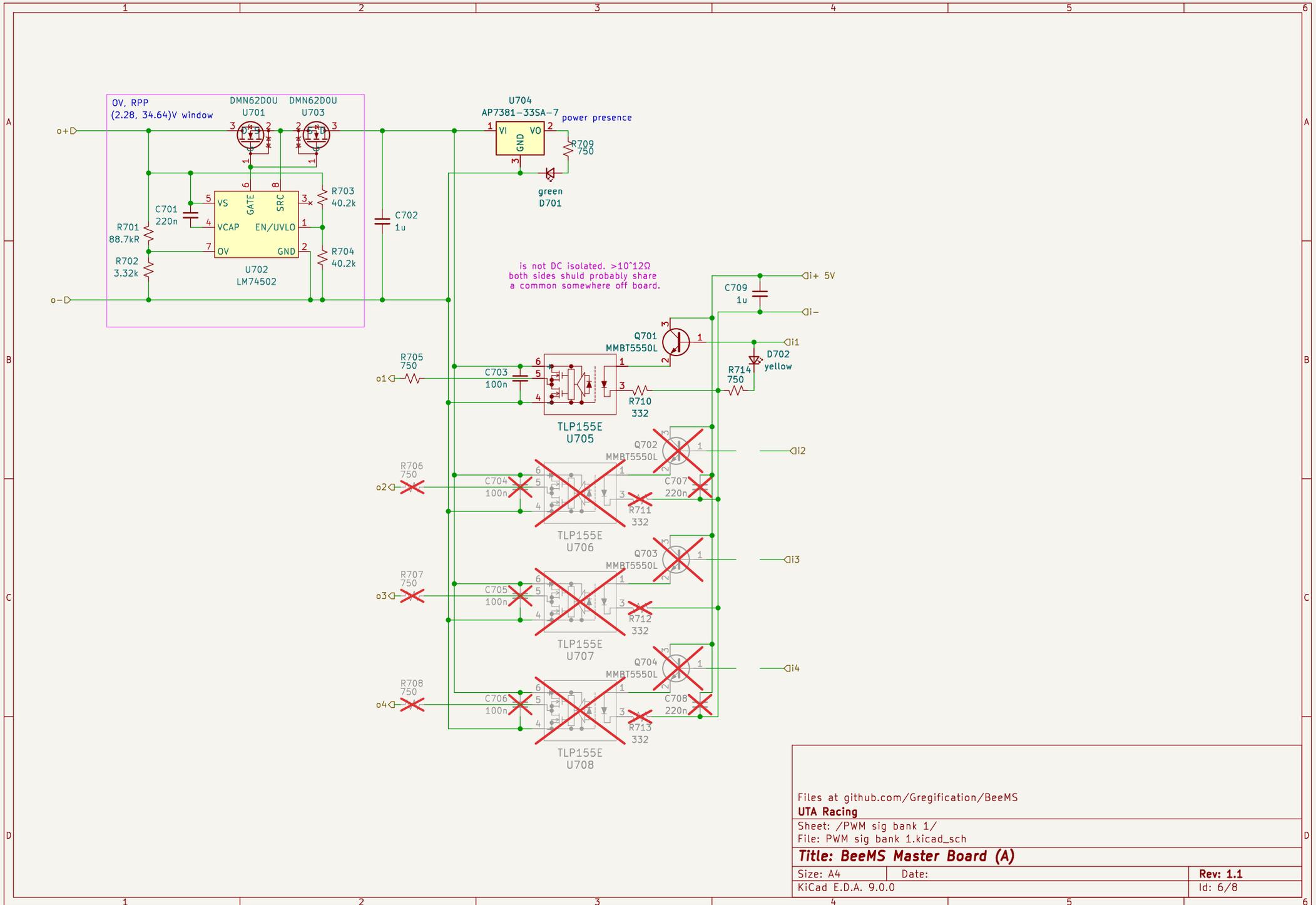
Size: A4

Date:

Rev: 1.1

KiCad E.D.A. 9.0.0

Id: 5/8



	1	2	3	4	5	6
A						A
B						B
C						C
D						D
	1	2	3	4	5	6



Sheet: /common designs/
File: common designs.kicad_sch

Title:		Rev:
Size: A4	Date:	Id: 999/8
KiCad E.D.A. 9.0.0		