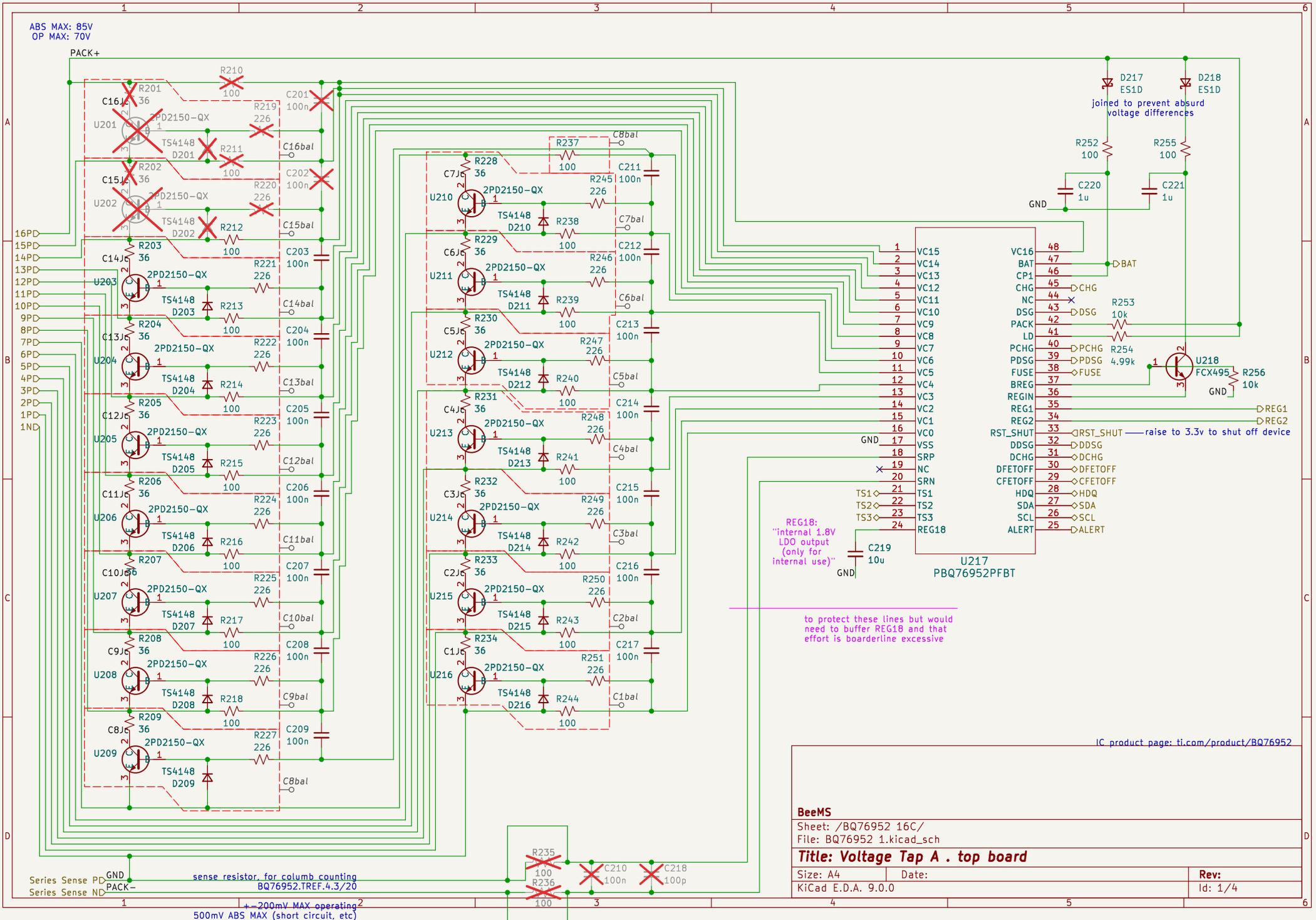


BeeMS
 Sheet: /
 File: VoltageTap_A.kicad_sch

Title: Voltage Tap A . top board

Size: A4	Date:
KiCad E.D.A. 9.0.0	Rev: 1.0
	Id: 0/4



ABS MAX: 85V
OP MAX: 70V

joined to prevent absurd voltage differences

REG18:
"internal 1.8V LDO output (only for internal use)"

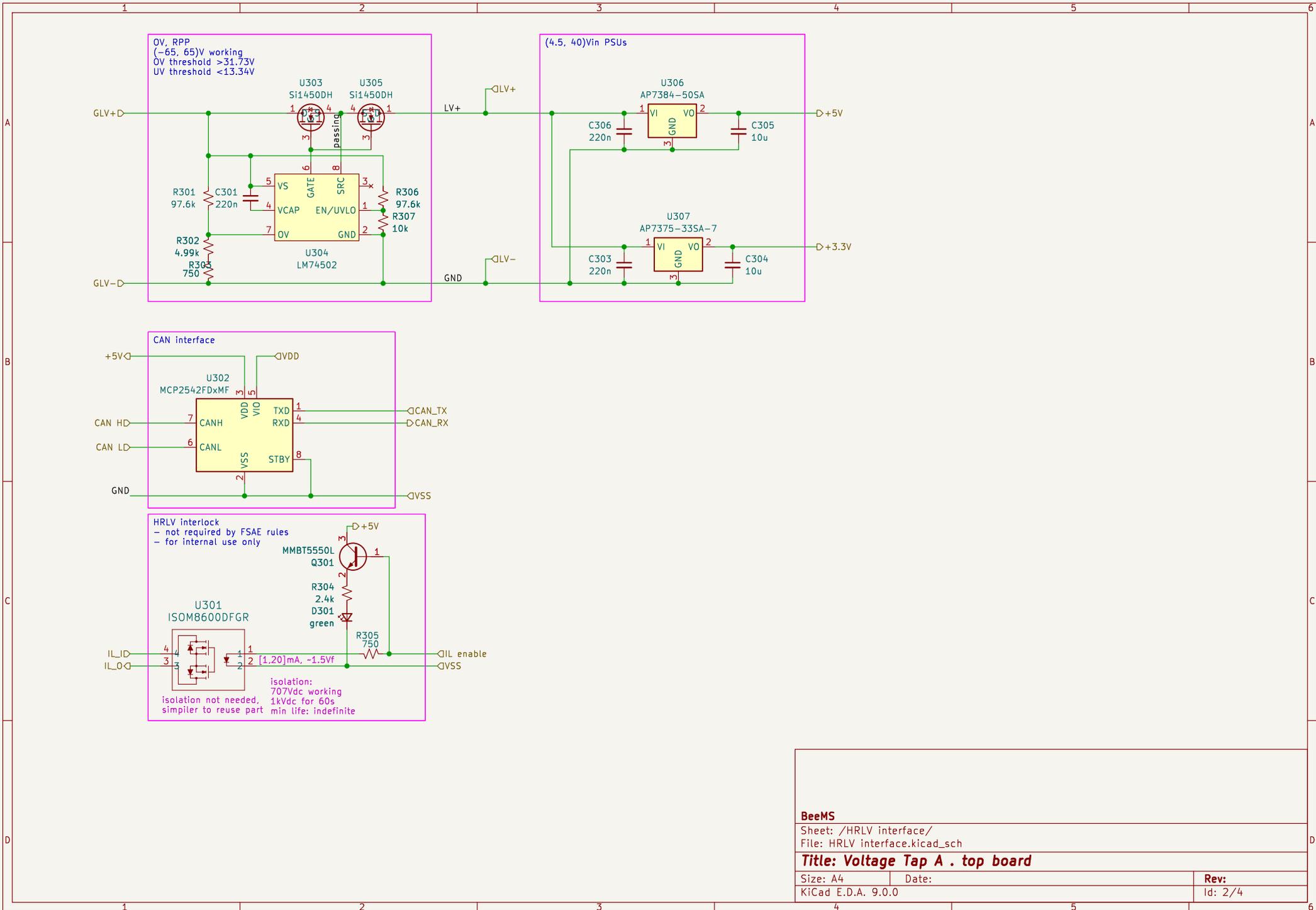
to protect these lines but would need to buffer REG18 and that effort is borderline excessive

IC product page: ti.com/product/BQ76952

BeeMS	
Sheet: /BQ76952 16C/	
File: BQ76952 1.kicad_sch	
Title: Voltage Tap A . top board	
Size: A4	Date:
KiCad E.D.A. 9.0.0	Rev: Id: 1/4

200mV MAX operating
500mV ABS MAX (short circuit, etc)

bit ballancing



BeeMS	
Sheet: /HRLV interface/ File: HRLV interface.kicad_sch	
Title: Voltage Tap A . top board	
Size: A4	Date:
KiCad E.D.A. 9.0.0	Rev: Id: 2/4

	1	2	3	4	5	6
A						A
B						B
C						C
D						D
	1	2	3	4	5	6



Sheet: /common designs/		File: common designs.kicad_sch	
Title:			
Size: A4	Date:	Rev:	
KiCad E.D.A. 9.0.0		Id: 999/4	