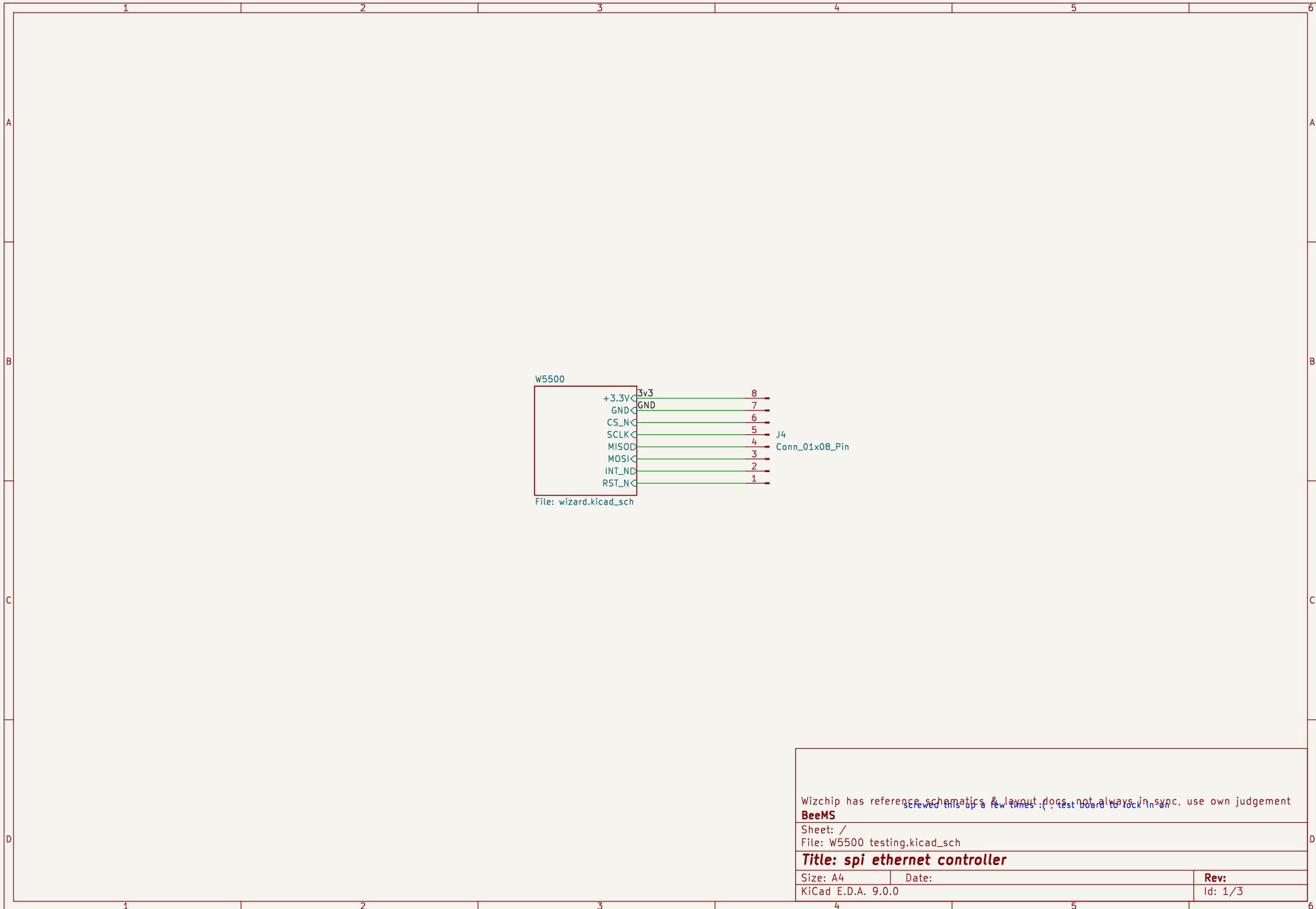


	1	2	3	4	5	6
A						A
B						B
C						C
D						D
	1	2	3	4	5	6



Sheet: /common designs/		File: common designs.kicad_sch	
Title:			
Size: A4	Date:	Rev:	
KiCad E.D.A. 9.0.0		Id: 0/3	



Wizchip has reference schematics & layout docs, not always in sync, use own judgement
 Solved this up a few times, test board to check in syn

BeeMS

Sheet: /
 File: W5500 testing.kicad_sch

Title: spi ethernet controller

Size: A4
 KiCad E.D.A. 9.0.0

Date:

Rev:
 Id: 1/3

