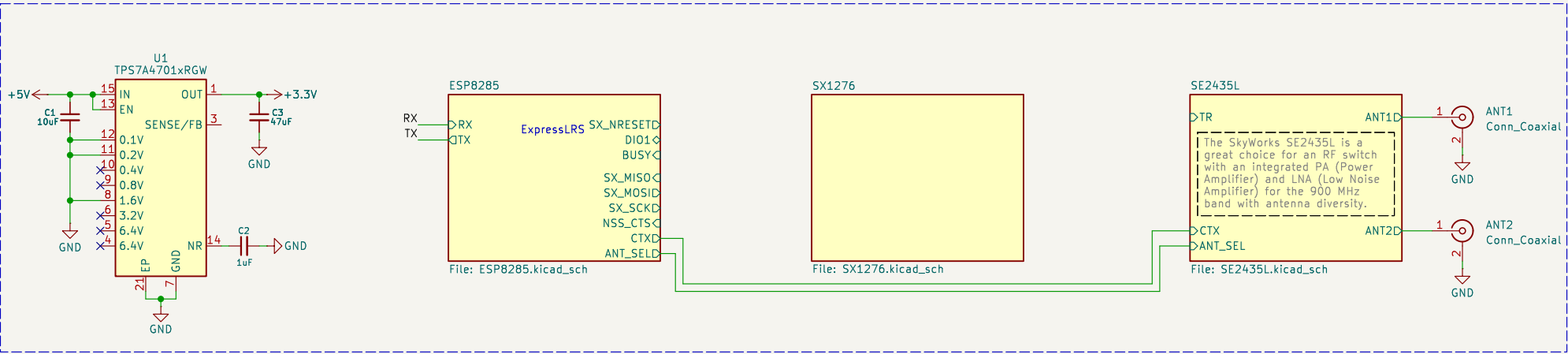


ExpressLRS_900MHz_RX



Pads, Headers and Connectors



<https://github.com/Paschalis | TeodoraMiu>

Sheet: /
File: AD2_900M.kicad_sch

Title:

Size: A4
KiCad E.D.A. 9.0.0

Date:

Rev: 1.0
Id: 1/4

The diagram shows a vertical circuit. At the top is a green line labeled '5V'. This line passes through two red rectangular resistors in series. The first resistor is labeled 'R5 0' and the second is labeled 'R6 1K'. After the resistors, the green line passes through a red LED symbol labeled 'D2 LED'. The LED is oriented with its cathode (the flat side) towards the 5V supply and its anode (the pointed side) towards the ground. The circuit ends at a green line labeled 'GND'.

The diagram shows a power distribution network (PDN) for a microcontroller. A +3.3V supply is connected to a series of capacitors (C23-C29) and a Tantalum capacitor (C30) connected to ground. The capacitors are labeled with their values: C23 (100nF), C24 (100nF), C25 (100nF), C26 (100nF), C27 (100nF), C28 (100nF), C29 (100nF), and C30 (10uF). The ground connection is labeled GND.

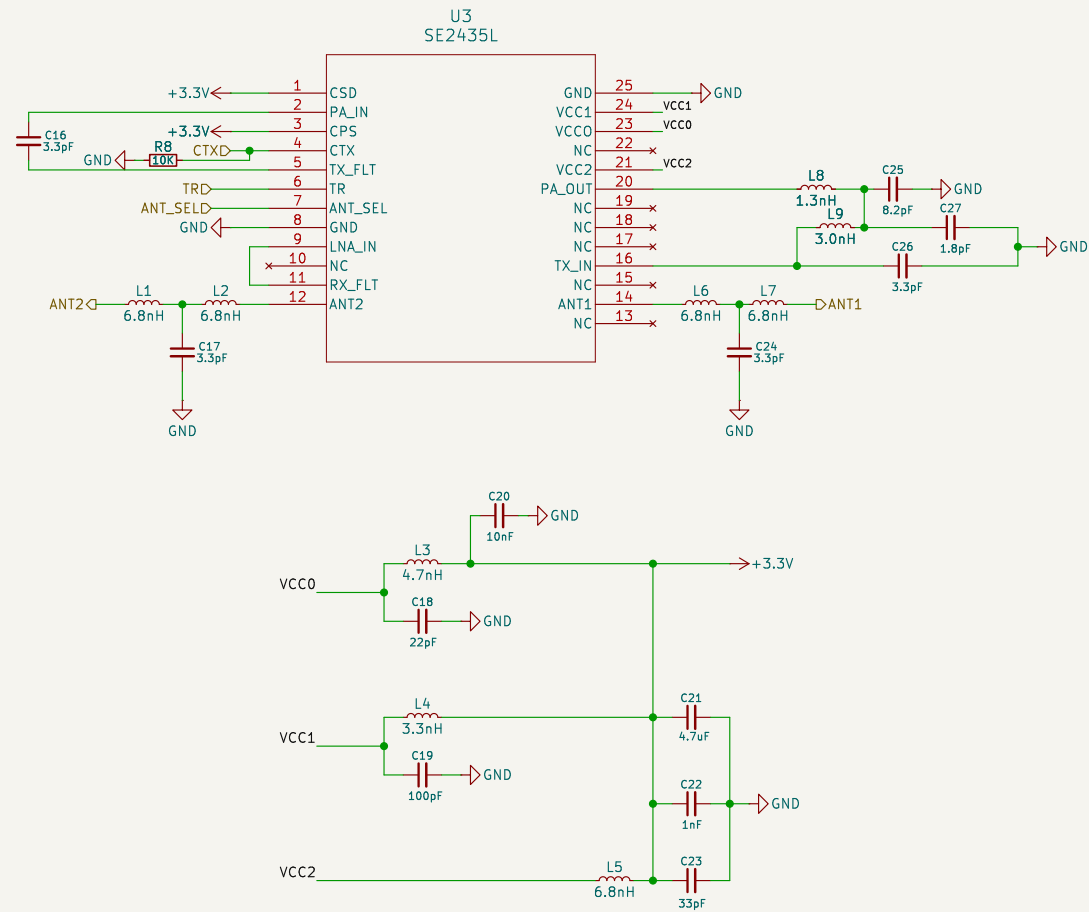
The schematic diagram illustrates the pin connections for the ESP8265 module (U4: ESP8285H16). The module is shown with its pins numbered 1 through 33. The connections are as follows:

- Power and Ground:**
 - VDDA (Pin 1) is connected to the antenna (AE1 W3008).
 - VDD3P3 (Pin 4) is connected to the antenna.
 - VDDA (Pin 33) is connected to GND.
 - EXT_RSTB (Pin 32) is connected to +3.3V via a 12kΩ resistor (R4).
 - RES12K (Pin 31) is connected to GND via a 12kΩ resistor (R4).
 - VDDA (Pin 30) is connected to GND via a 12kΩ resistor (R4).
 - VDDA (Pin 29) is connected to VDD.
 - XTAL_IN (Pin 28) is connected to GND.
 - XTAL_OUT (Pin 27) is connected to GND.
 - U0TXD (Pin 26) is connected to TX.
 - U0RXD (Pin 25) is connected to RX.
 - GPI05 (Pin 24) is connected to GND.
 - SD_DATA_1 (Pin 23) is connected to GND.
 - SD_DATA_0 (Pin 22) is connected to GND.
 - SD_CLK (Pin 21) is connected to GND.
 - SD_CMD (Pin 20) is connected to GND.
 - SD_DATA_3 (Pin 19) is connected to GND.
 - SD_DATA_2 (Pin 18) is connected to GND.
 - VDDPST (Pin 17) is connected to VDDPST.
- Antenna:**
 - AE1 W3008 is connected to VDDA (Pin 1) and VDD3P3 (Pin 4).
- LED:**
 - LED (Pin 8) is connected to the LED pin.
- Serial Communication:**
 - TX (Pin 26) is connected to TX.
 - RX (Pin 25) is connected to RX.
- Other Connections:**
 - R7 (10kΩ) is connected to +3.3V and GND.
 - C31 (0.1uF) and C32 (10uF) are connected to +3.3V and GND.
 - R8 (10kΩ) is connected to +3.3V and GND.
 - JP2 (Jumper_2_Small_Bridged) is connected to GND.
 - C21 (10pF) and C22 (10pF) are connected to GND.

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[illegible]

SE2435L: 860 to 930 MHz High-Power RF Front-End Module



<https://github.com/Paschalis | TeodoraMiu>

Sheet: /SE2435L/ Paschalis M.

File: SE2435L.kicad_sch

Title: SE2435L is a Sub-GHz RF Front-End Module (FEM) that integrates

Size: A4

Date:

Rev: 1.0

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Id: 4/4