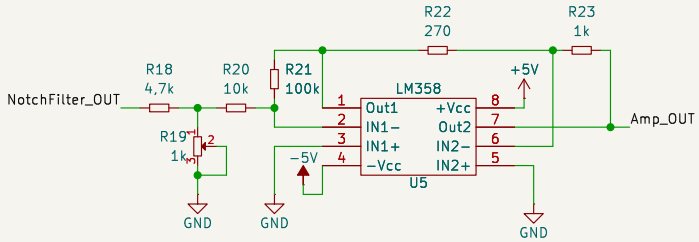
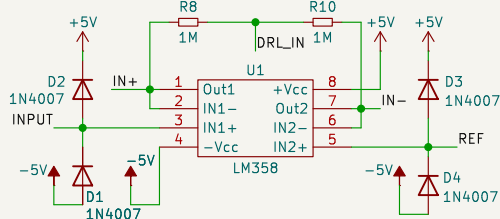


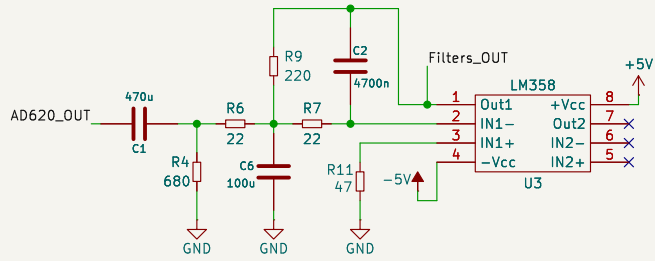
2nd Phase of Ajustable Amplification



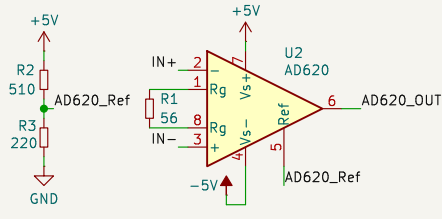
Protection Filter



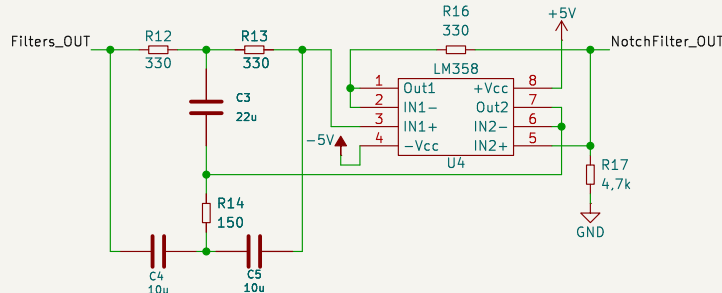
High Passes (0.5hz) - Low Passes (100hz)



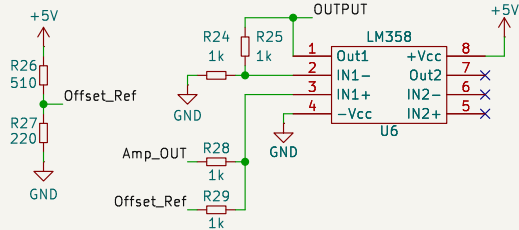
Instrumentation Amplifier AD620



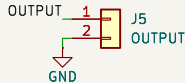
Notch Filter (50hz)



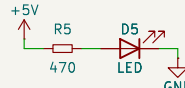
Offset Regulation



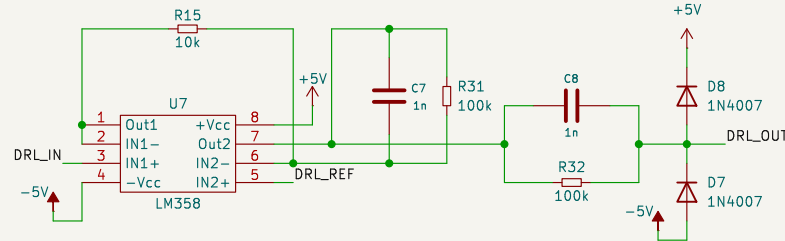
OUTPUT



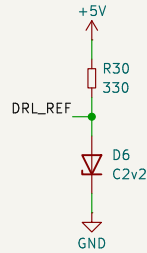
Power LED



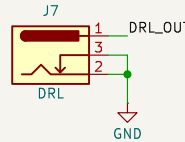
Diven Right Leg System



DRL Voltage Reference



DRL Output



 1
J6
Mounting Hole

 1  1
J8 J9
Jail Hole Jail Hole

More info <https://github.com/Romano047/Electroencefalograma-AD620.git>

Grupo: 2
Curso: 7mo 6ta
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Sheet: /
File: Amplificador_EEG.kicad_sch

Title: Amplificador Electroencefalograma

Size: A3 Date: 2025-08-19
KiCad E.D.A. 9.0.0

Rev: v4.0
Id: 1/1

