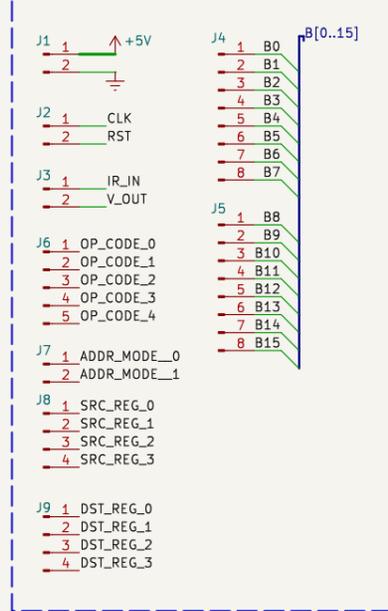
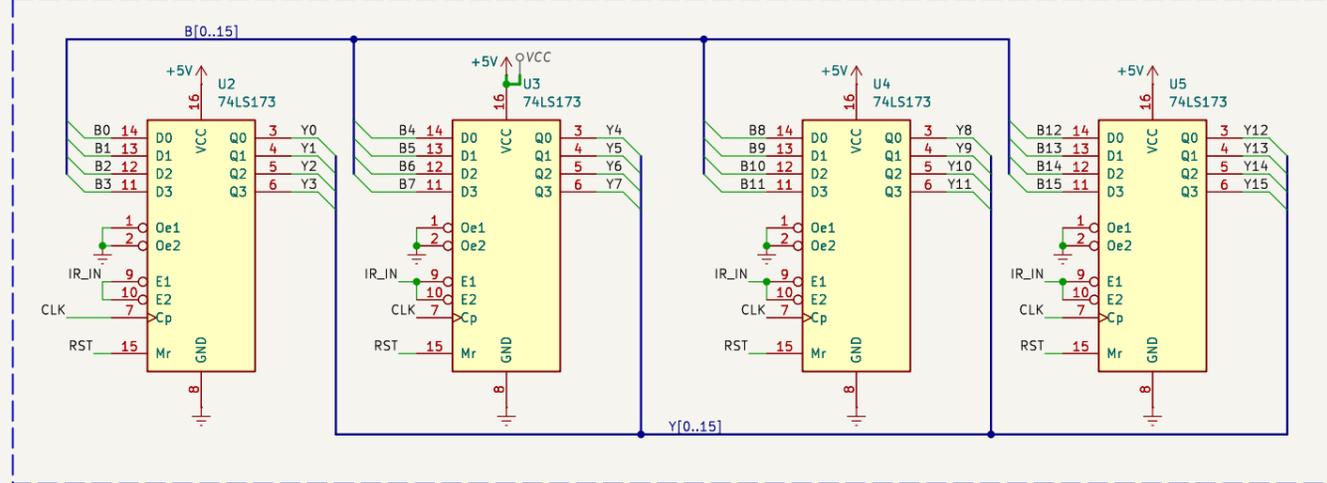


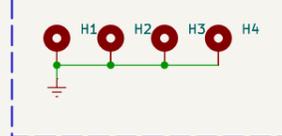
Connectors



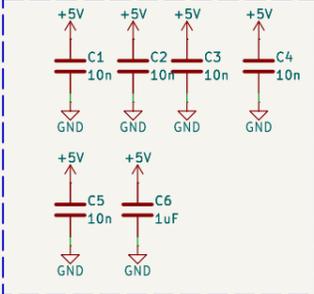
Registers



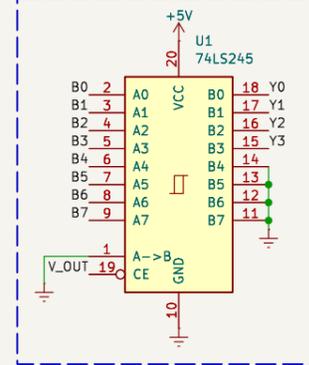
Mounting Holes



Bypass Capacitors

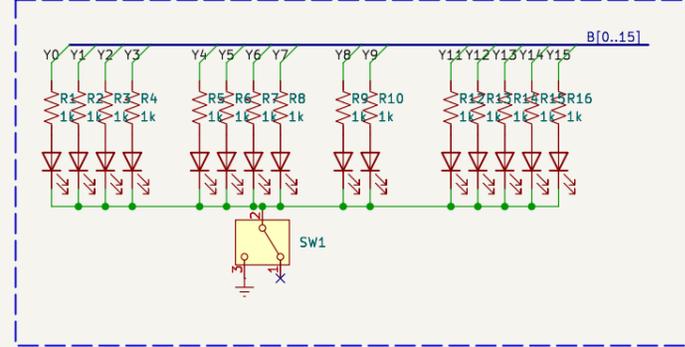


Transcievers



- OP_CODE_0 Y11
- OP_CODE_1 Y12
- OP_CODE_2 Y13
- OP_CODE_3 Y14
- OP_CODE_4 Y15
- ADDR_MODE_0 Y8
- ADDR_MODE_1 Y9
- SRC_REG_0 Y4
- SRC_REG_1 Y5
- SRC_REG_2 Y6
- SRC_REG_3 Y7
- DST_REG_0 Y0
- DST_REG_1 Y1
- DST_REG_2 Y2
- DST_REG_3 Y3

Data LED's



SHMERLARD

Sheet: /
File: Instruction_Register.kicad_sch

Title: SH8 - Instruction Register

Size: A3 Date: 2024-09-25
KiCad E.D.A. 9.0.0

Rev: 1.0
Id: 1/1