

Connector

	A[0..15]	□
	D[0..7]	□
	EXP_nSELD	□
	CLKD	□
	CLK/2D	□
×	PHI10	□
×	PHI20	□
	CPU_CLKD	□
	SLOT_OSC	×
×	ISYNC	nSLOW0
×	RDY	nSLOW1
×	nVP	□
×	nML	nRESD
×	nSO	RnWD
×	BE	□
	nIRQ	×
	nNMI	×
	nRAMDISABLE	×
	nROMDISABLE	×
	nOSC_DISABLE	×
	CPLD_nDIS	×

File: Connector.kicad\_sch

Sheet: /  
File: Expansion-Template.kicad\_sch

**Title:**

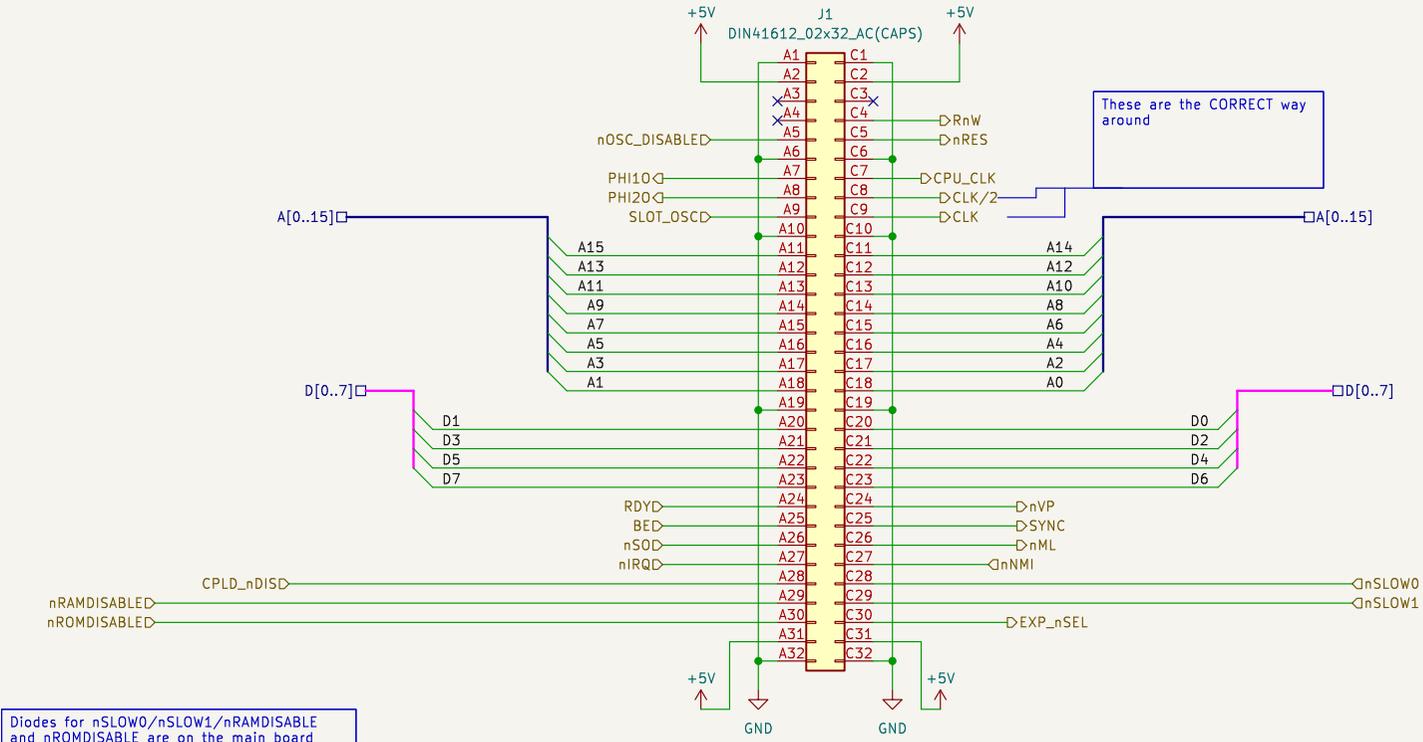
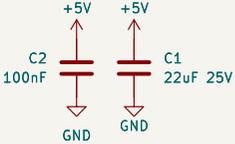
Size: A4  
KiCad E.D.A. 9.0.0

Date:

**Rev:**

Id: 1/2

Diodes for nSLOW0/nSLOW1/nRAMDISABLE and nROMDISABLE are on the main board so are not needed here. All ACTIVE LOW  
 CPLD\_nDIS has pull up main board, ACTIVE LOW



These are the CORRECT way around

Sheet: /Connector/		Date:	
File: Connector.kicad_sch		Rev:	
Size: A4	KiCad E.D.A. 9.0.0		Id: 2/2