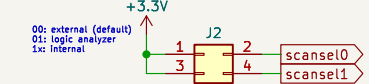


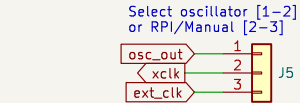
TinyTapeout 1/2/3 Demo Board

User Input + Config

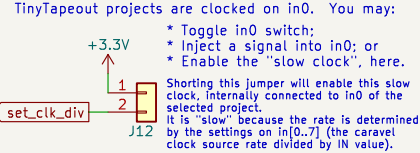
Scan Chain Driver Select



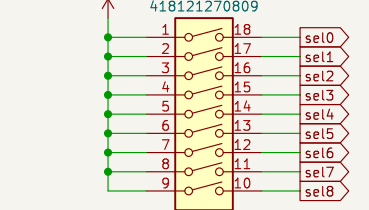
Caravel Clock Source



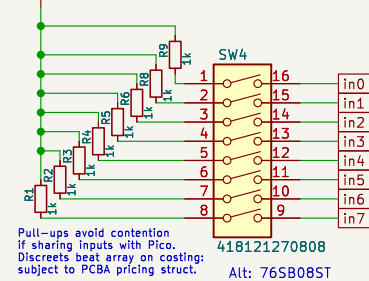
TT Project Internal Clock



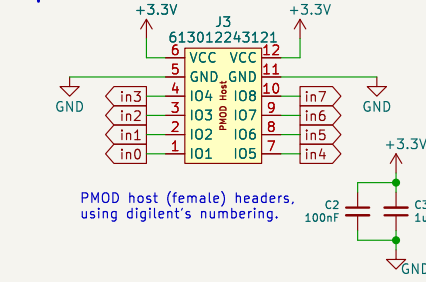
Active Select



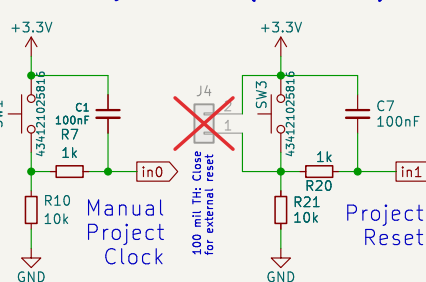
Input DIP



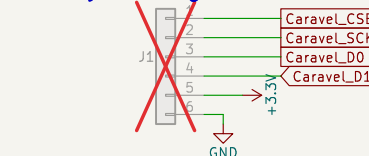
Input PMOD



Momentary Switches (debounced)

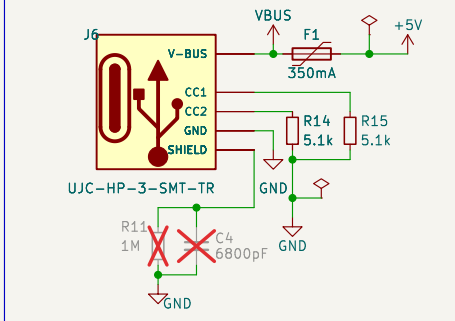


Memory flashing connector

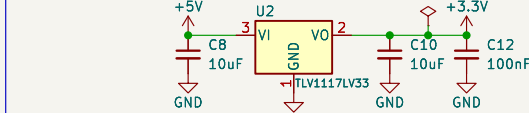


Power

USB connector

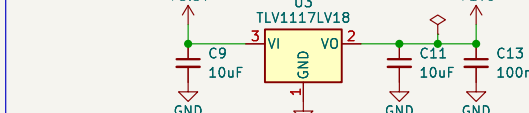


3.3V LDO



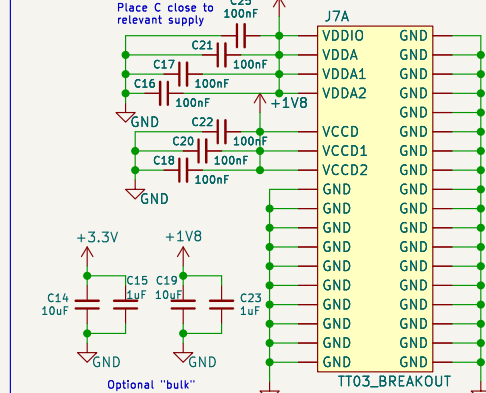
Both of these LDOs are serious overkill, but this means you can safely power much external circuitry using the 3v3 and 1v8 header pins.

1.8V LDO



The 1v8 regulator is cascaded on the 3v3, which should allow operation if back-powering 3v3 line, as Matt did in TT2 bringup livestream.

TT Carrier Power



Note: All this decoupling should be amply handled by the carrier. Would rather DNP than regret.

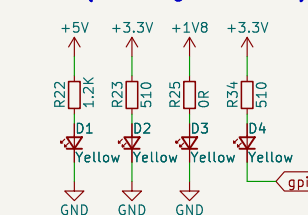


Extensive labelling, jumpers to set scan selection and clock source (on-board oscillator or manual/pico), DIP switches for inputs and selection, 7-segment display (remappable with jumpers) on outputs, full access to 8 in and out via PMODs, all pins broken out in breadboardable headers. Optional Raspberry Pi Pico, pads on underside.

Power via +5V USB, or 5V breakout pin. On-board regulation to 3v3 and 1v8. VDDIO is 3v3, including on PMODs.

Outputs

LEDs (Power good, GPIO)



7-segment Display

