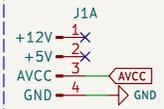
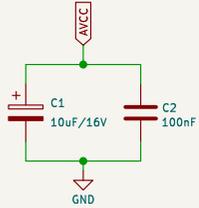


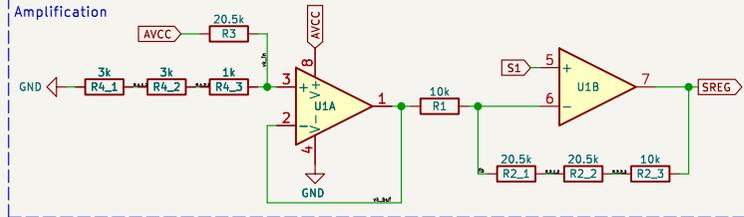
Power Input



Filter/ Buffer Capacitors

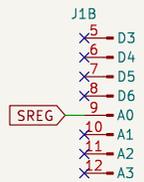


Amplification

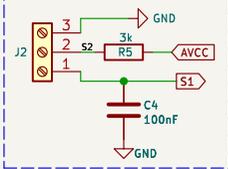


V\_max = 5.033V  
 V\_low = V(-50 C) = 1.071V  
 V\_high = V(150 C) = 1.896V  
 V\_diff = 0.825V  
 gain = (V\_max - V\_diff) / V\_diff = 5.1  
 V\_k = ((1 + gain) / gain) \* V\_low = 1.281V  
 Gain  
 R\_1 = 10k  
 R\_2 = R\_1 \* gain = 51k  
 R\_3 = 20.5k  
 Inv Input Voltage Divider  
 R\_4 = R\_3 \* (V\_k / (V\_max - V\_k)) = 7k  
 Sensor Voltage divider R\_5 = 3k

ATMega



Front Terminal



Sheet: /  
 File: temp-module.kicad\_sch

Title:

Size: A4  
 KiCad E.D.A. 9.0.0

Date:

Rev:

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