



65c02 board needs buffer for R/W (for slots)  
 DMA OR-circuit for PH0? - See if MEGA handles it  
 buffers for Address bus 74HCT244s  
 The processor can be held off for a total of 10 microseconds. (10 0o clock cycles).  
<http://www.1000bit.it/support/manuali/apple/technotes/aiie/tn.aiie.02.html>

<b>Title: IO Prototype (Slot)</b>	
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