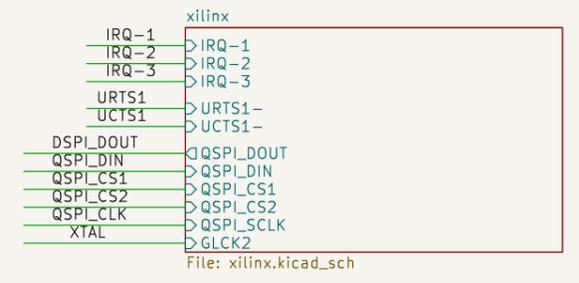
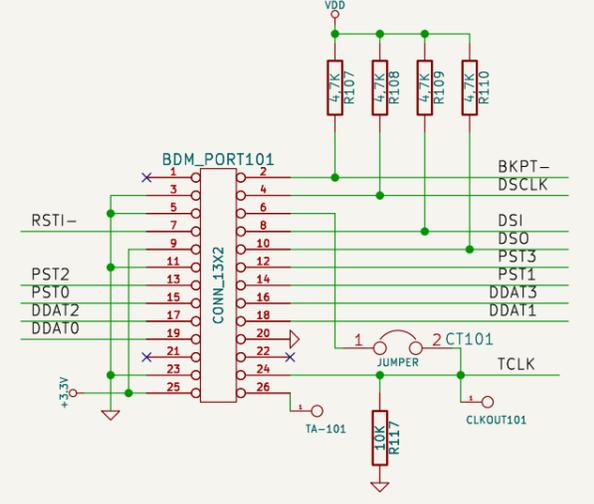
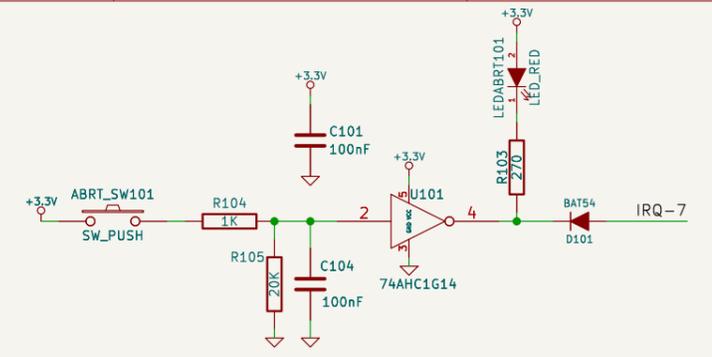
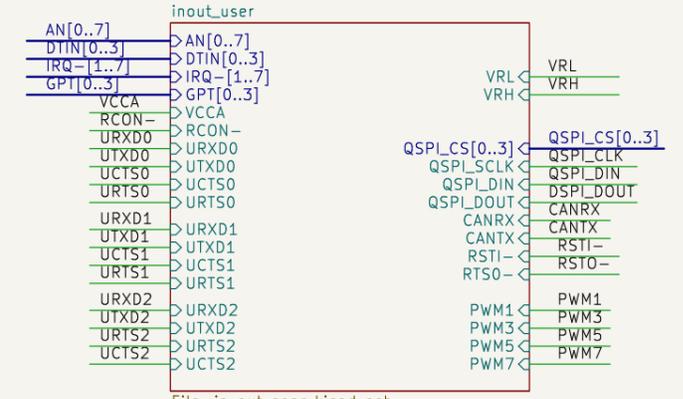


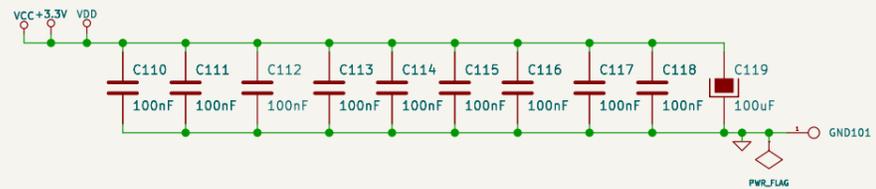
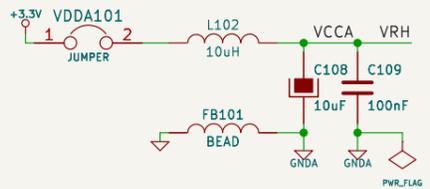
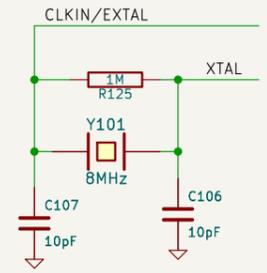
38	PWM1	PWM1/PTD0
33	PWM3	PWM3/PTD1
60	PWM5	PWM5/PTD2
63	PWM7	PWM7/PTD3
58	GPT0	GPT0/PWM1/PTA0
59	GPT1	GPT1/PWM3/PTA1
61	GPT2	GPT2/PWM5//PTA2
62	GPT3	GPT3/PWM7/PTA3
36	DTINO	DTINO/DTOUT0/PWM0/PTC0
37	DTIN1	DTIN1/DTOUT1/PWM2/PTC1
31	DTIN2	DTIN2/DTOUT2/PWM4/PTC2
32	DTIN3	DTIN3/DTOUT3/PWM6/PTC3
10	CANTX	SCL/CANTX/PAS0/UTXD2
11	CANRX	SDA/CANRX/PAS1/URXD2
8	UTXD0	UTXD0/PUA0
7	URXD0	URXD0/PUA1
99	UTXD1	UTXD1/PUB0
100	URXD1	UTXD1/PUB1
29	UTXD2	UTXD2/PUC0
28	URXD2	UTXD2/PUC1
9	URTS0	URTS0/CANTX/PUA2
6	UCTS0	UCTS0/CANRX/PUA3
4	URTS1	URTS1/SYNCR/UTXD2/PUB2
98	UCTS1	UCTS1/SYNCA/URXD2/PUB3
30	URTS2	URTS2/PUC2
27	UCTS2	UCTS2/PUC3
17	DSPL_DOUT	QSPIL_DOUT/EZPQ/CANTX/RXD0/PQS0
16	QSPIL_DIN	QSPIL_DIN/EZPD/CANRX/RXD1/PQS1
18	QSPIL_CLK	QSPIL_CLK/EZPCK/SCL/RTS1/PQS2
20	QSPIL_CS0	QSPIL_CS0/SDA/CTS1/PQS3
19	QSPIL_CS1	QSPIL_CS1/PQS4
13	QSPIL_CS2	QSPIL_CS2/PQS5
12	QSPIL_CS3	QSPIL_CS3/SYNCA/SYNCR/PQS6
43	ANO/PAN0	ANO
44	ANO/PAN1	ANO
45	ANO/PAN2	ANO
46	ANO/PAN3	ANO
54	ANO/PAN4	ANO
53	ANO/PAN5	ANO
52	ANO/PAN6	ANO
51	ANO/PAN7	ANO
49	VRH	VRH
48	VRL	VRL
50	VCCA	VCCA
47	VSSA	VSSA

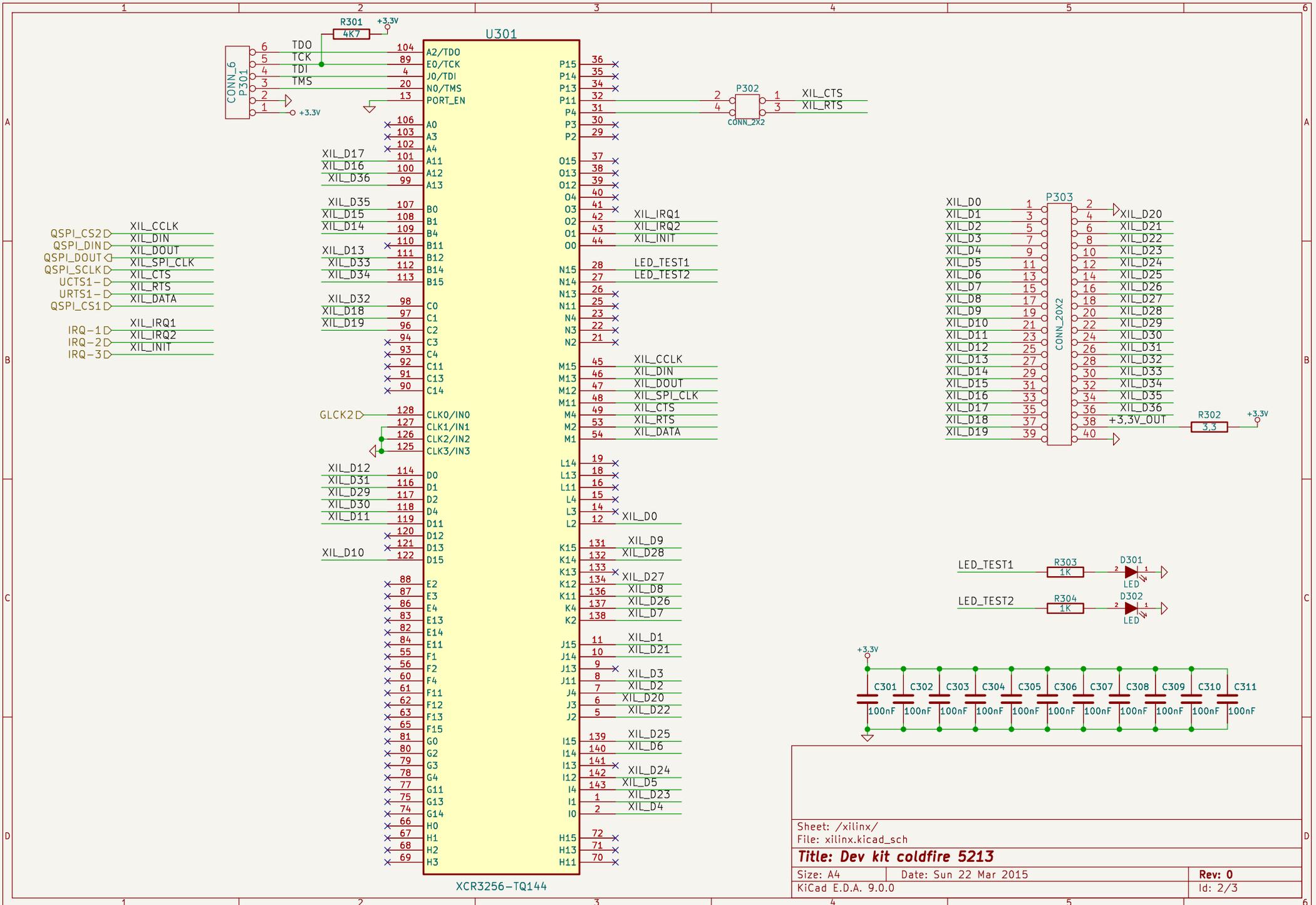


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