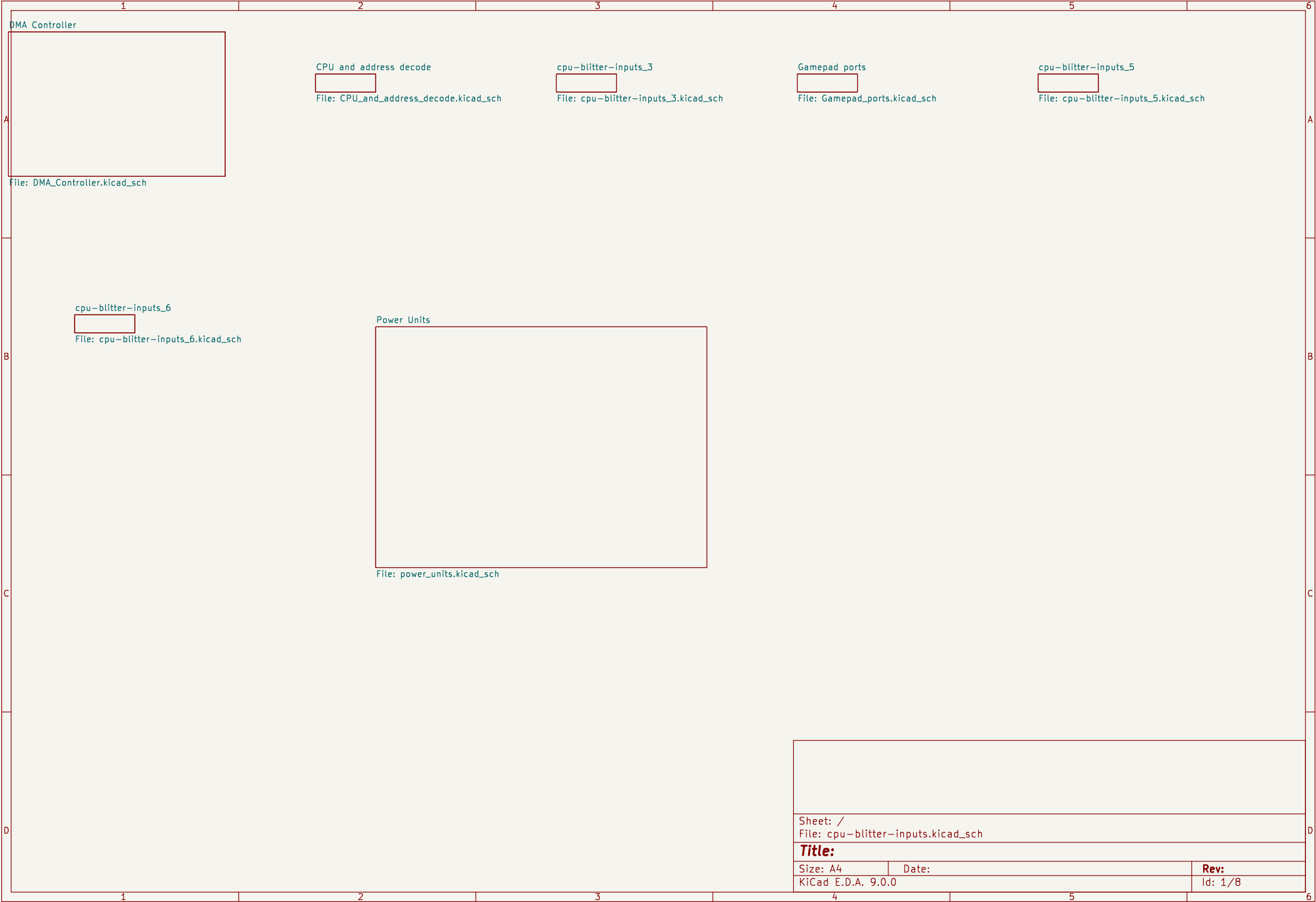
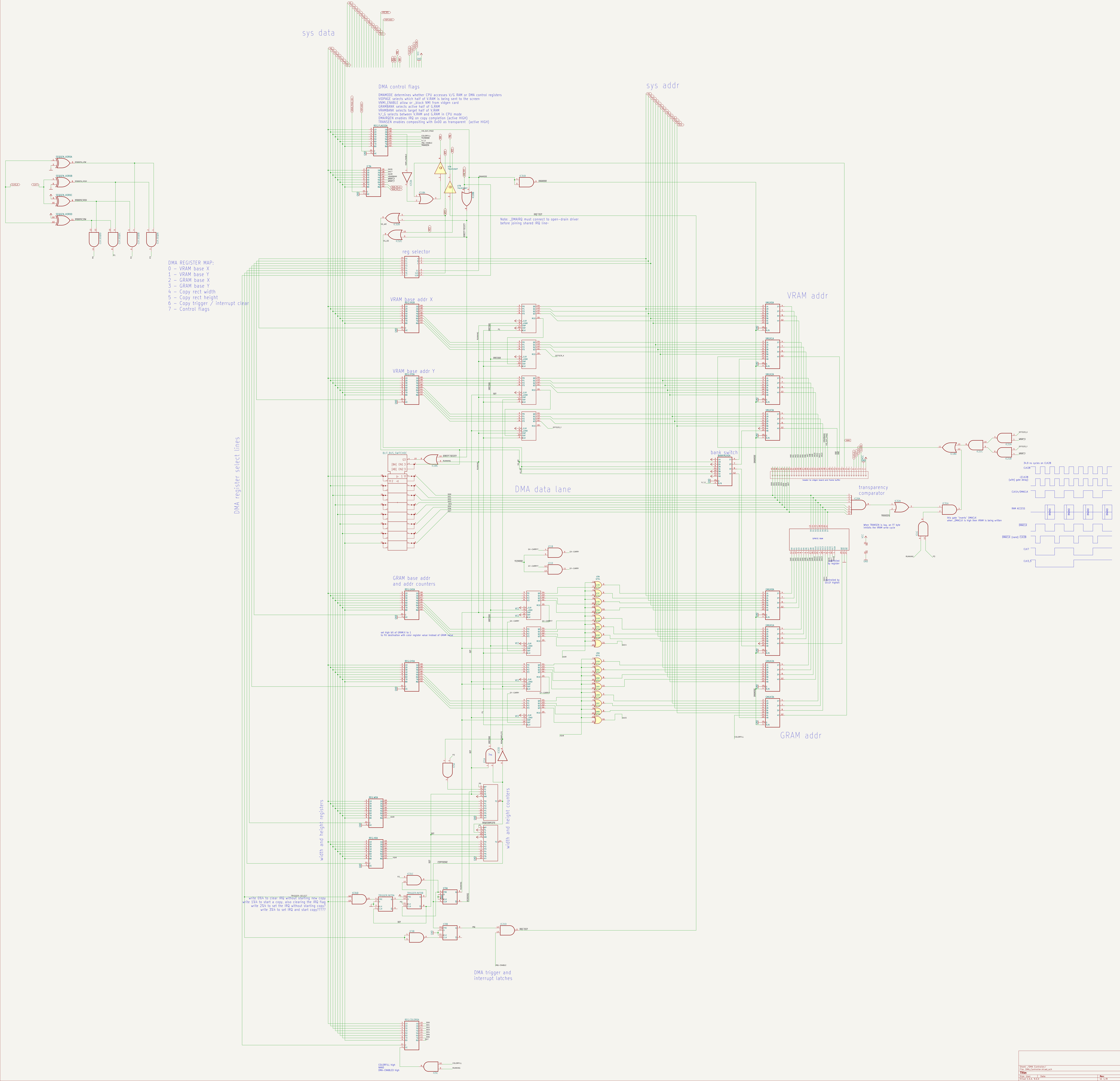
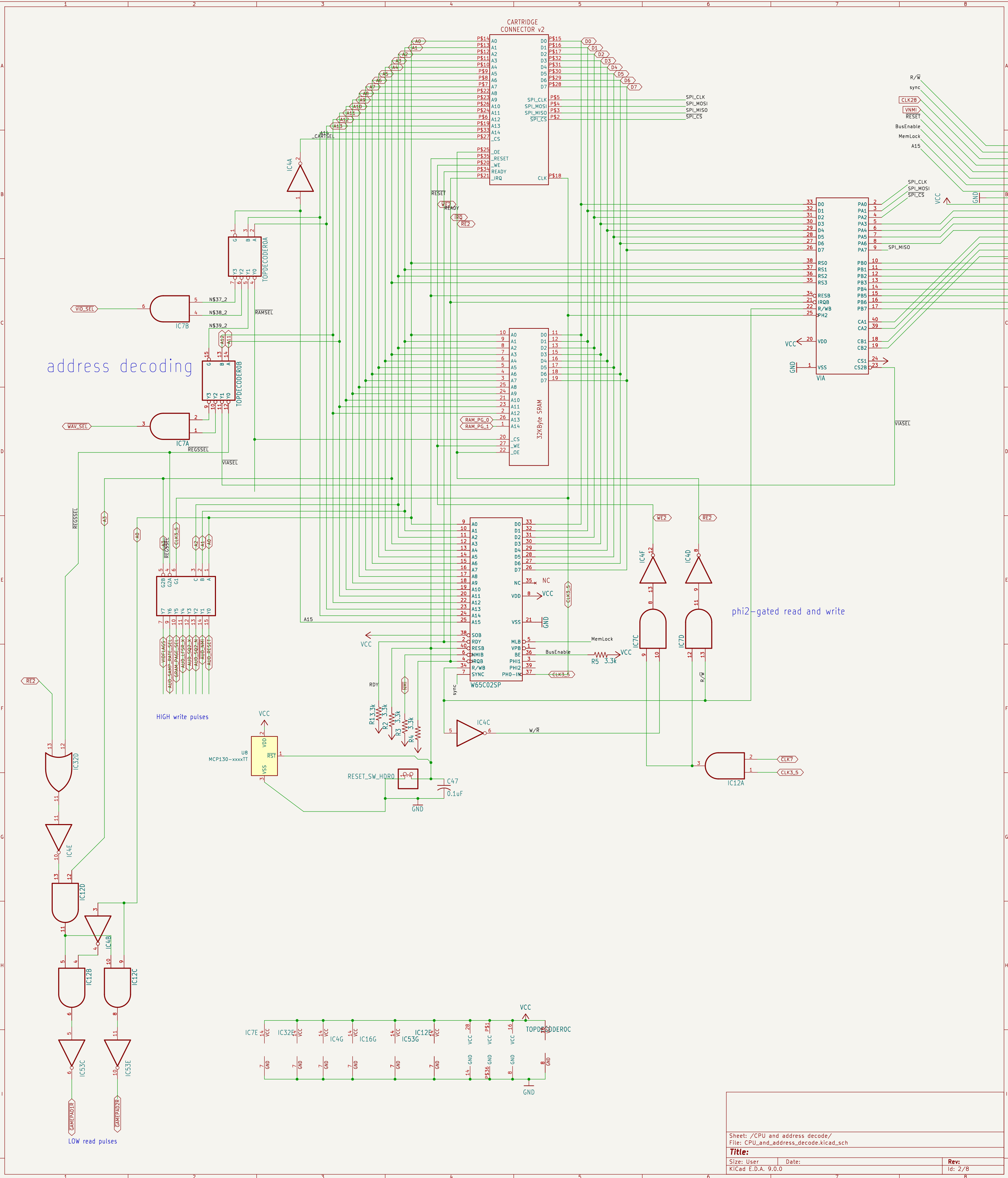
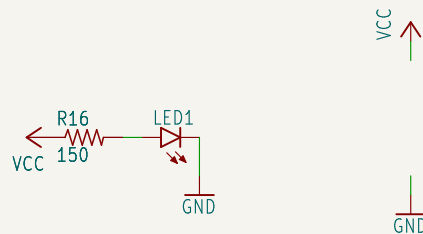
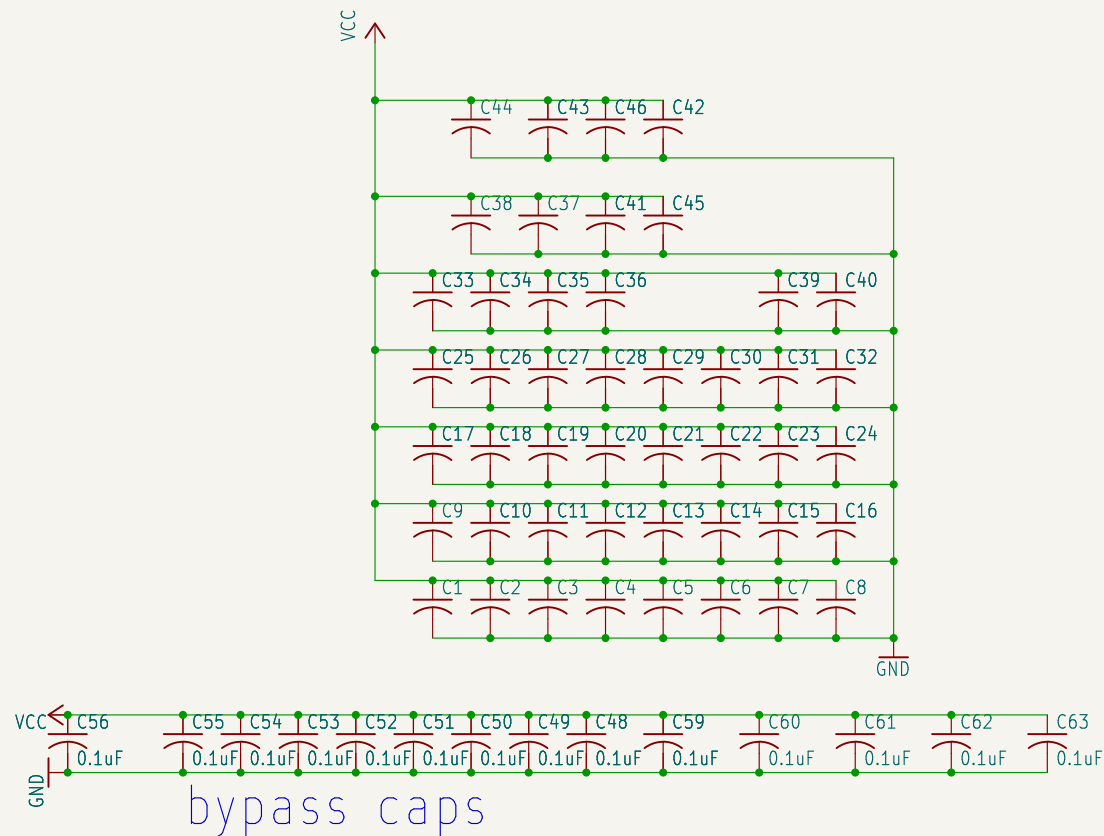
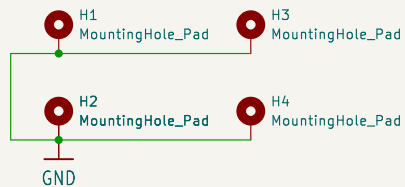






DMA REGISTER MAP:
0 - VRAM base X
1 - VRAM base Y
2 - GRAM base X
3 - GRAM base Y
4 - Copy rect width
5 - Copy rect height
6 - Copy trigger / interrupt clear
7 - Control flags





Sheet: /cpu-blitter-inputs_3/
File: cpu-blitter-inputs_3.kicad_sch

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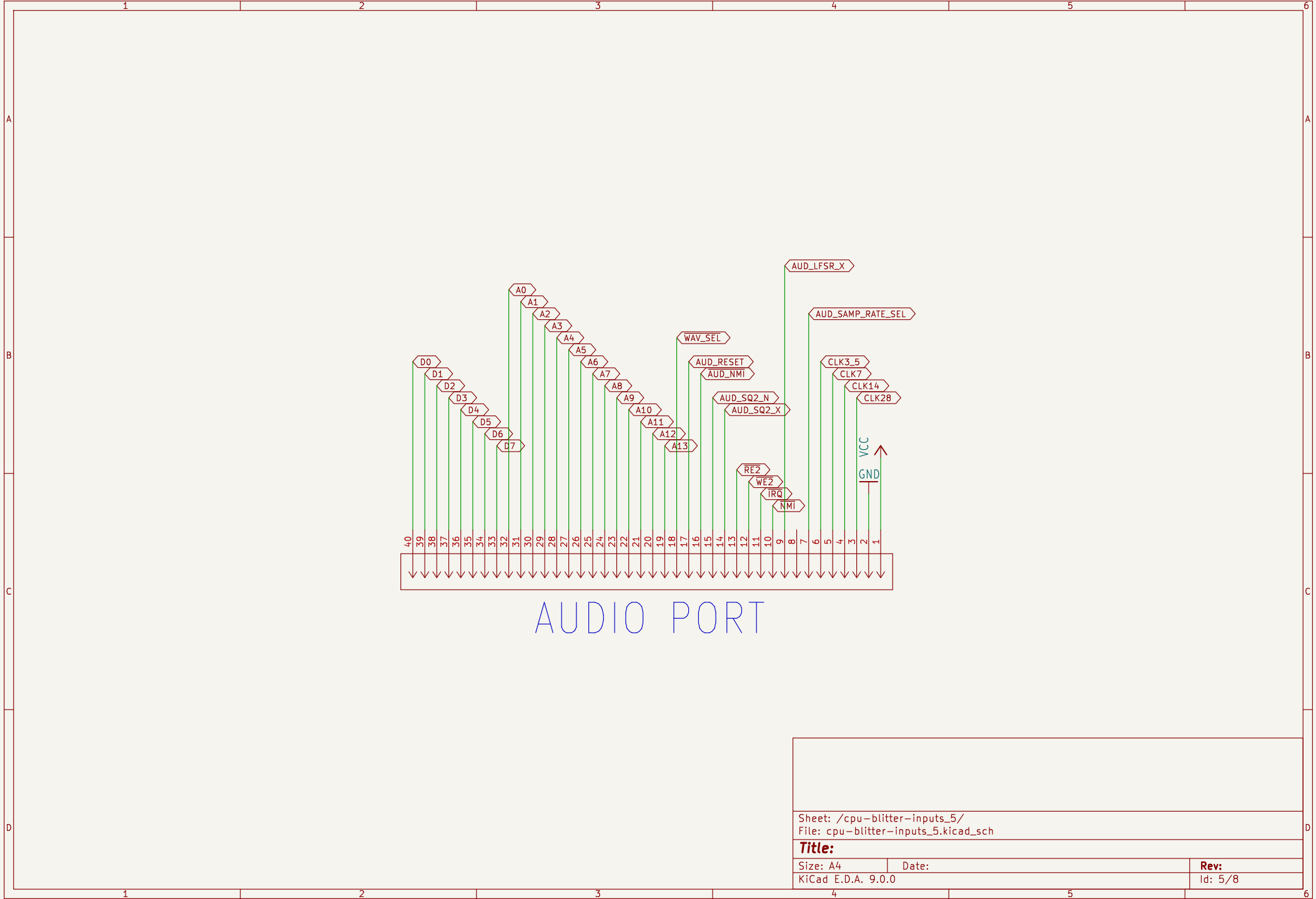
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Date:

Rev:

KiCad E.D.A. 9.0.0

Id: 3/8



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KiCad E.D.A. 9.0.0

Rev:

Id: 5/8

