



Same Frequency Comparator U22,U23
It comes into operation when a note is released.
When this occurs, the circuit removes the gate
signal from the respective memory location but
rewrites the pitch data which would otherwise
be cleared!

U1, U2, U3, U4, U5 decides when to assign a new channel to a voice:
ANC Assign New Channel to voice
CLC Clear Existing One

New note priority
MODE A (Open Connection) Note stealing
MODE B (Closed Connection) No stealing

Channel Status RAM U24
Stores the pitch and gate parameters, the number
of parameters stored being determined by the selector
switch SW1 (Number of voices). Data bits D0 to D5
generate pitch and bit D6 is the gate signal.

Channel Position Counter U16
Decides which will be the next channel to be assigned
in MODE B if a channel is already in use, then it will skip it
and continue on until it finds a free one.
In MODE A the Channel Position Counter indicates the
oldest channel which will then be reassigned if no free
channel is available.

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