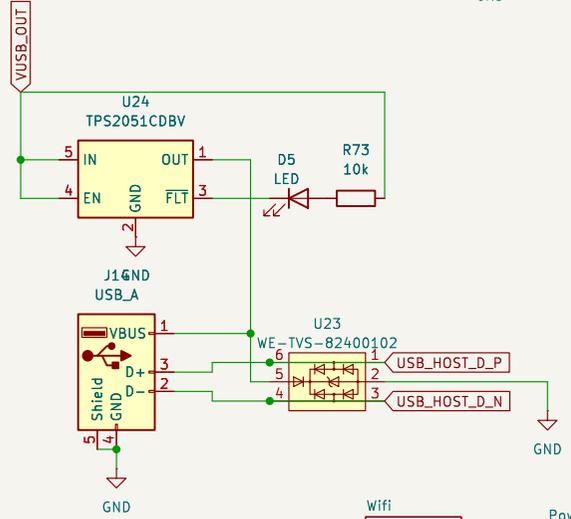
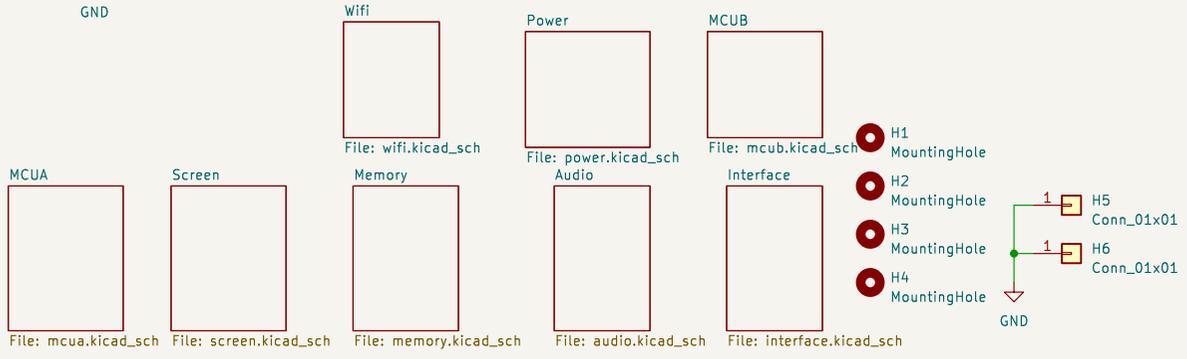
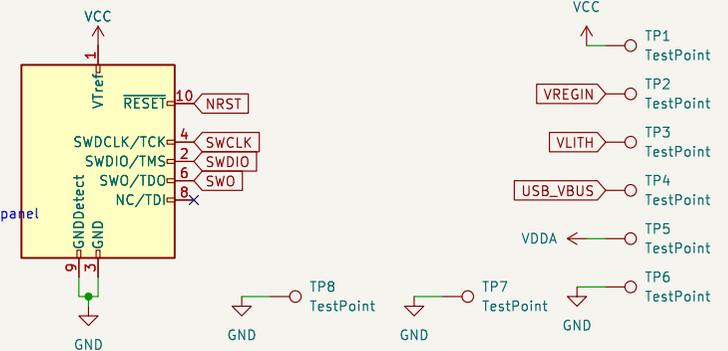


Input current limit -1.1A  
 Max charge current -890mA  
 Uses default timers. Bridge JP4 to disable charge timers.

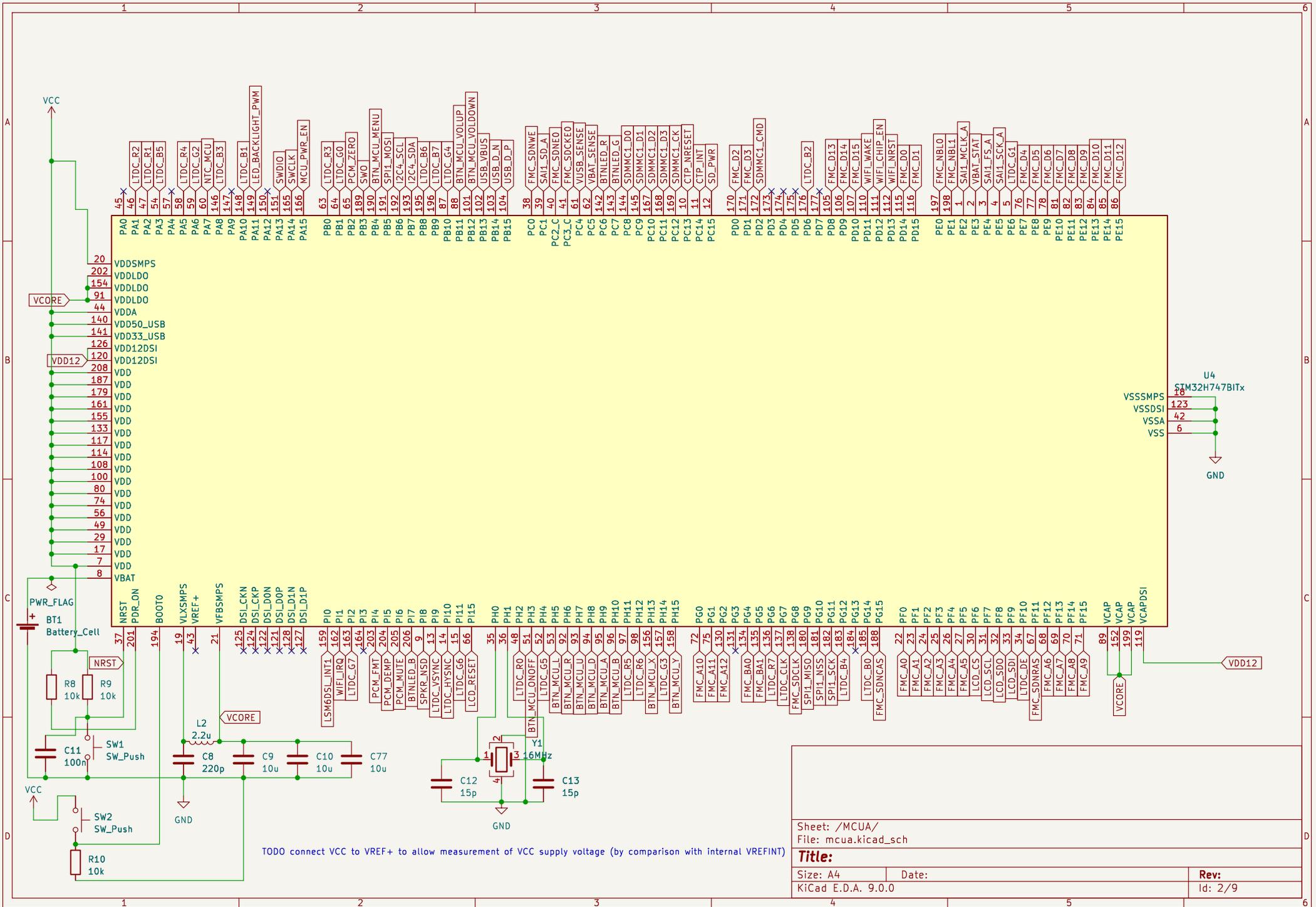


Next version:  
 Fix audio output amp polarity – usually signal on -ve input  
 Fix power button issue necessitating wire on board currently  
 Use IO expander for buttons (e.g. PI4IOE5V6416ZDEX)  
 Place joystick(s) and ?buttons on separate board to raise them up closer to front panel  
 Add extra analogue joystick (use freed up IOs)  
 Add hardware power cycle (e.g. another TPS2041BDBV) to tilt sensor  
 Use J-Link power output as another possible option to keep the main power going (would need 19-pin header on board instead of 9 pin)

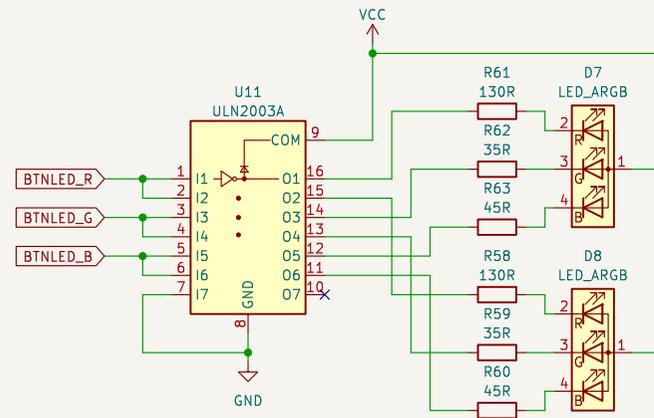
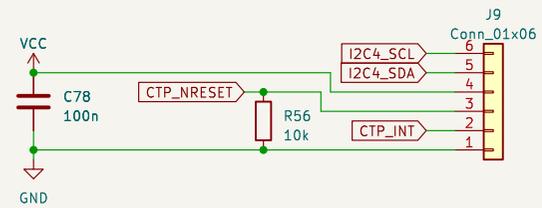
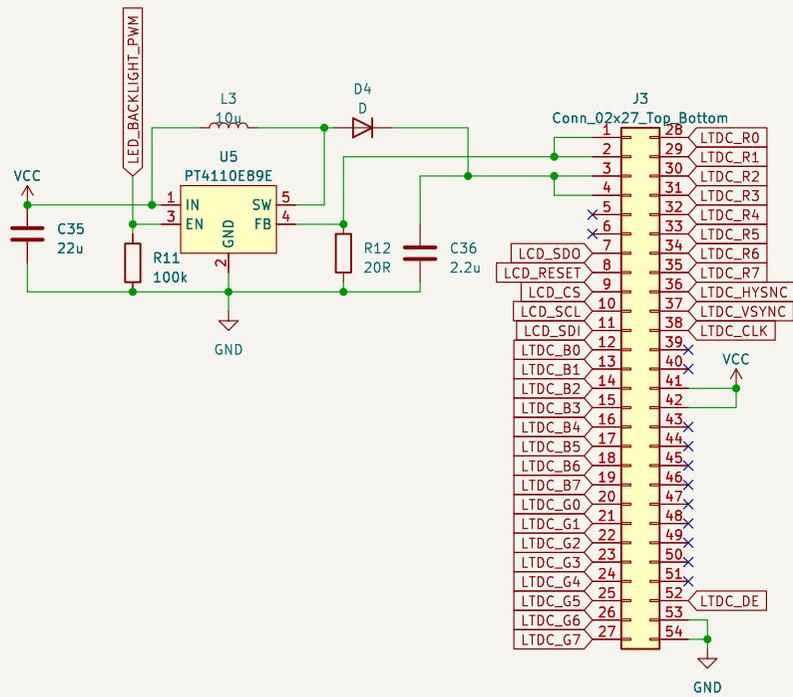


Hotplate profile:  
 SMD291SNL: 160C 3 minutes, 260C 2 minutes.  
 SMD291AX: 140C 3 minutes, 240C 2 minutes.

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TODO connect VCC to VREF+ to allow measurement of VCC supply voltage (by comparison with internal VREFINT)



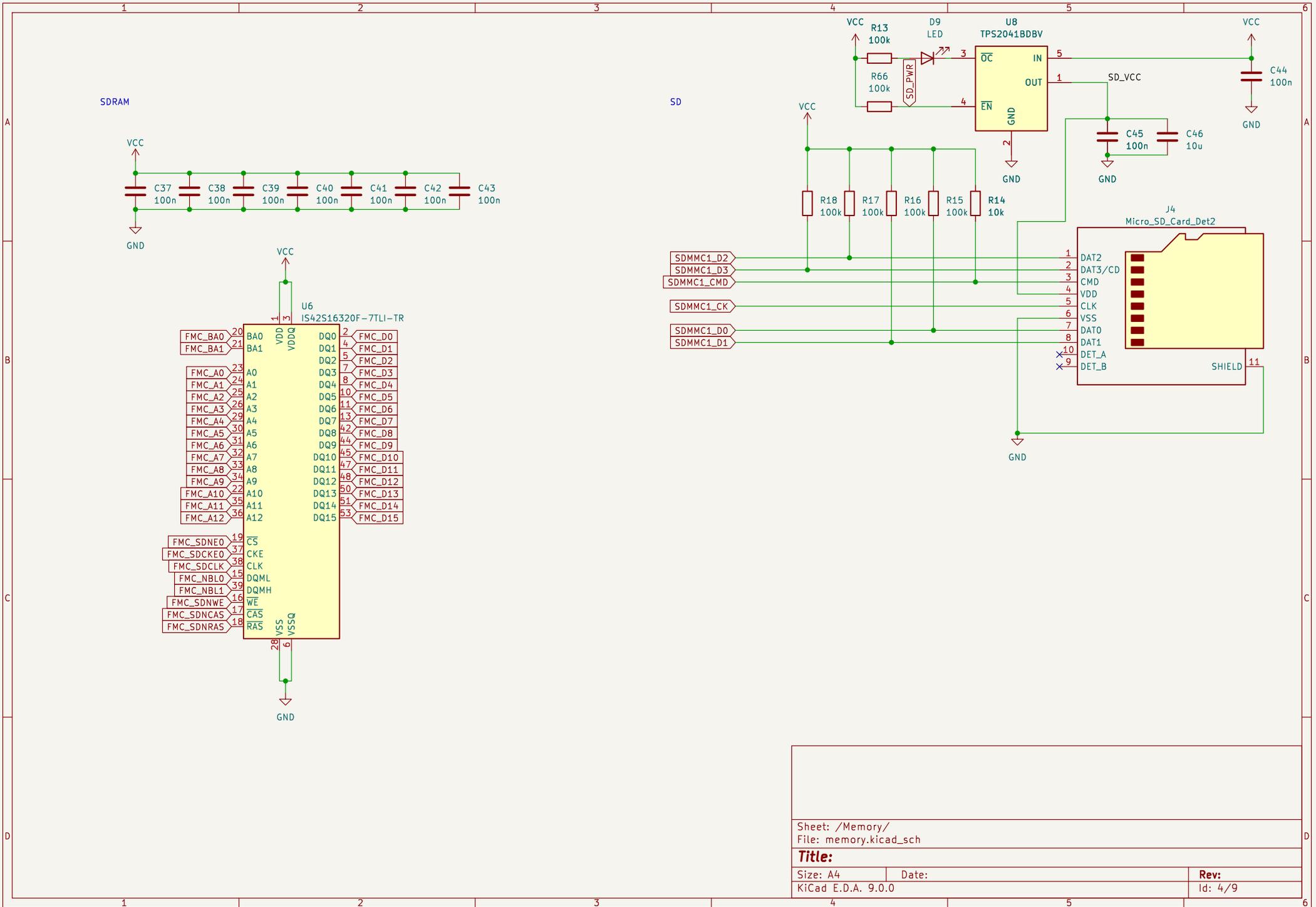
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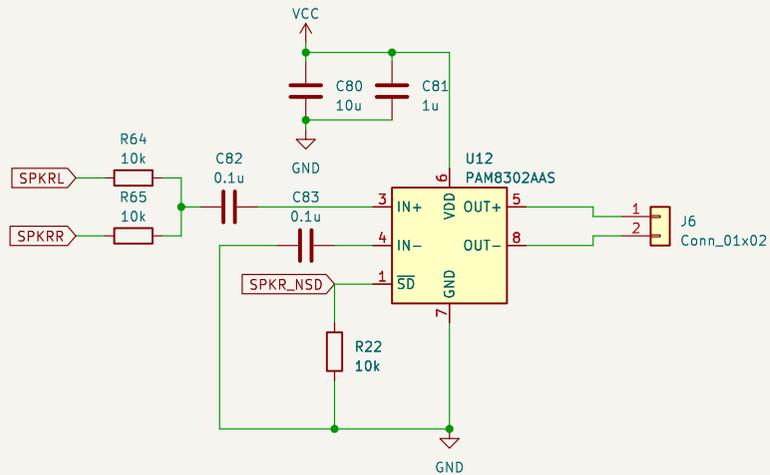
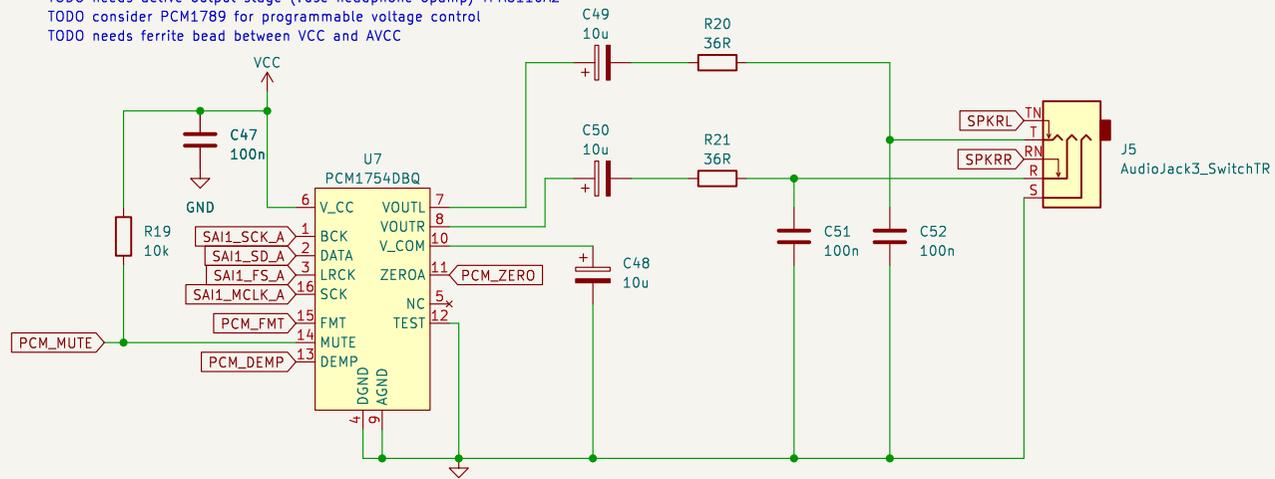
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TODO needs active output stage (?use headphone opamp) TPA6110A2  
 TODO consider PCM1789 for programmable voltage control  
 TODO needs ferrite bead between VCC and AVCC



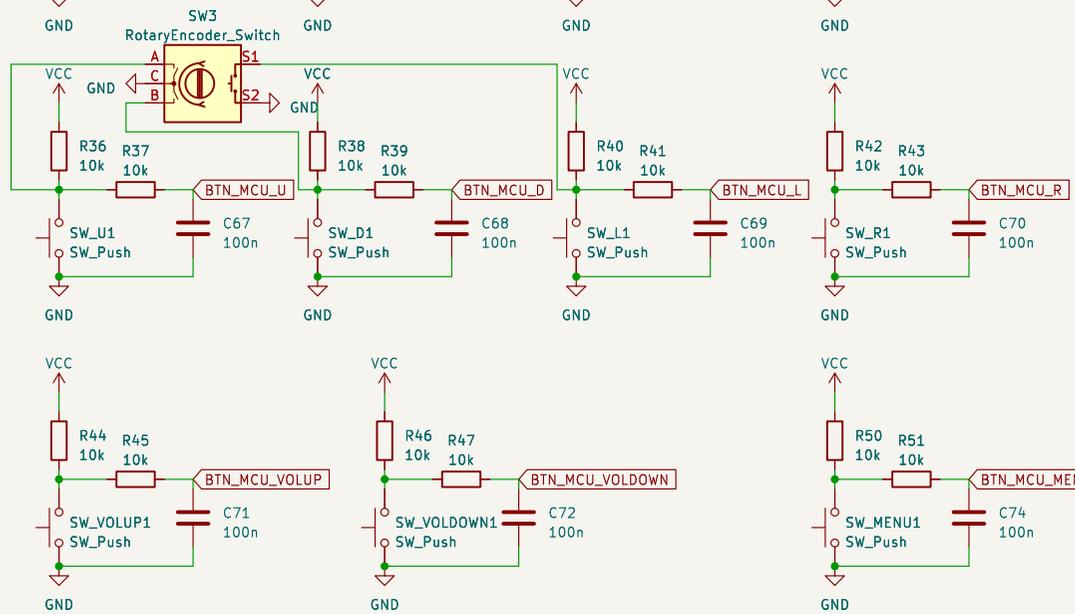
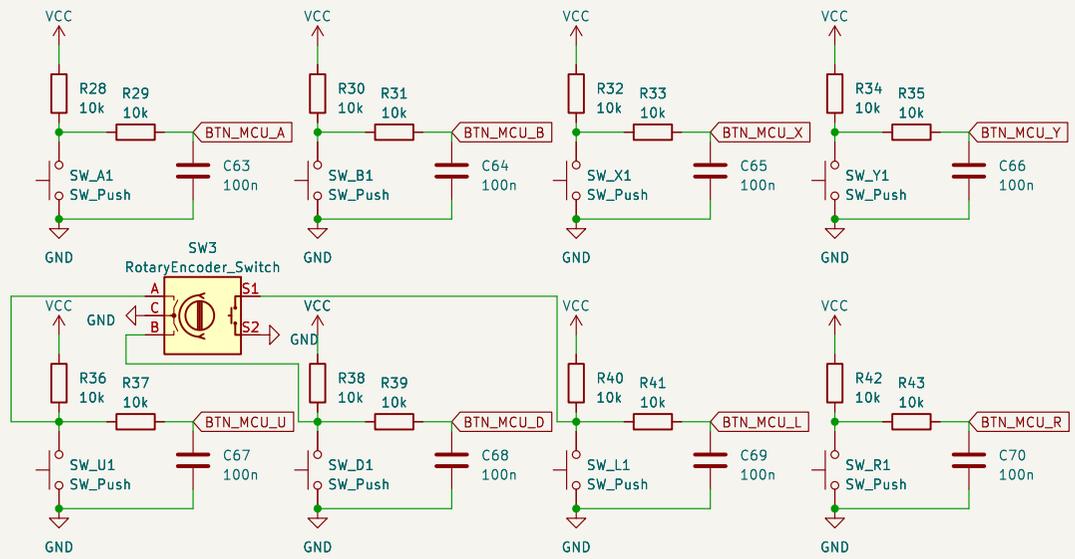
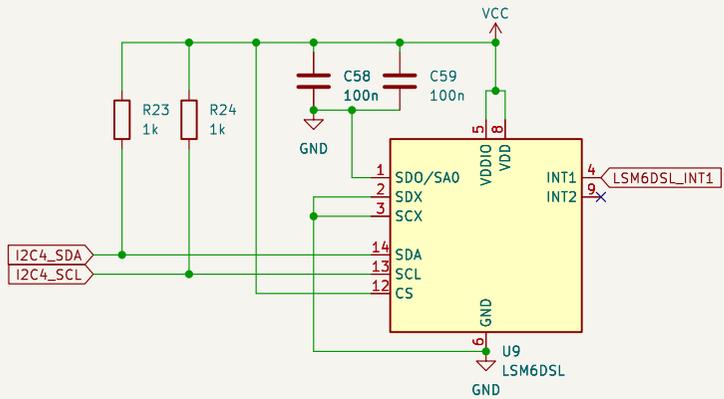
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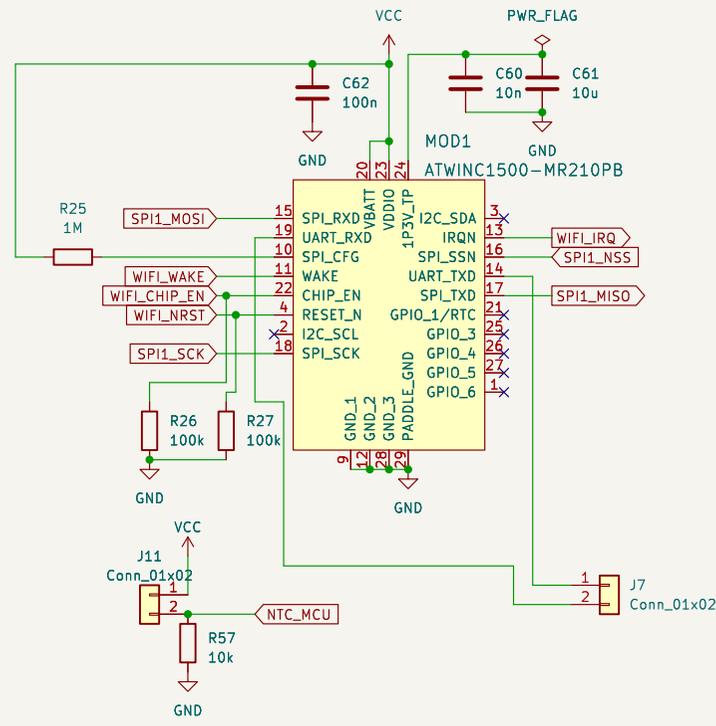
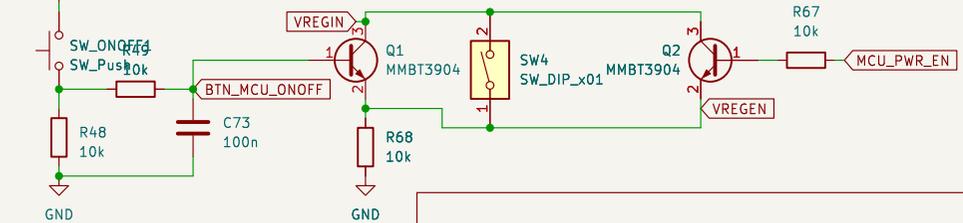
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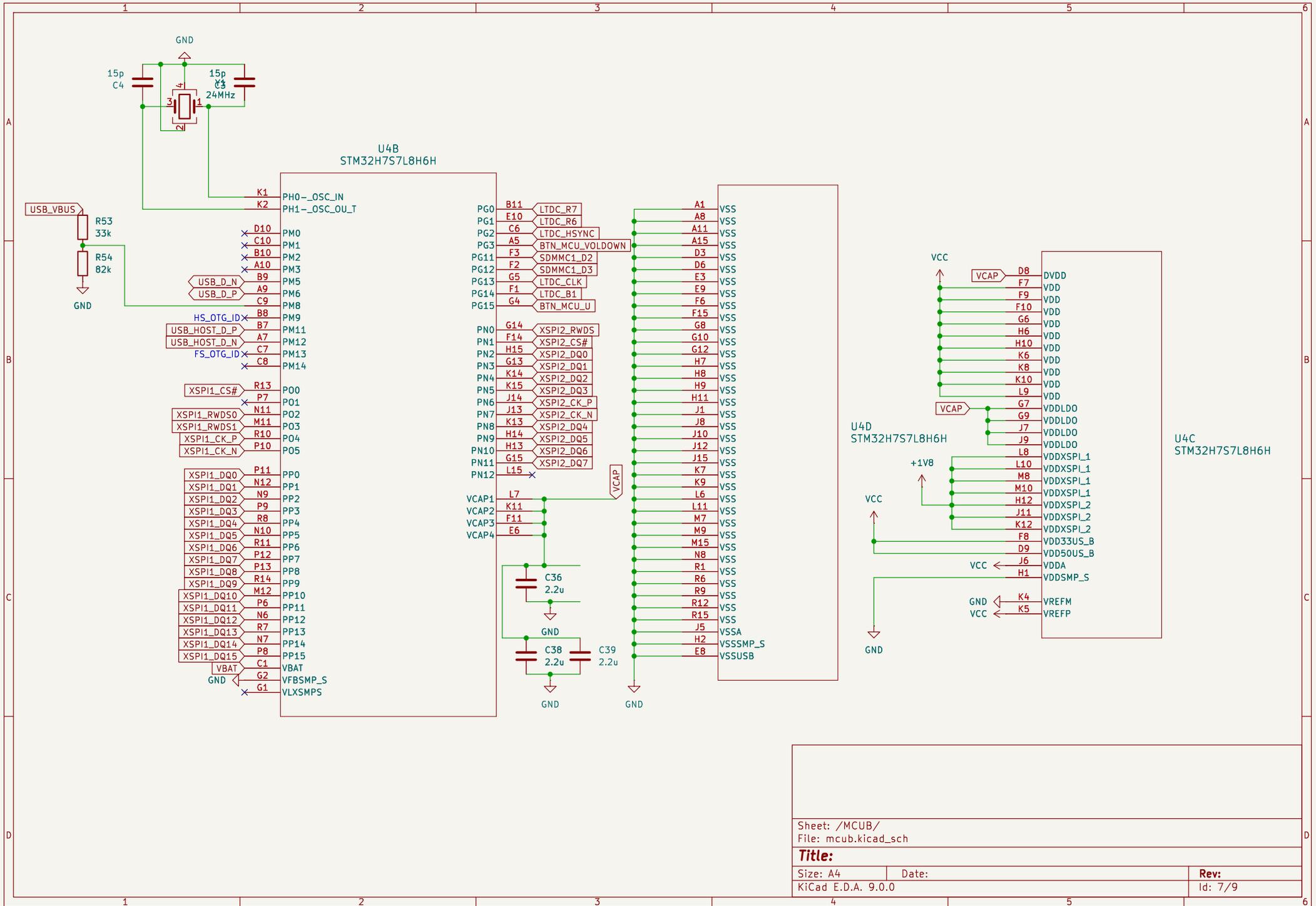
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ON/OFF triggers an NPN to enable VREG as well as trigger a GPIO  
 The MCU also needs to be able to trigger the VREG  
 Finally, we provide a slide switch for debugging purposes to keep the system always on





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