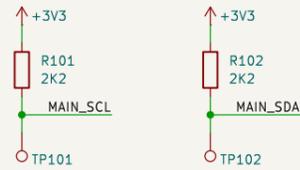
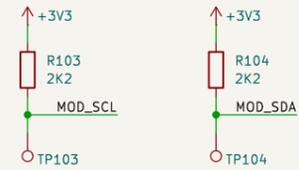


ESP32

I2C MAIN



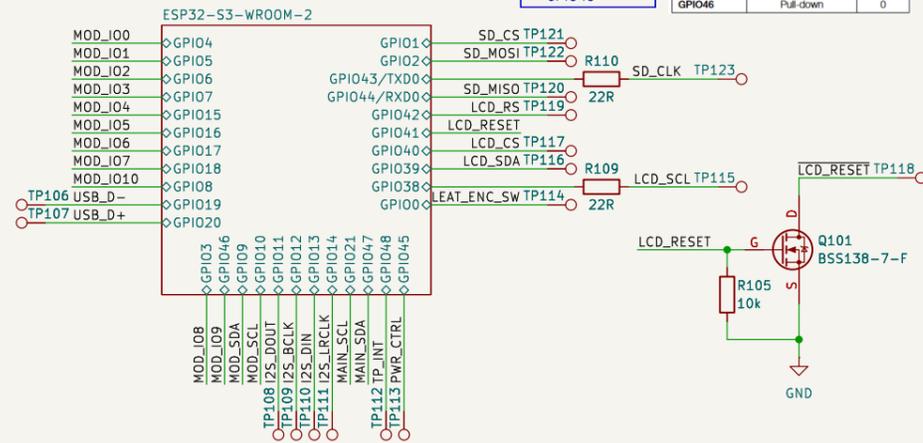
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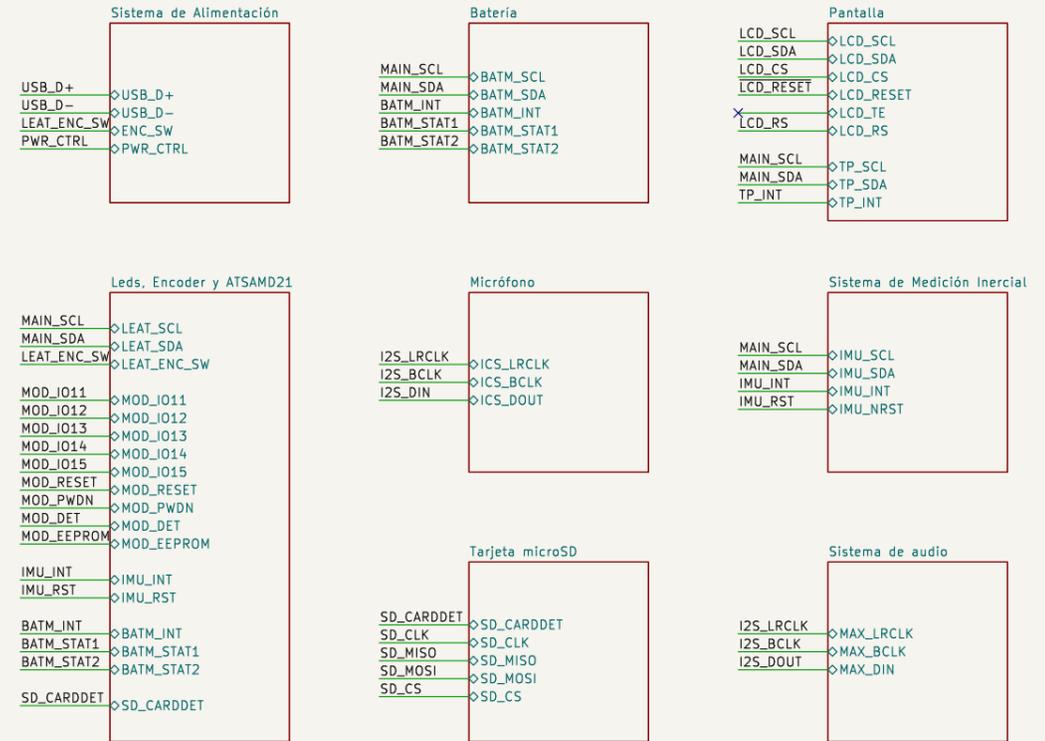
GPIO43 --> TXD0
GPIO44 --> RXD0

STRAPPING PINS
- GPIO0
- GPIO3
- GPIO45
- GPIO46

Strapping Pin	Default Configuration	Bit Value
GPIO0	Pull-up	1
GPIO3	Floating	-
GPIO45	Pull-down	0
GPIO46	Pull-down	0

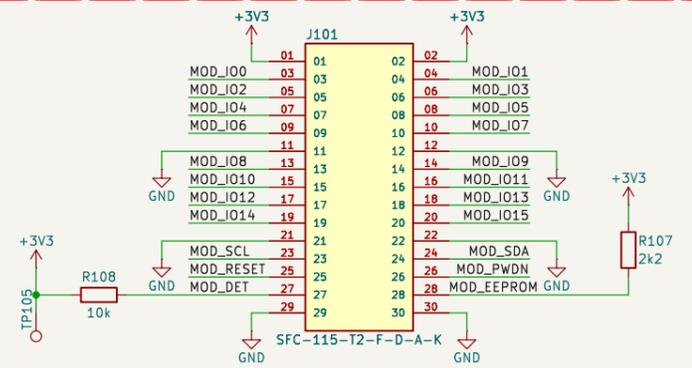


COMPONENTS

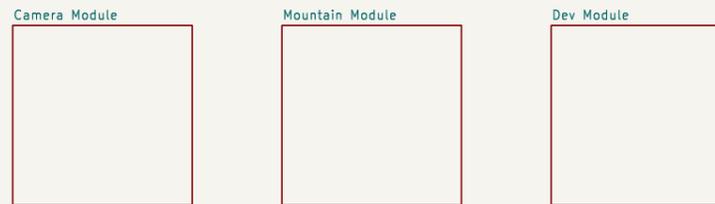


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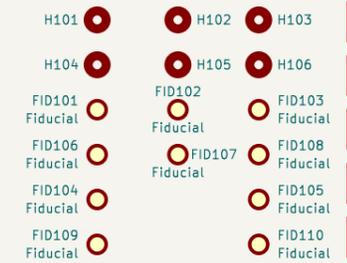
MODULE CONNECTOR



COMPONENTS



MISCELLANEOUS



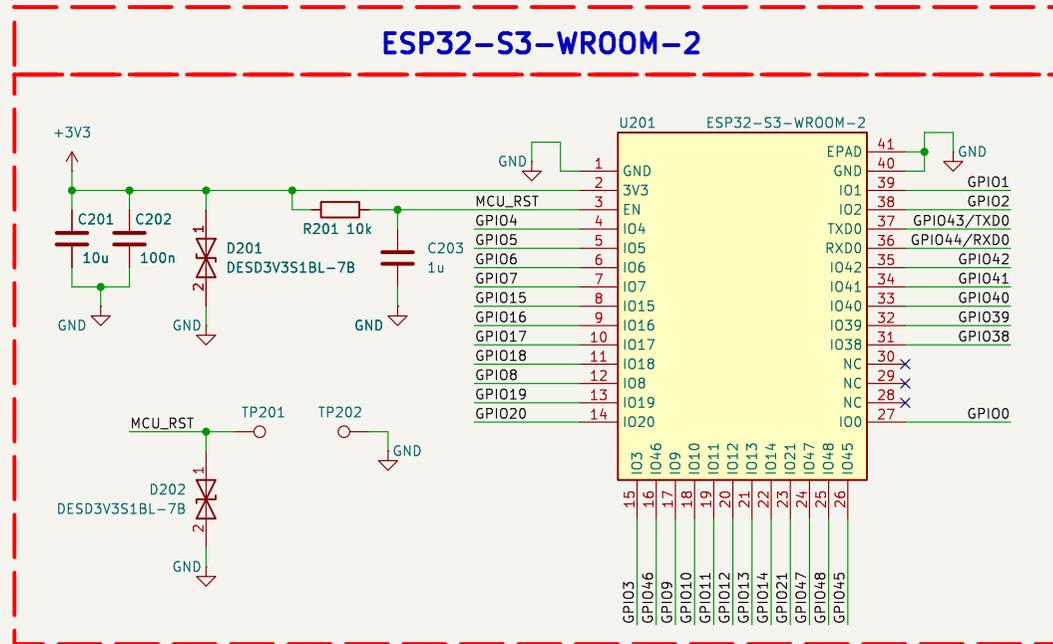
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KiCad E.D.A. 9.0.0

Rev: Final
Id: 1/13

ESP32-S3-WROOM-2



GPIO0	◇	GPIO00
GPIO1	◇	GPIO01
GPIO2	◇	GPIO02
GPIO3	◇	GPIO03
GPIO4	◇	GPIO04
GPIO5	◇	GPIO05
GPIO6	◇	GPIO06
GPIO7	◇	GPIO07
GPIO8	◇	GPIO08
GPIO9	◇	GPIO09
GPIO10	◇	GPIO10
GPIO11	◇	GPIO11
GPIO12	◇	GPIO12
GPIO13	◇	GPIO13
GPIO14	◇	GPIO14
GPIO15	◇	GPIO15
GPIO16	◇	GPIO16
GPIO17	◇	GPIO17
GPIO18	◇	GPIO18
GPIO19	◇	GPIO19
GPIO20	◇	GPIO20
GPIO21	◇	GPIO21
GPIO38	◇	GPIO38
GPIO39	◇	GPIO39
GPIO40	◇	GPIO40
GPIO41	◇	GPIO41
GPIO42	◇	GPIO42
GPIO43/TXD0	◇	GPIO43/TXD0
GPIO44/RXD0	◇	GPIO44/RXD0
GPIO45	◇	GPIO45
GPIO46	◇	GPIO46
GPIO47	◇	GPIO47
GPIO48	◇	GPIO48

MQuero

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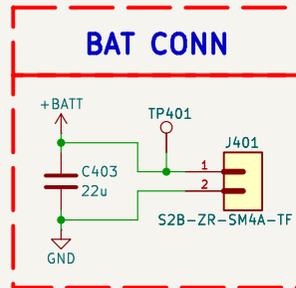
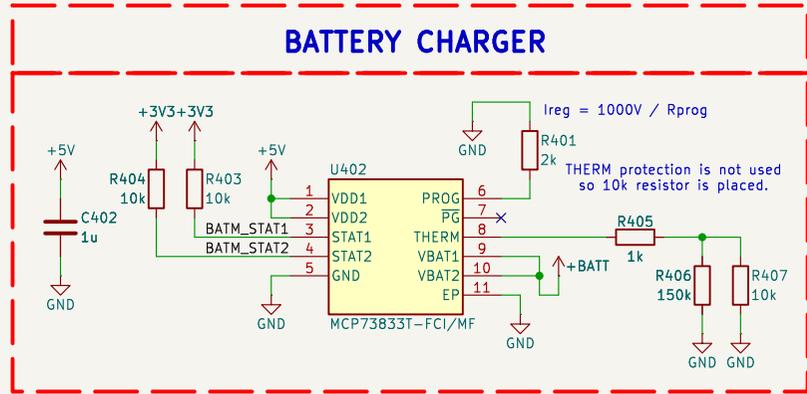
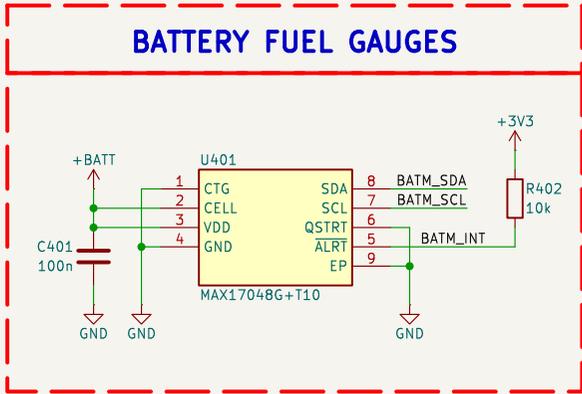
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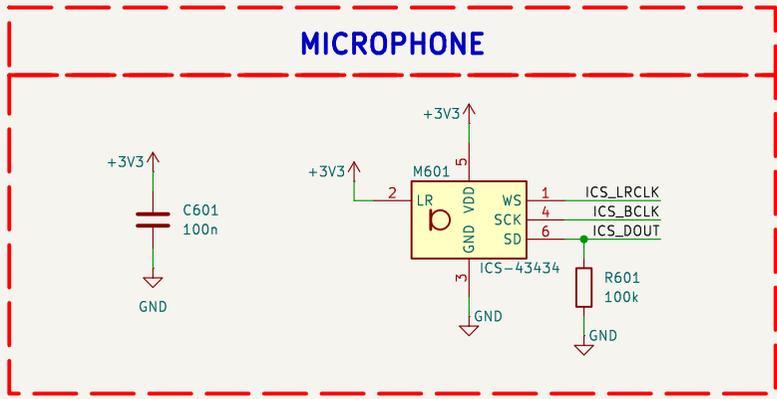
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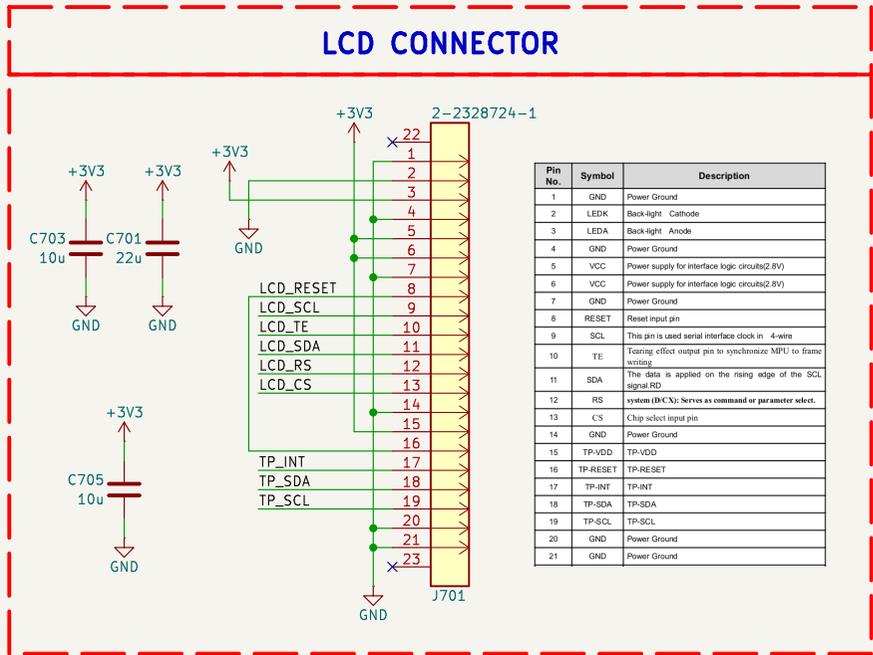
BATM_SCL ↗ BATM_SCL
 BATM_SDA ↗ BATM_SDA
 BATM_INT ↗ BATM_INT
 BATM_STAT1 ↗ BATM_STAT1
 BATM_STAT2 ↗ BATM_STAT2

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Size: A4	Date: 2024-07-18
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ICS_LRCLK ⇨ ICS_LRCLK
 ICS_BCLK ⇨ ICS_BCLK
 ICS_DOUT ⇨ ICS_DOUT

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Size: A4	Date: 2024-07-18	Rev: Final
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Pin No.	Symbol	Description
1	GND	Power Ground
2	LEDK	Back-light Cathode
3	LEDA	Back-light Anode
4	GND	Power Ground
5	VCC	Power supply for interface logic: circuits(2.8V)
6	VCC	Power supply for interface logic: circuits(2.8V)
7	GND	Power Ground
8	RESET	Reset input pin
9	SCL	This pin is used serial interface clock in 4-wire
10	TE	Tearing effect output pin to synchronize MPU to frame writing
11	SDA	The data is applied on the rising edge of the SCL signal RD
12	RS	system (D/C): Serves as command or parameter select.
13	CS	Chip select input pin
14	GND	Power Ground
15	TP-VDD	TP-VDD
16	TP-RESET	TP-RESET
17	TP-INT	TP-INT
18	TP-SDA	TP-SDA
19	TP-SCL	TP-SCL
20	GND	Power Ground
21	GND	Power Ground

LCD_SCL ◇ LCD_SCL
 LCD_SDA ◇ LCD_SDA
 LCD_CS ◇ LCD_CS
 LCD_RESET ◇ LCD_RESET
 LCD_TE ◇ LCD_TE
 LCD_RS ◇ LCD_RS

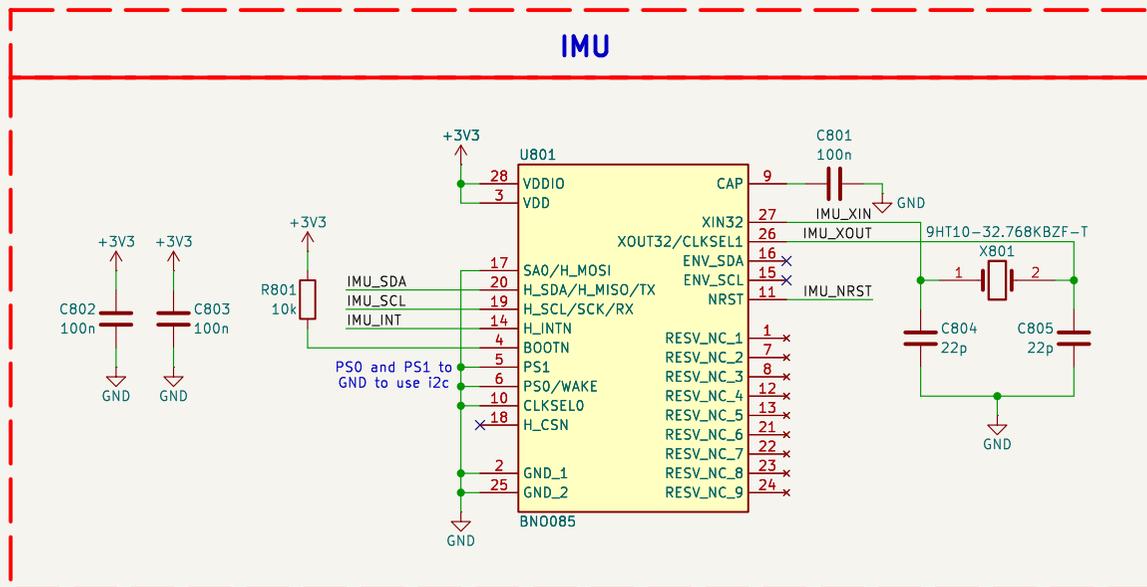
TP_SCL ◇ TP_SCL
 TP_SDA ◇ TP_SDA
 TP_INT ◇ TP_INT

MQuero

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Size: A4	Date: 2024-07-18	Rev: Final
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IMU_SCL ◊ IMU_SCL
 IMU_SDA ◊ IMU_SDA
 IMU_INT ◊ IMU_INT
 IMU_Nrst ◊ IMU_Nrst

MQuero

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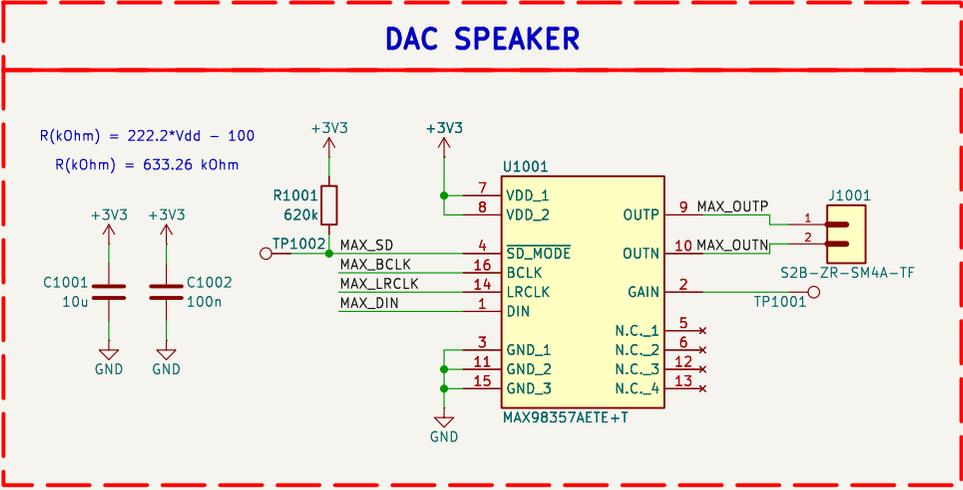
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Size: A4 Date: 2024-07-18

KiCad E.D.A. 9.0.0

Rev: Final

Id: 8/13



MAX_BCLK ⇨ MAX_BCLK
 MAX_LRCLK ⇨ MAX_LRCLK
 MAX_DIN ⇨ MAX_DIN

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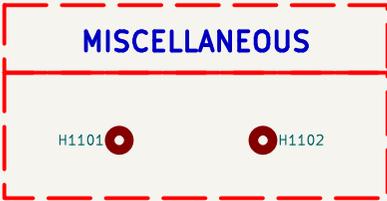
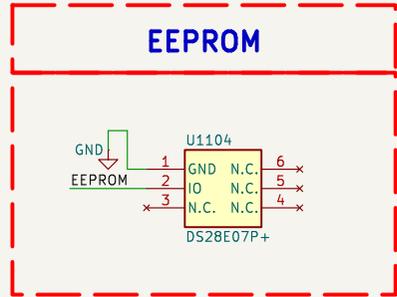
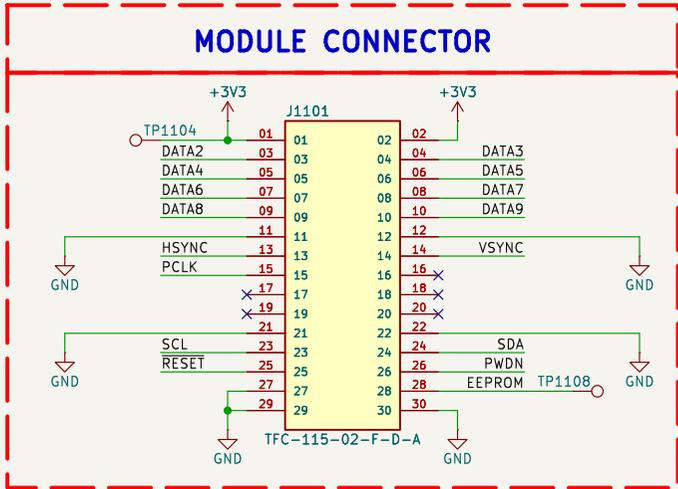
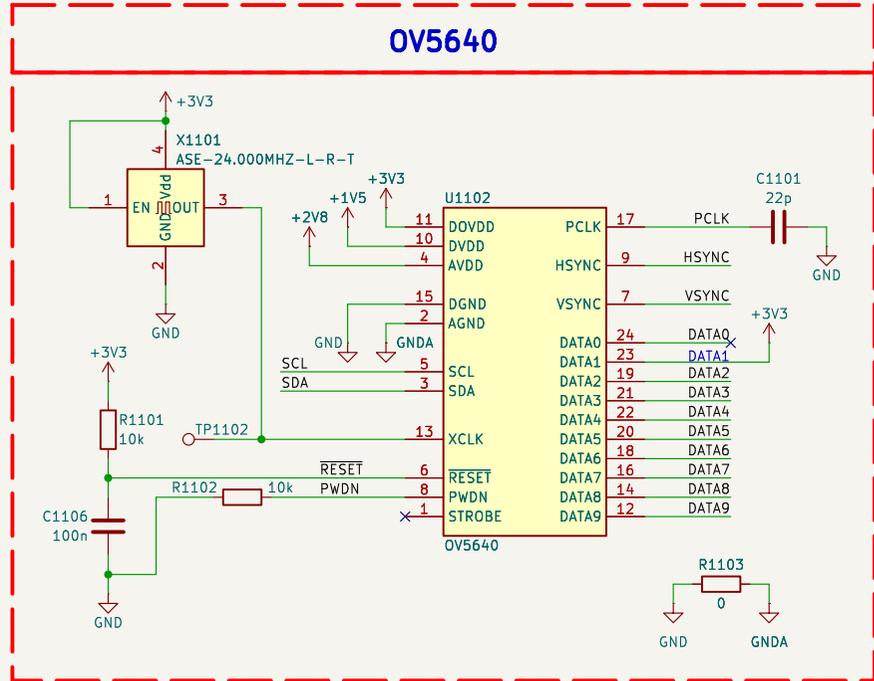
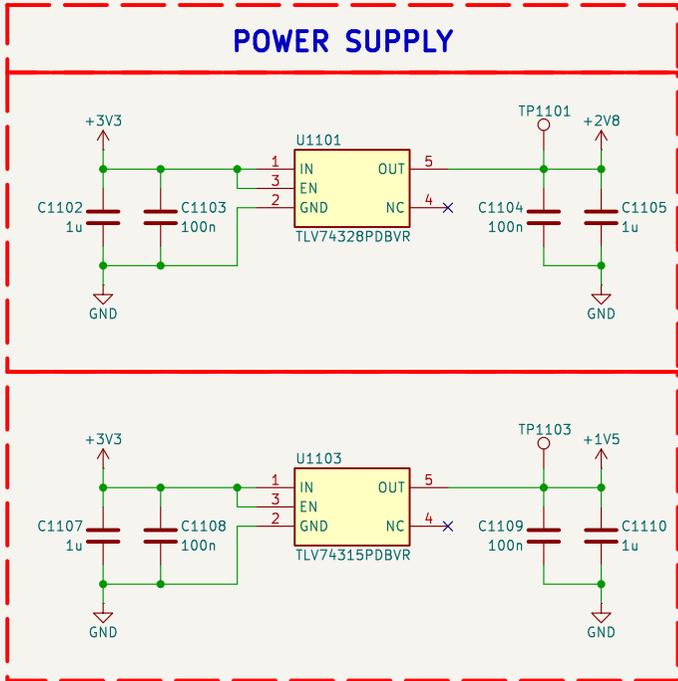
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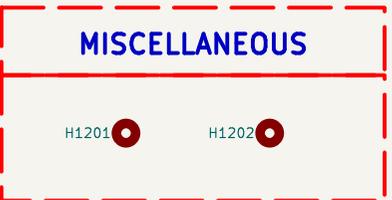
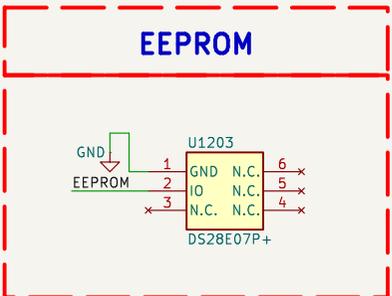
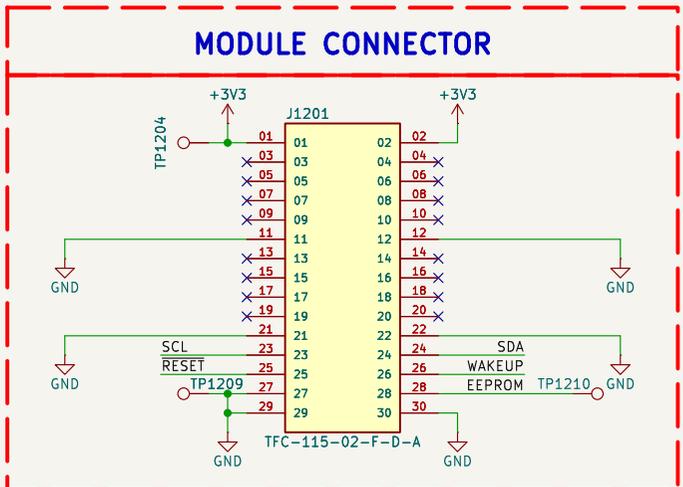
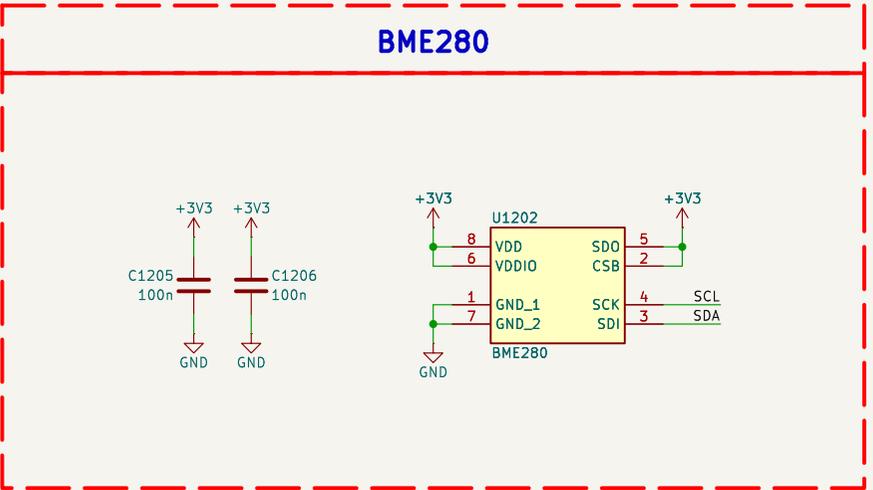
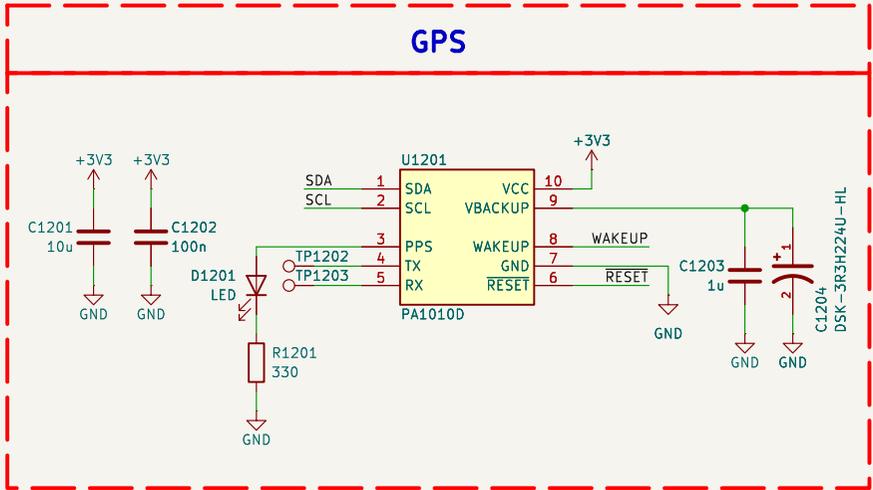
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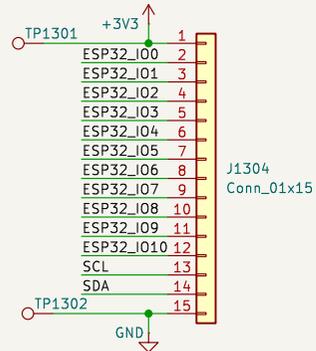


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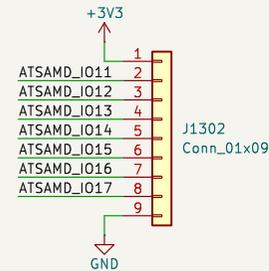


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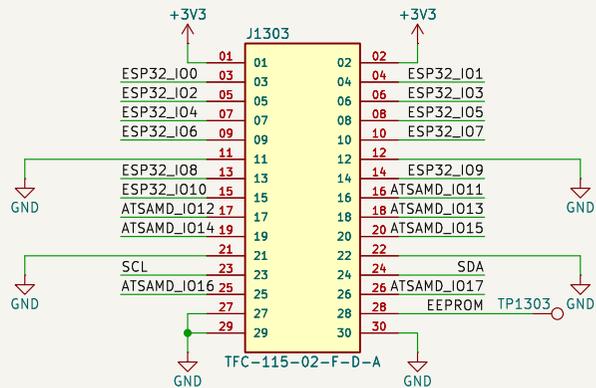
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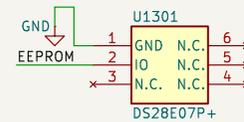
ATSAMD21G18 GPIOs



MODULE CONNECTOR



EEPROM



MISCELLANEOUS



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Date: 2024-07-18

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Rev: Final

Id: 13/13