



Choose power supply wisely:  
 A) U2,C2,C3 populated, JP1 open  
 -> VCC from VCOM via LDO  
 B) U2,C2,C3 not present, JP1 closed  
 -> VCC == +3V0 from mainboard

Include in BOM, to have 3V3 LDO in case 3V0 is not enough.

Include in BOM, to not forget to order it.

J1 CF\_EXP\_LEFT

VCC 3V	1	TXD
PC11 RX1	2	RXD
PC10 TX1	3	SDA
PB7 SDA	4	SCL
PB6 SCL	5	IO1_ESP_EN
PB8 IO_1	6	IO2_ESP
PB5 IO_2	7	IO3_ESP
PB4 IO_3	8	IO2_ESP_CS
PC12 IO_4	9	
GND	10	

J2 CF\_EXP\_RIGHT

TX2 PA2	1
RX2 PA3	2
SCLK PA5	3
MISO PA6	4
MOSI PA7	5
IO2 P0.11	6
IO1 P0.12	7
OW P0.08	8
VCOM	9
VUSB	10

Voltage dividers for the transmitting data lines, in case VCC voltage is too high for other decks. Upper R is 0R/10R if not required.

EXTRA1  
**SMD Solder Paste**  
 ChipQuik TS991SNL35T3  
 Sn96.5/Ag03/Cu0.5  
 Thermally Stable No-Clean

<https://github.com/mrxllone/Crazyflie-ESP32-Deck>  
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**Marcell Varadi**  
 Sheet: /  
 File: Crazyflie\_ESP32\_Deck.kicad\_sch

**Title: Crazyflie – ESP32 deck**

Size: A4	Date: 2026-02-04
KiCad E.D.A. 9.0.0	Rev: 1 Id: 1/1