

CM5_NAS

2026-02-13
Rev 6

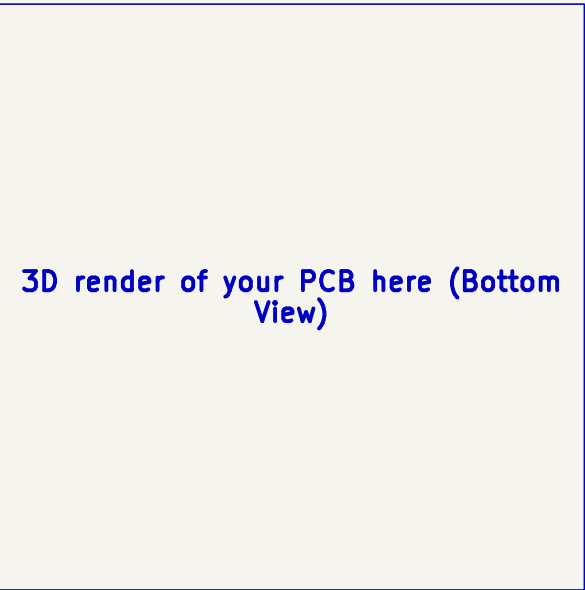
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References	
Github CM5_NAS	
CM4-Nvme-NAS	
	
	
	
	
	
	
	
	

TOP VIEW



BOTTOM VIEW



NOTES

Add a comment here

Not fitted components are marked as **X**

DRAFT - Very early stage of schematic, ignore details.
PRELIMINARY - Close to final schematic.
CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.
RELEASED - A board with this schematic has been sent to production.

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

Block Diagram
File: Block Diagram.kicad_sch

Revision History
File: Revision History.kicad_sch

Project Architecture
File: Project Architecture.kicad_sch

Sheet: /
File: CM5_Nas.kicad_sch

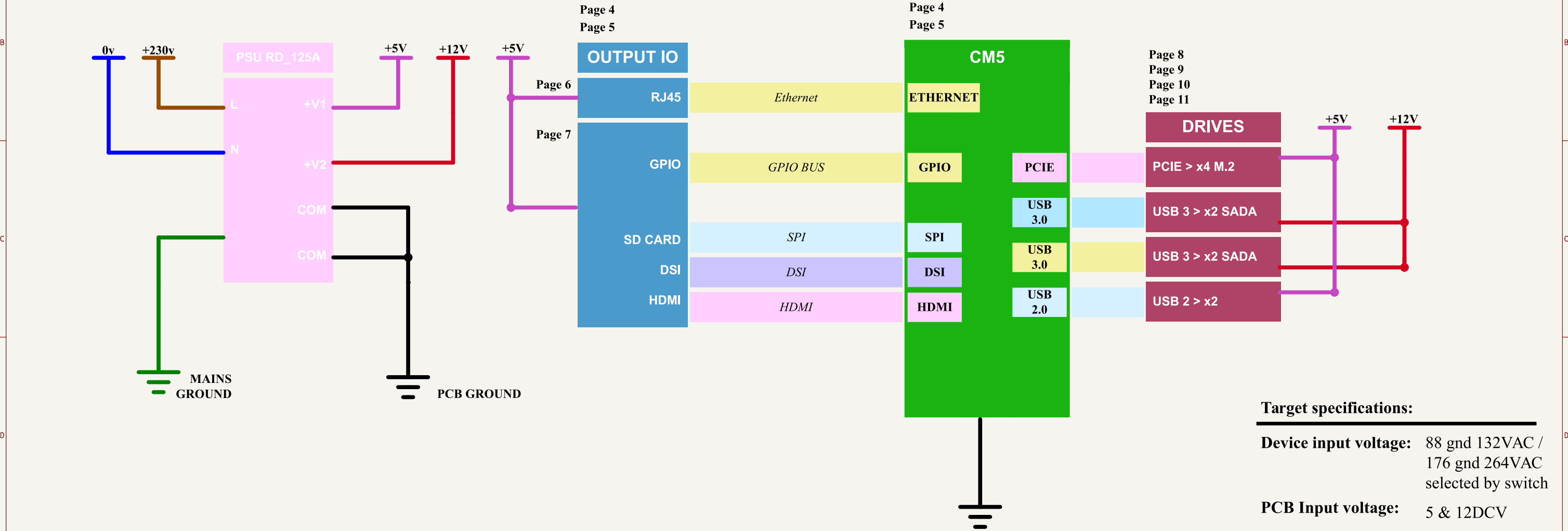
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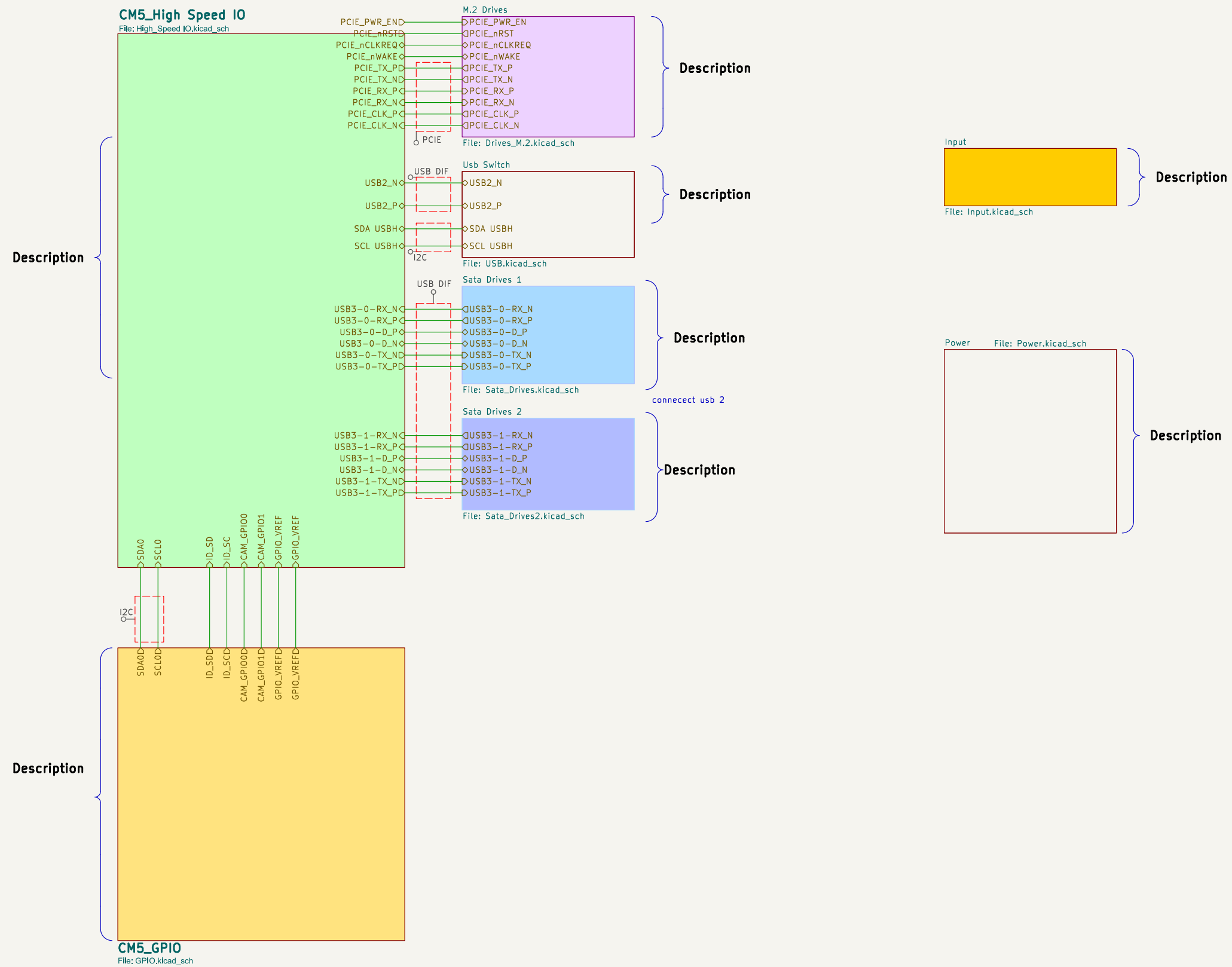
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[2] Block Diagram

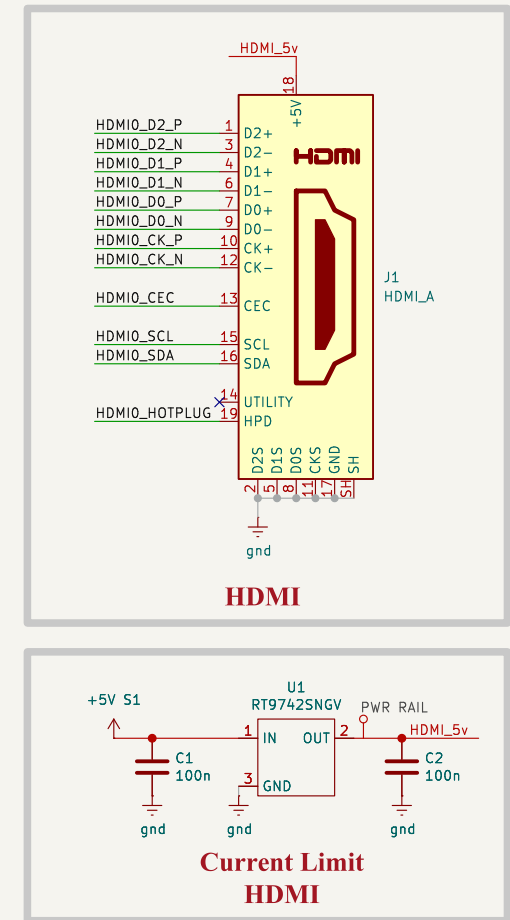
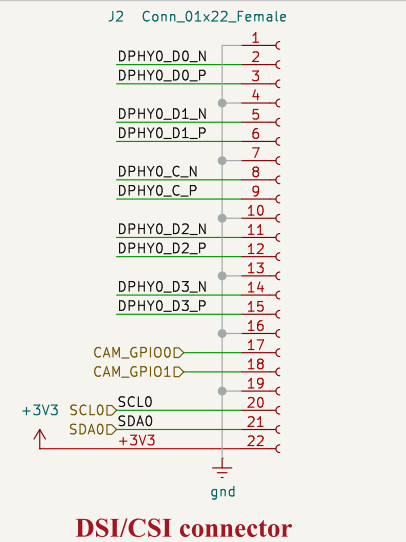
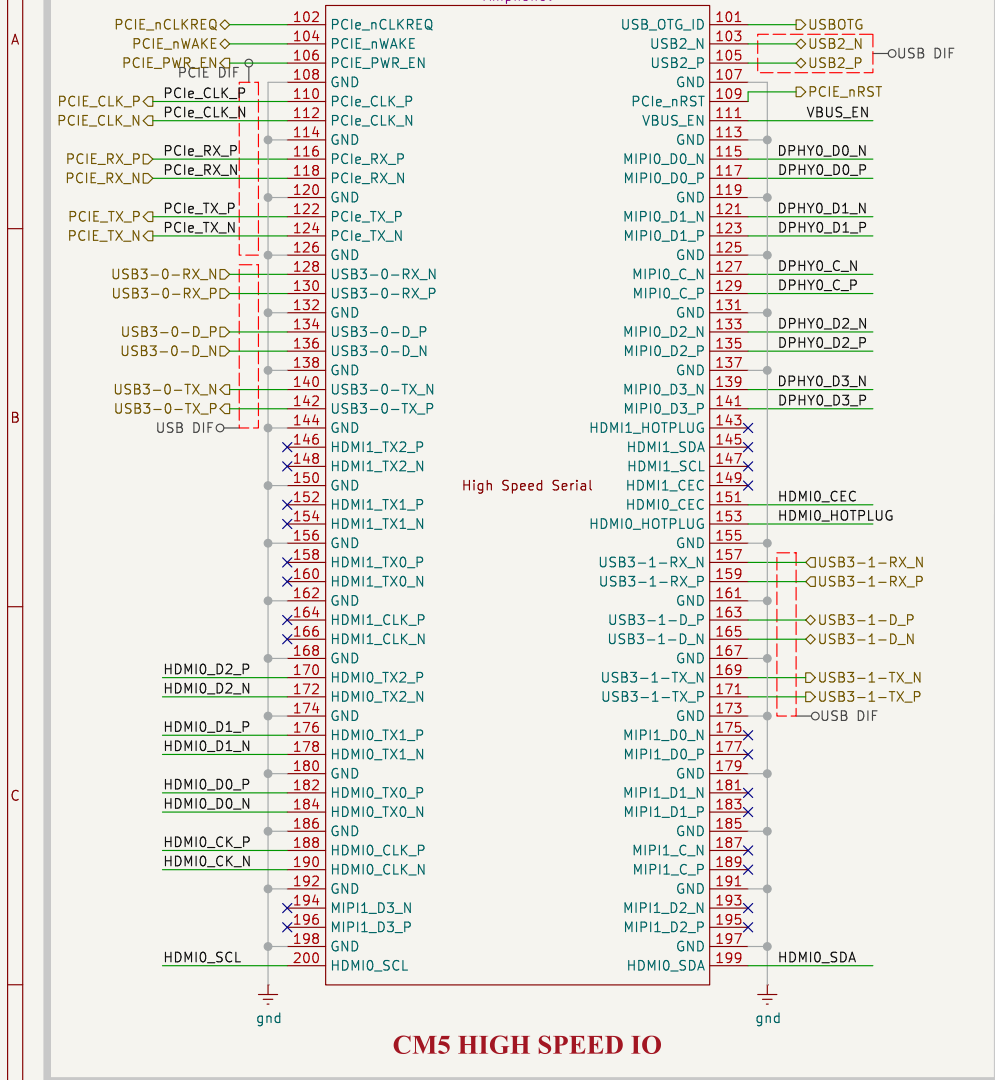


Target specifications:	
Device input voltage:	88 gnd 132VAC / 176 gnd 264VAC selected by switch
PCB Input voltage:	5 & 12DCV
Spec 2	5V 7.7A RANGE OF 0-12A
Spec 3	12V 7.7A RANGE OF 0-10A

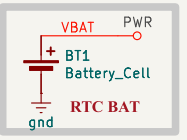
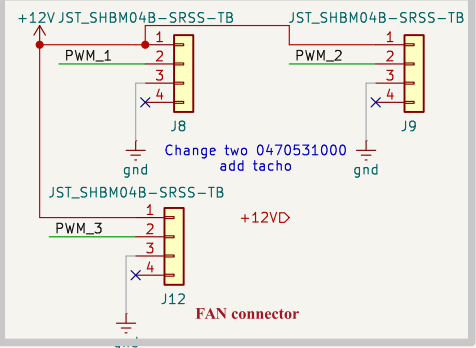
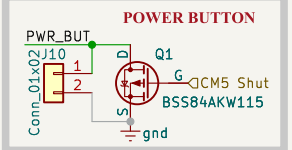
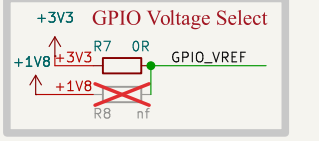
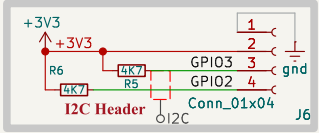
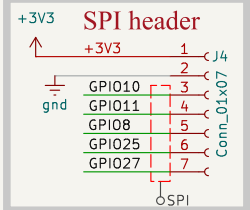
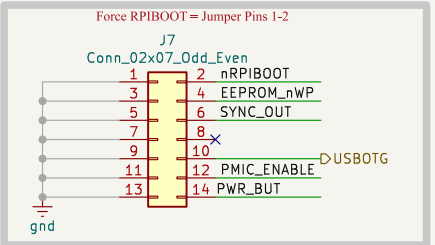
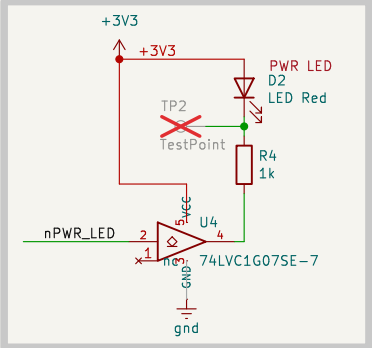
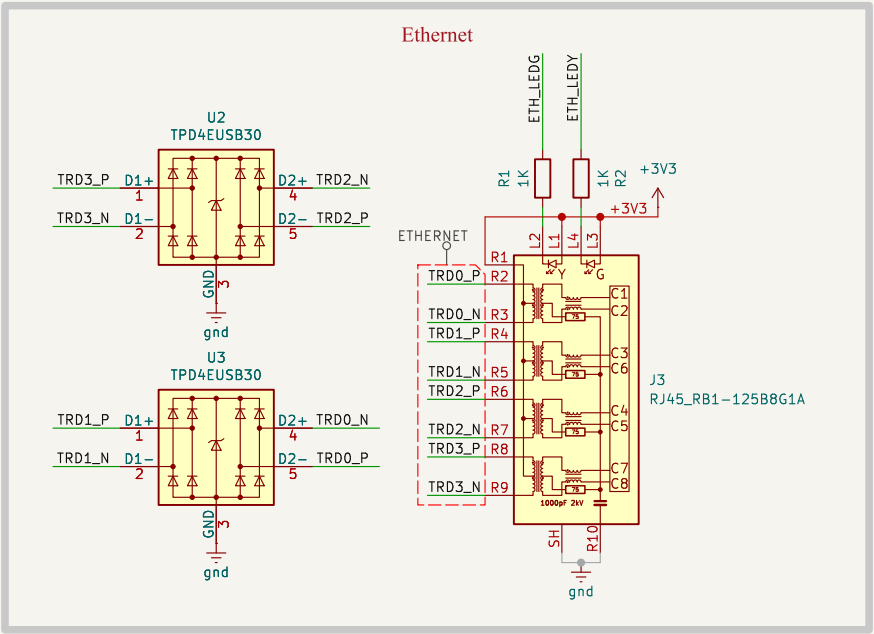
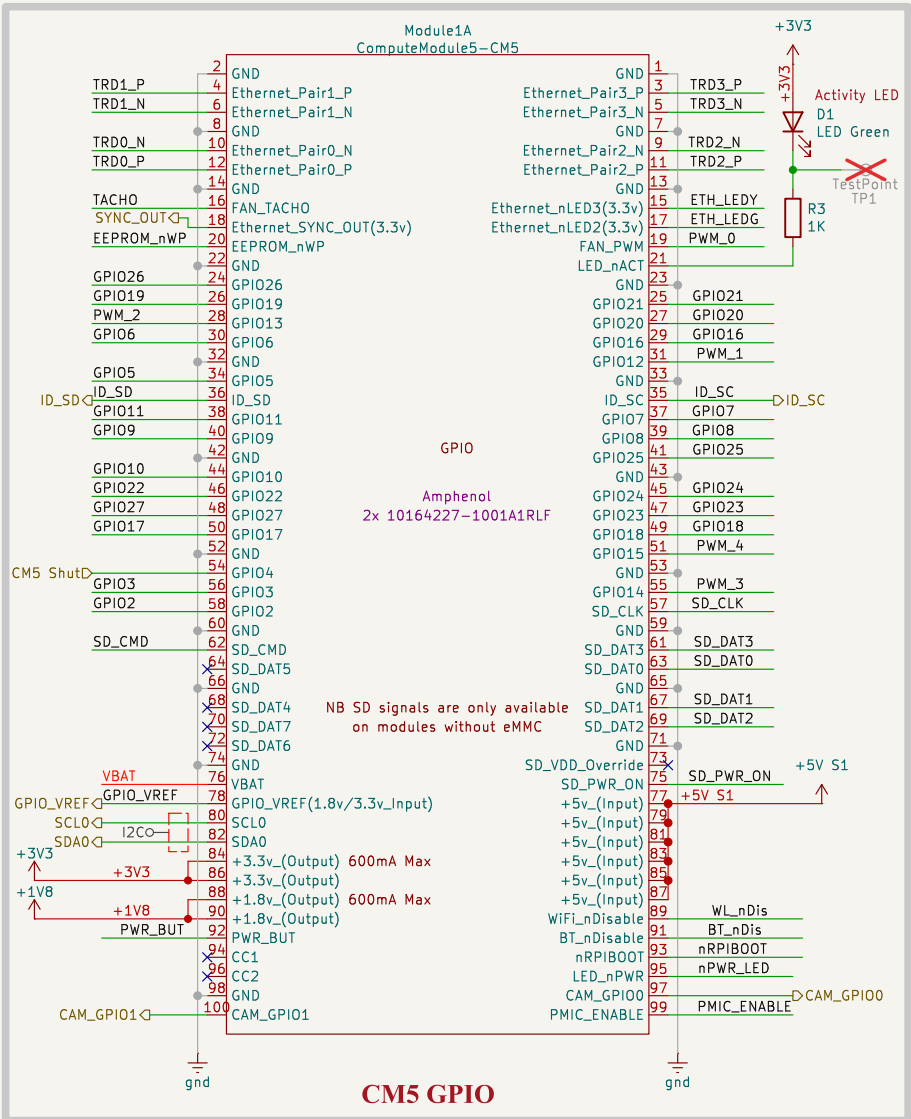
[3] Project Architecture

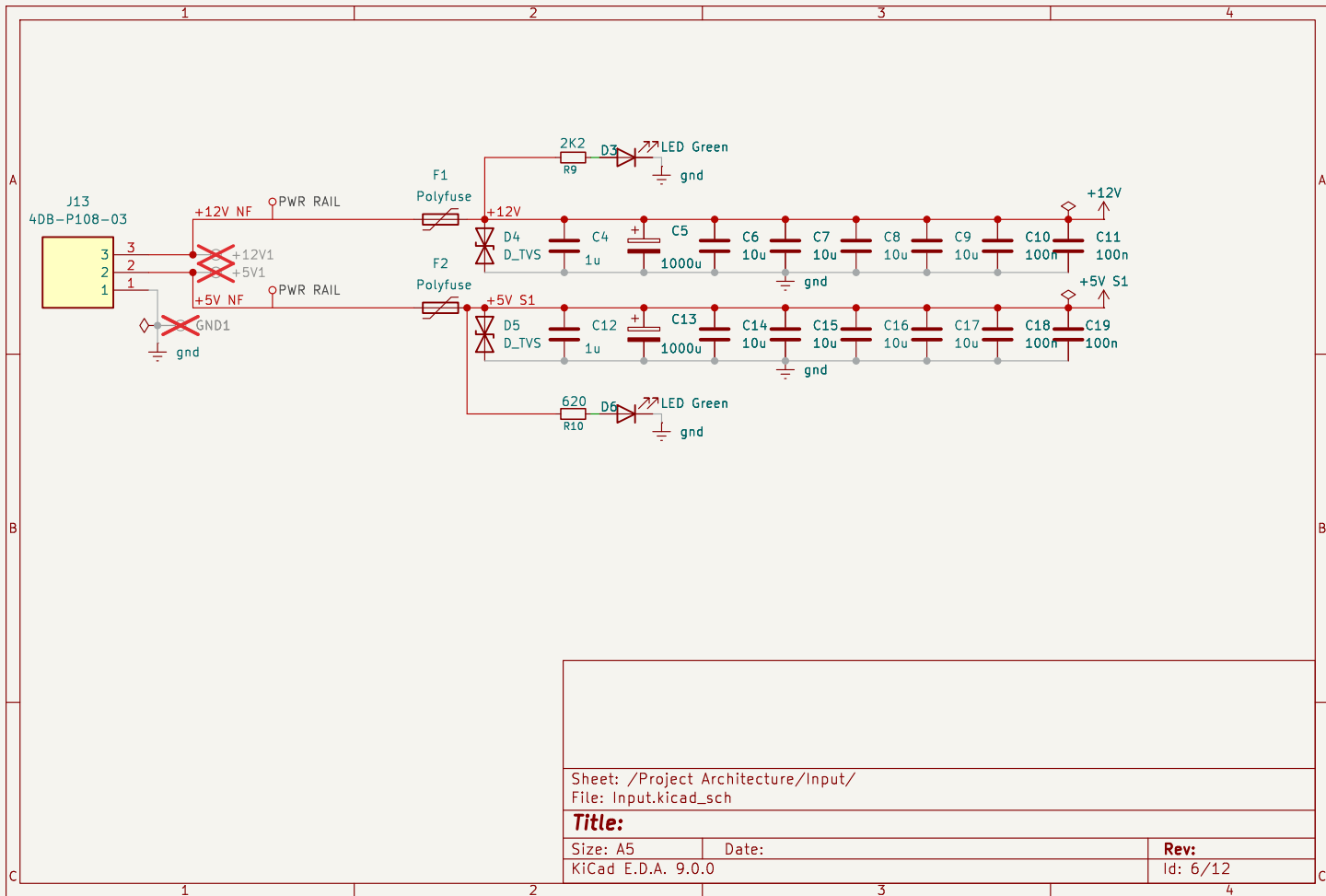


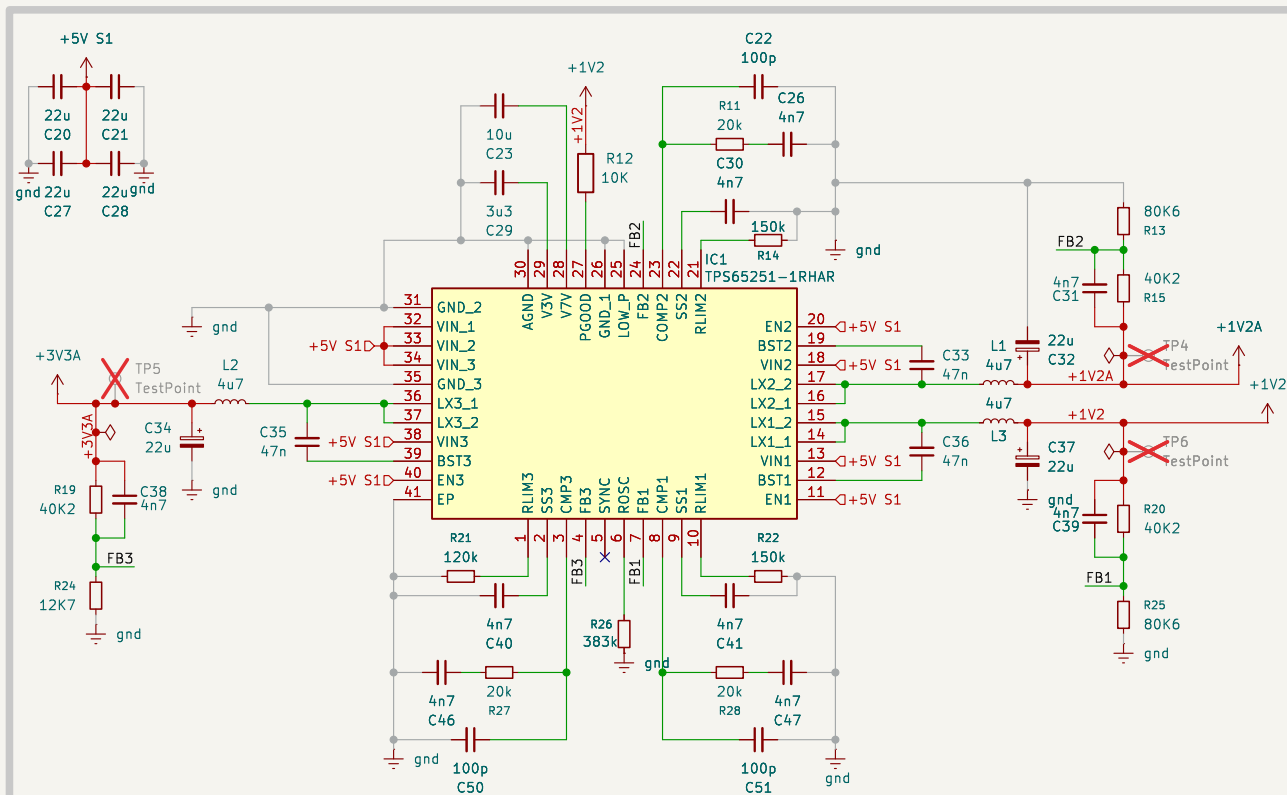
[4] CM5_High Speed



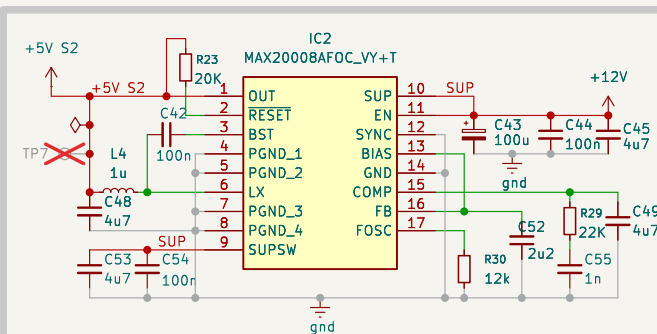
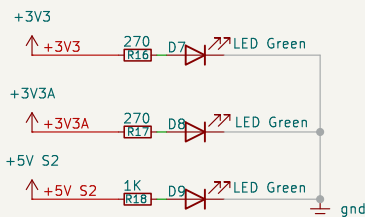
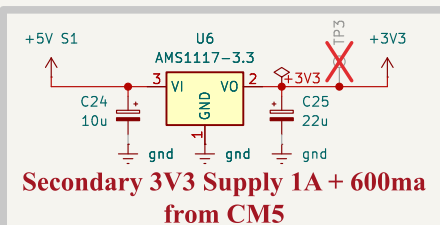
[5] CM5_GPIO







Analog & Digital Supply



Sata SSD Supply

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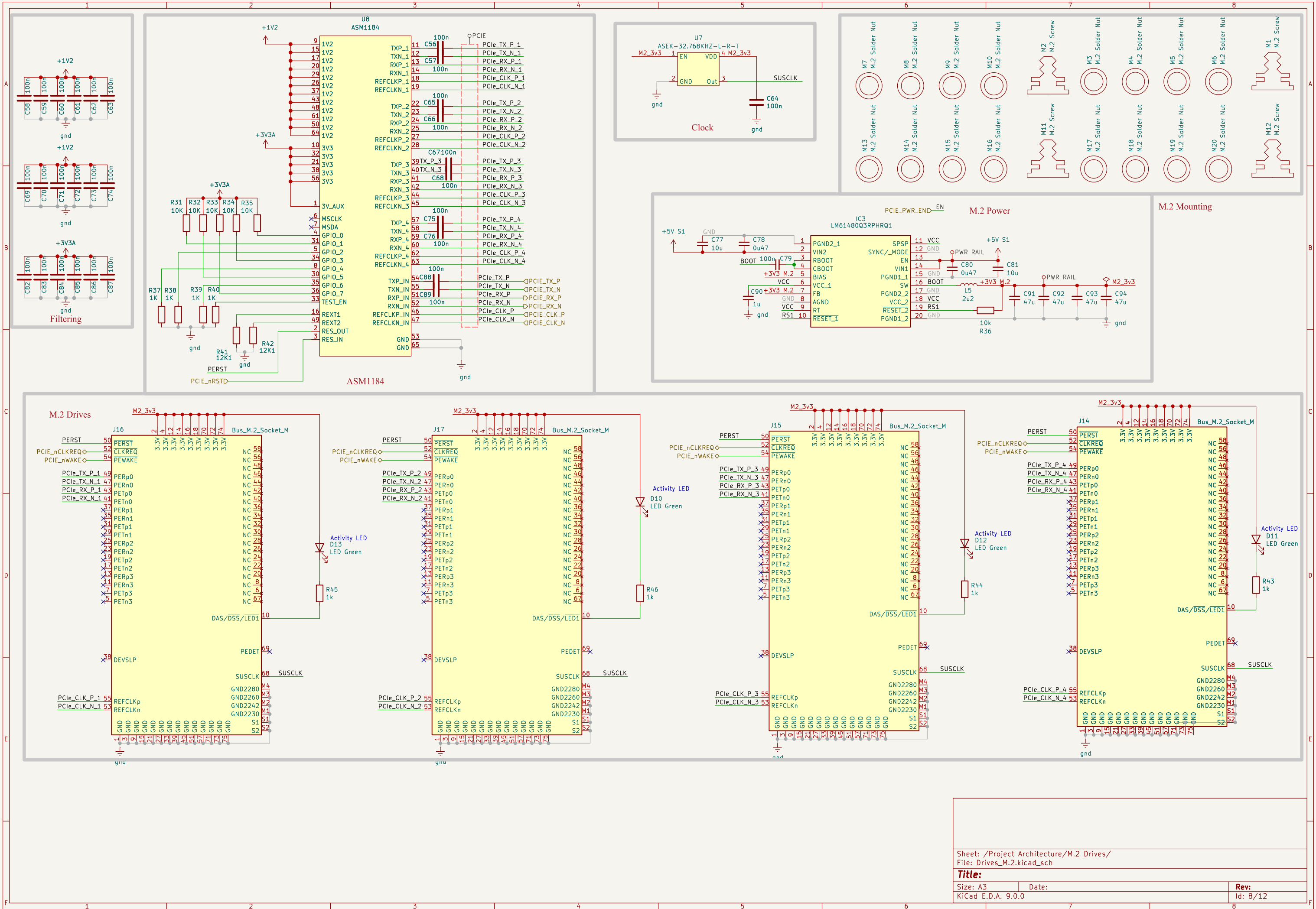
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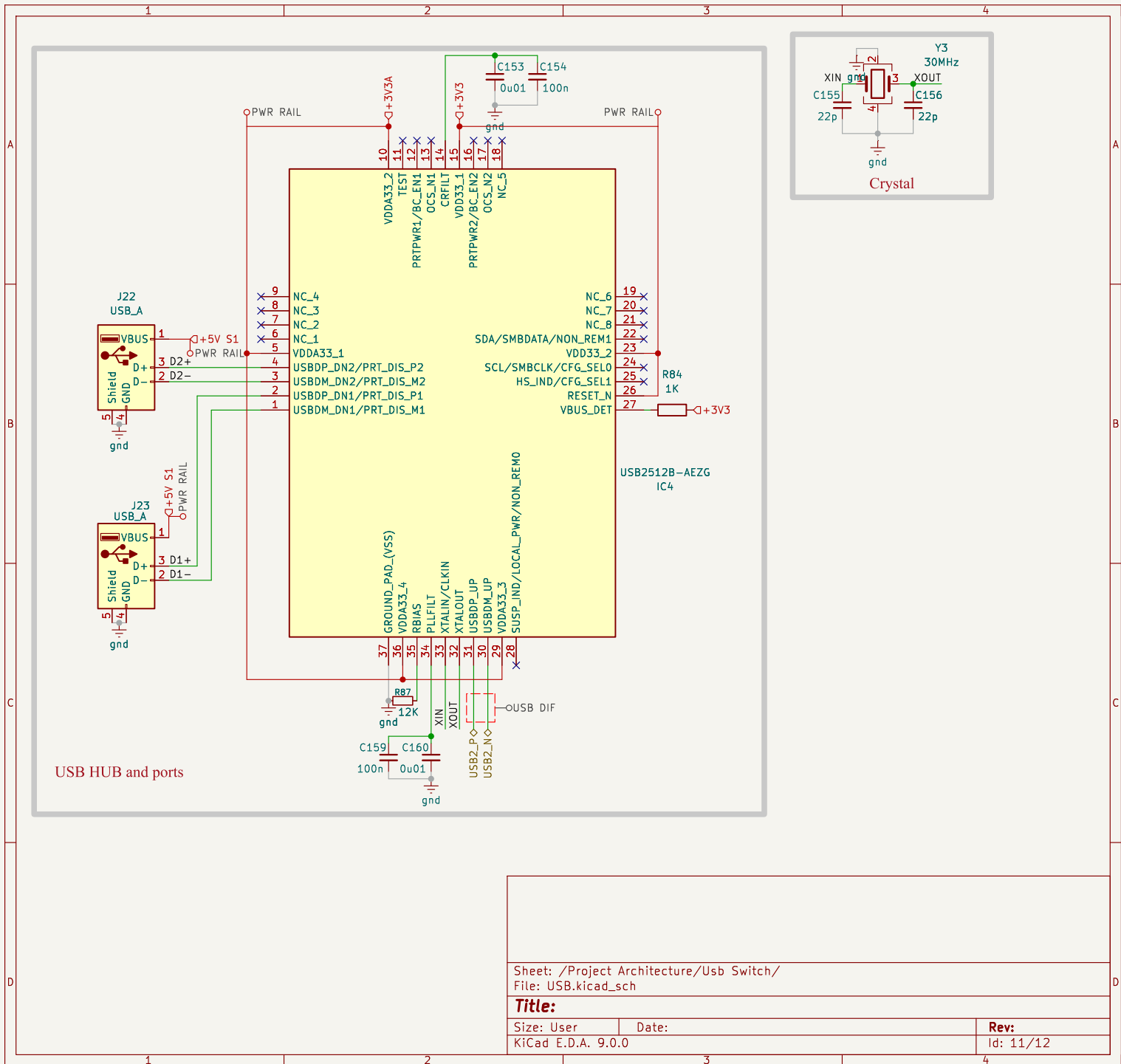
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[13] Revision History

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