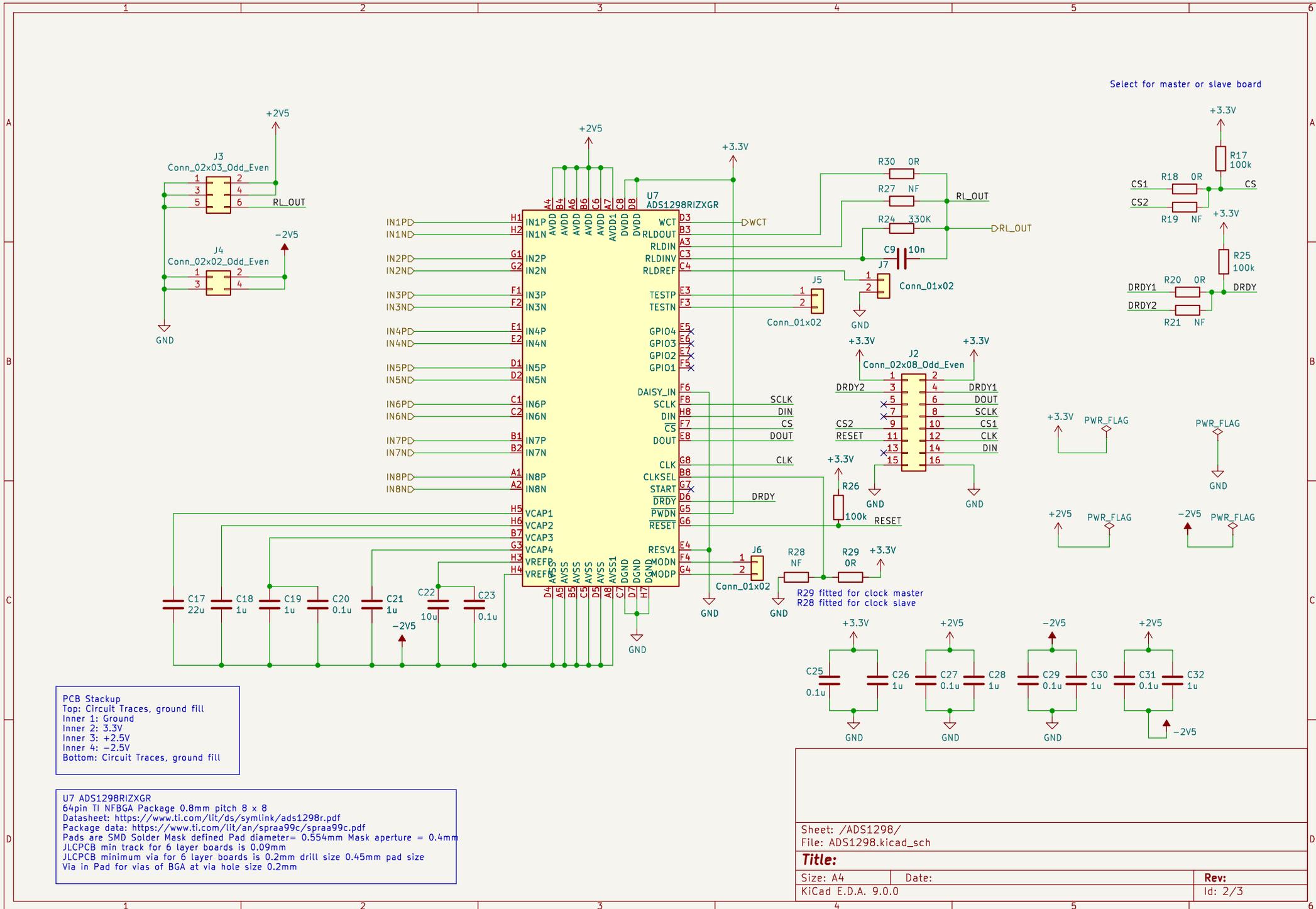


Symbol: OSHW-Logo2\_33mm\_SilkScreen

Sheet: /		
File: OpenBCL_ESP32_ADS1298.kicad_sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.0		Id: 1/3



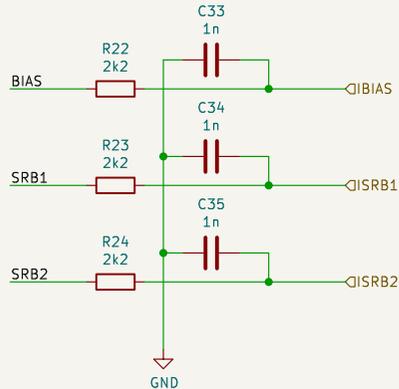
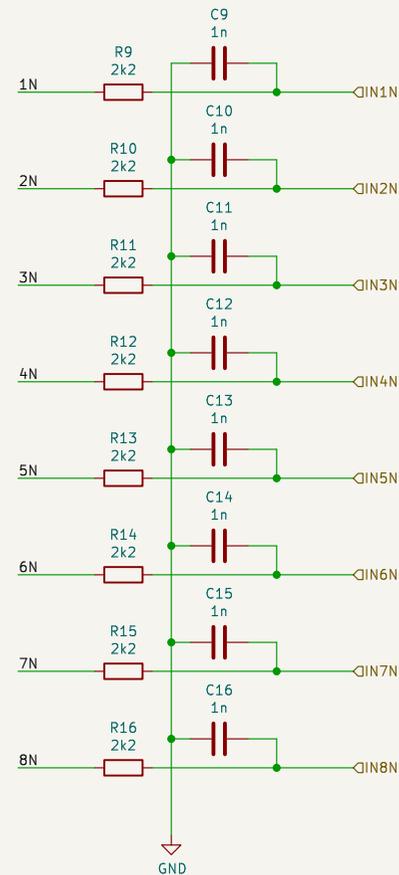
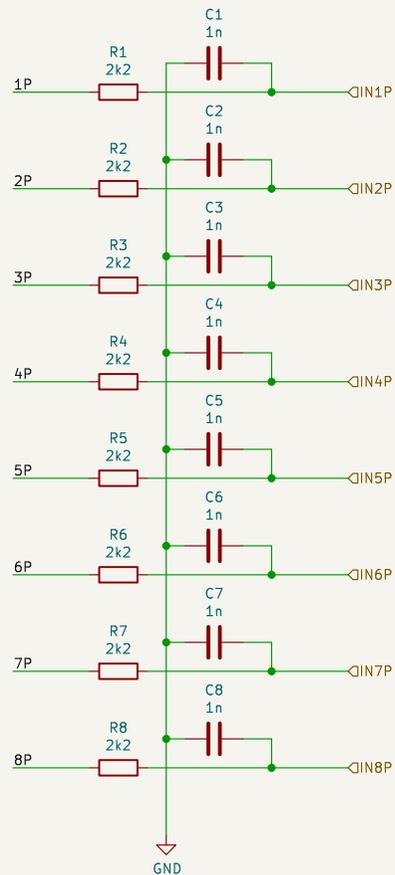
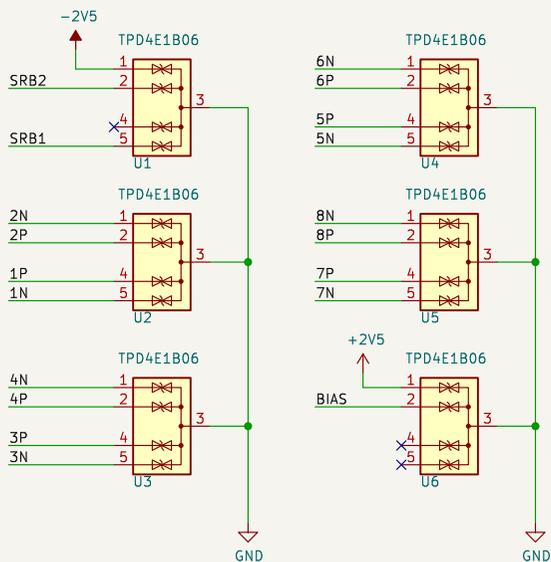
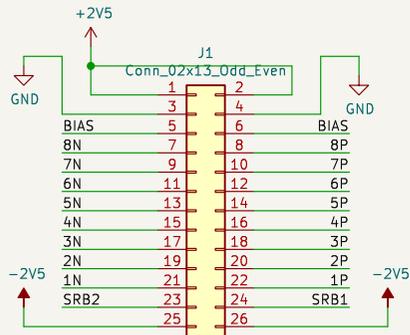
Select for master or slave board

PCB Stackup  
 Top: Circuit Traces, ground fill  
 Inner 1: Ground  
 Inner 2: 3.3V  
 Inner 3: +2.5V  
 Inner 4: -2.5V  
 Bottom: Circuit Traces, ground fill

U7 ADS1298RIZXGR  
 64pin TI NFBGA Package 0.8mm pitch 8 x 8  
 Datasheet: <https://www.ti.com/lit/ds/symlink/ads1298r.pdf>  
 Package data: <https://www.ti.com/lit/an/spraa99c/spraa99c.pdf>  
 Pads are SMD Solder Mask defined Pad diameter= 0.554mm Mask aperture = 0.4mm  
 JLCPCB min track for 6 layer boards is 0.09mm  
 JLCPCB minimum via for 6 layer boards is 0.2mm drill size 0.45mm pad size  
 Via in Pad for vias of BGA at via hole size 0.2mm

Sheet: /ADS1298/  
 File: ADS1298.kicad\_sch

<b>Title:</b>		<b>Rev:</b>	
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Sheet: /Analog\_Input/  
File: Analog\_Input.kicad\_sch

**Title:**

Size: A4  
KiCad E.D.A. 9.0.0

Date:

Rev:  
Id: 3/3