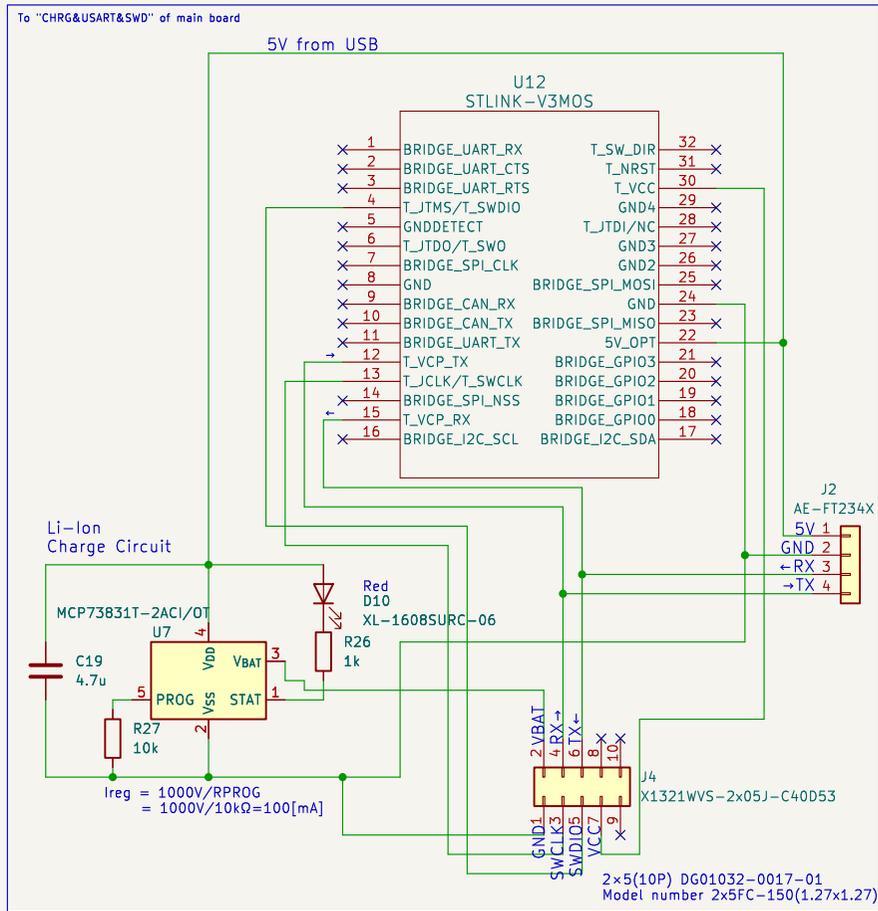


**STM32F413**  
 Set to output in BOOT mode:  
 PA9 (USART1-TX) , PD5 (USART2-TX) , PB10 (USART3-TX) , PB13 (CAN2-TX) ,  
 PA6 (SPI1-MISO) , PC11 (SPI3-MISO) , PE13 (SPI4-MISO) , PA11,12 (USB)  
 BOOT mode will not function as intended unless it is maintained at an appropriate level during BOOT mode:  
 PA10 (USART1-RX) , PD6 (USART2-RX) , PB11 (USART3-RX) , PB5 (CAN2-RX) ,  
 PA5 (SPI1-SCK) , PC10 (SPI3-SCK) , PE12 (SPI4-SCK)

**STM32F411**  
 Set to output in BOOT mode:  
 PA6 (SPI1-MISO) , PB14 (SPI2-MISO) , PC11 (SPI3-MISO) PA11,12 (USB) PA9 (USART1-TX) PD6 (USART2-TX)  
 BOOT mode will not function as intended unless it is maintained at an appropriate level during BOOT mode:  
 PA10 (USART1-RX) PD5 (USART2-RX) PA5 (SPI1-SCK) PB13 (SPI2-SCK) PC10 (SPI3-SCK)

**STM32 microcontroller system memory boot mode**  
[https://www.stmcu.jp/design/document/application\\_note/51328/](https://www.stmcu.jp/design/document/application_note/51328/)



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