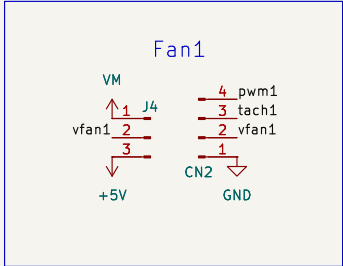
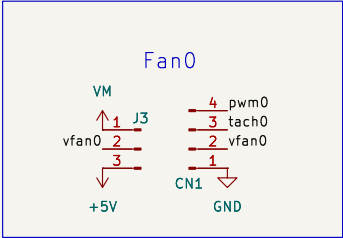


Headers

The diagram illustrates two 8-pin headers. The left header has pins labeled tach0, tach1, pwm0, and pwm1. The right header has pins labeled VM, J2, and GND. The connections are as follows:

Header	Pin	Label	Connection
Left Header	1	tach0	J1
	2	X	
	3	X	
	4	tach1	
	5	X	
	6	X	
	7	pwm0	
	8	pwm1	
Right Header	1	VM	↑
	2	J2	→ GND
	3	X	
	4	X	
	5	X	
	6	X	
	7	X	
	8		→ +5V



Sheet: /
File: tmtua.kicad_sch

Title: TMtua

Size: A5	Date: 2024-08-22
KiCad E.D.A. 9.0.0	

Rev: 0.1
Id: 1/1