

spudglo_v8p0_micro
 File: spudglo_v8p0_micro.kicad_sch

spudglo_v8p0_power
 File: spudglo_v8p0_power.kicad_sch

- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole

Sheet: /	
File: spudglo_driver_v8pWB55.kicad_sch	
Title:	
Size: A4	Date:
KiCad E.D.A. 9.0.0	Rev: Id: 1/3

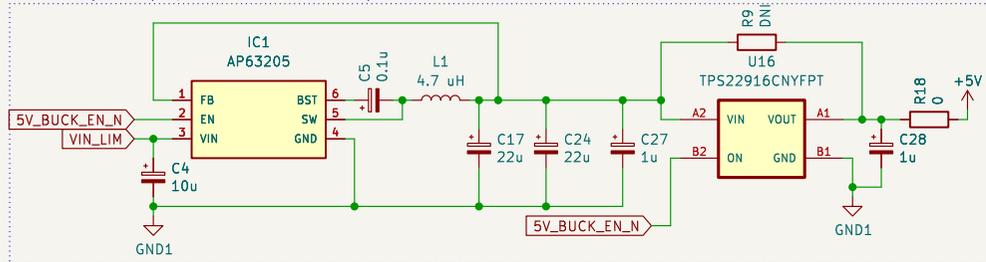
$VIN_UV = VUV_LO[R] * (R1 + R2 + R3) / (R2 + R3)$
 $VIN_UV = 1.2 * (768000 + 226000 + 100000) / (226000 + 100000)$
 $VIN_UV = 13.13\text{ V}$

$VIN_OV = VOV_R * (R1 + R2 + R3) / (R3)$
 $VIN_OV = 1.2 * (768000 + 226000 + 100000) / (100000)$
 $VIN_OV = 4.03\text{ V}$

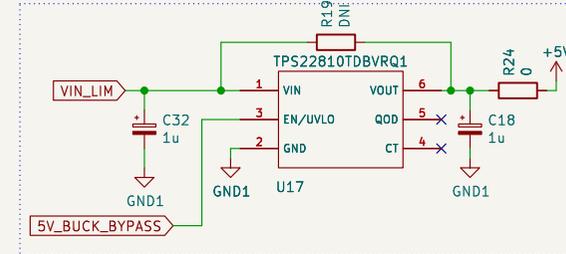
U11
 MMSZ5244BT1G



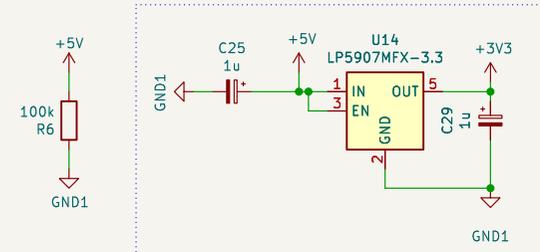
5V buck regulator (only enabled if VIN == 12 V)



VIN connection to 5V rail (only enabled if VIN == 5 V)



3.3 V micro domain



Sheet: /spudglo_v8p0_power/
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Title:

Size: A4
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Date:

Rev:
 Id: 4/3