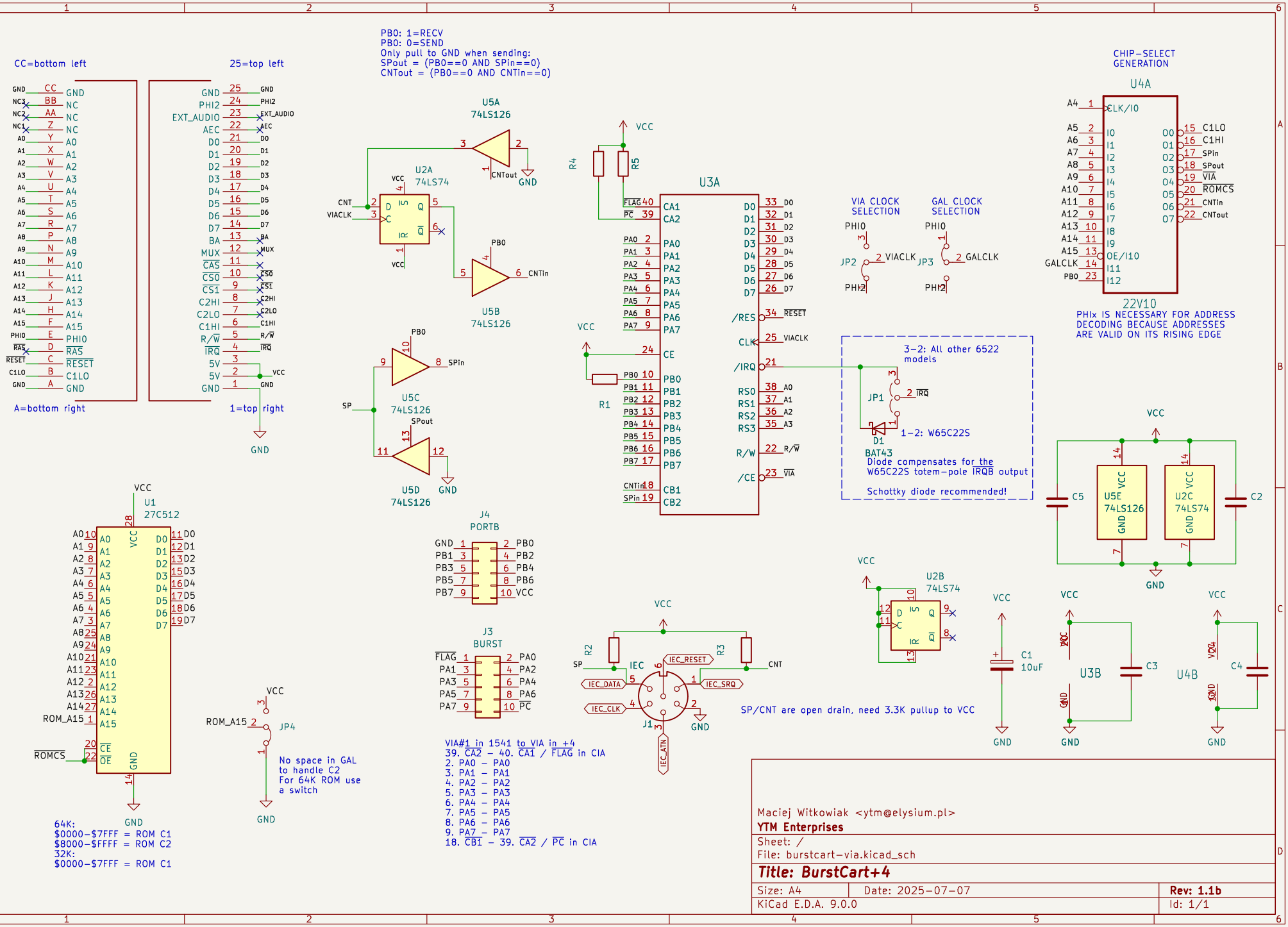
CC=bottom left
25=top left
A=bottom right
1=top right
PB0: 1=RECV
PB0: 0=SEND
Only pull to GND when sending:
SPout = (PB0==0 AND SPin==0)
CNTout = (PB0==0 AND CNTin==0)
U5A
74LS126
U2A
74LS74
U5B
74LS126
U5C
74LS126
U5D
74LS126
U3A
VIA CLOCK SELECTION
GAL CLOCK SELECTION
CHIP-SELECT GENERATION
U4A
22V10
PHIx IS NECESSARY FOR ADDRESS DECODING BECAUSE ADDRESSES ARE VALID ON ITS RISING EDGE
3-2: All other 6522 models
1-2: W65C22S
BAT43
Diode compensates for the W65C22S totem-pole IRQB output
Schottky diode recommended!
U1
27C512
64K:
$0000-$7FFF = ROM C1
$8000-$FFFF = ROM C2
32K:
$0000-$7FFF = ROM C1
No space in GAL to handle C2
For 64K ROM use a switch
J4
PORTB
J3
BURST
VIA#1 in 1541 to VIA in +4
39. CA2 - 40. CA1 / FLAG in CIA
2. PA0 - PA0
3. PA1 - PA1
4. PA2 - PA2
5. PA3 - PA3
6. PA4 - PA4
7. PA5 - PA5
8. PA6 - PA6
9. PA7 - PA7
18. CB1 - 39. CA2 / PC in CIA
SP/CNT are open drain, need 3.3K pullup to VCC
U2B
74LS74
U5E
74LS126
U2C
74LS74
U3B
U4B
C1
10uF
Maciej Witkowiak <ytm@elysium.pl>
YTM Enterprises
Sheet: /
File: burstcart-via.kicad_sch
Title: BurstCart+4
Size: A4
Date: 2025-07-07
Rev: 1.1b
KiCad E.D.A. 9.0.0
Id: 1/1