

U2C
XC6SLX9-FTG256

C12	FPGA_TDI
F14	FPGA_TDO
TDO	FPGA_TMS
TMS	FPGA_TCK
TCK	FPGA_TCK
DONE_2	P13
PROGRAM_B_2	P12
SUSPEND	P14
CMPCS_B_2	L11

Xilinx requires these amounts for decoupling caps.
Vcc b2: 1x 100u, 1x 4.7u, 1x 0.47u
Vcc b3: 1x 100u, 1x 4.7u, 2x 0.47u

